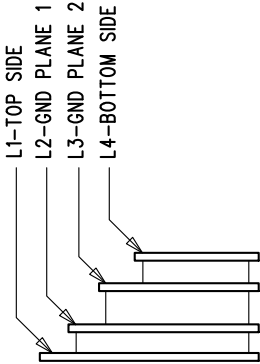


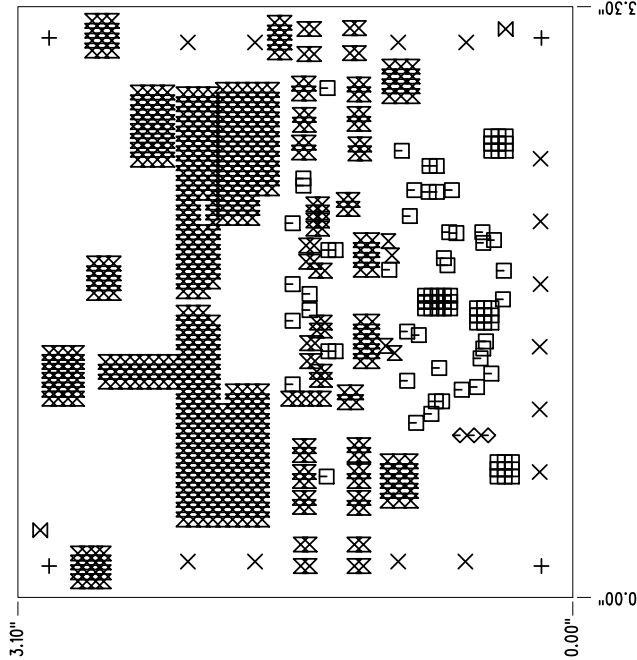
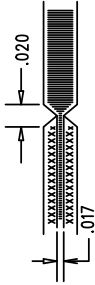
REVISION HISTORY			
ECO	REV	DESCRIPTION	APP. ENG. DATE
-	4	PRODUCTION	GEORGE Q. 10-29-13

LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

- 1. FAB PER IPC-A-600.
- 2. MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS
AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- 3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
- 4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003"
IN RELATION TO CENTER
-FOR VIAS SIZE > 0.01". VIAS HOLES NEED TO BE PLUGGED AND COVERED WITH SOLDERMASK.
-GOLD IMMERSION BOTH SIDES.
- 5. FINISH: -SNOBC USING LPI BOTH SIDES, COLOR GREEN.
(LEAD FREE SOLDER CAN BE USED FOR PROTOTYPE)
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
- 6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- 7. PCBs ARE TO BE RoHS COMPLIANT.
- 8. SCORING FOR PANELIZED PCB:



SIZE	QTY	SYM	PLATED	TOL
0.187	4	+	NO	+/-0.003
0.096	14	X	YES	+/-0.003
0.01	88	□	YES	+/-0.003
0.0315	3	◇	YES	+/-0.003
0.07	2	⊗	NO	+/-0.003
0.02	593	⊗	YES	+/-0.003

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES:
0.XX" = ±0.01"
0.XXX" = ±0.005"
INTERPRET DIM AND TOL
PER ASME Y14.5M-1994
THIRD ANGLE PROJECTION

APPROVALS

PCB DES.	JW
APP. ENG.	GEORGE Q.
TITLE: FABRICATION DRAWING	
SYNCHRONOUS INVERTING DC/DC CONVERTER	
SIZE	IC NO.
N/A	LT8710EFE
SCALE = NONE	DC2067A

LINEAR TECHNOLOGY

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FILENAME: DC2067A-4.PCB

SHT 1 OF 1