
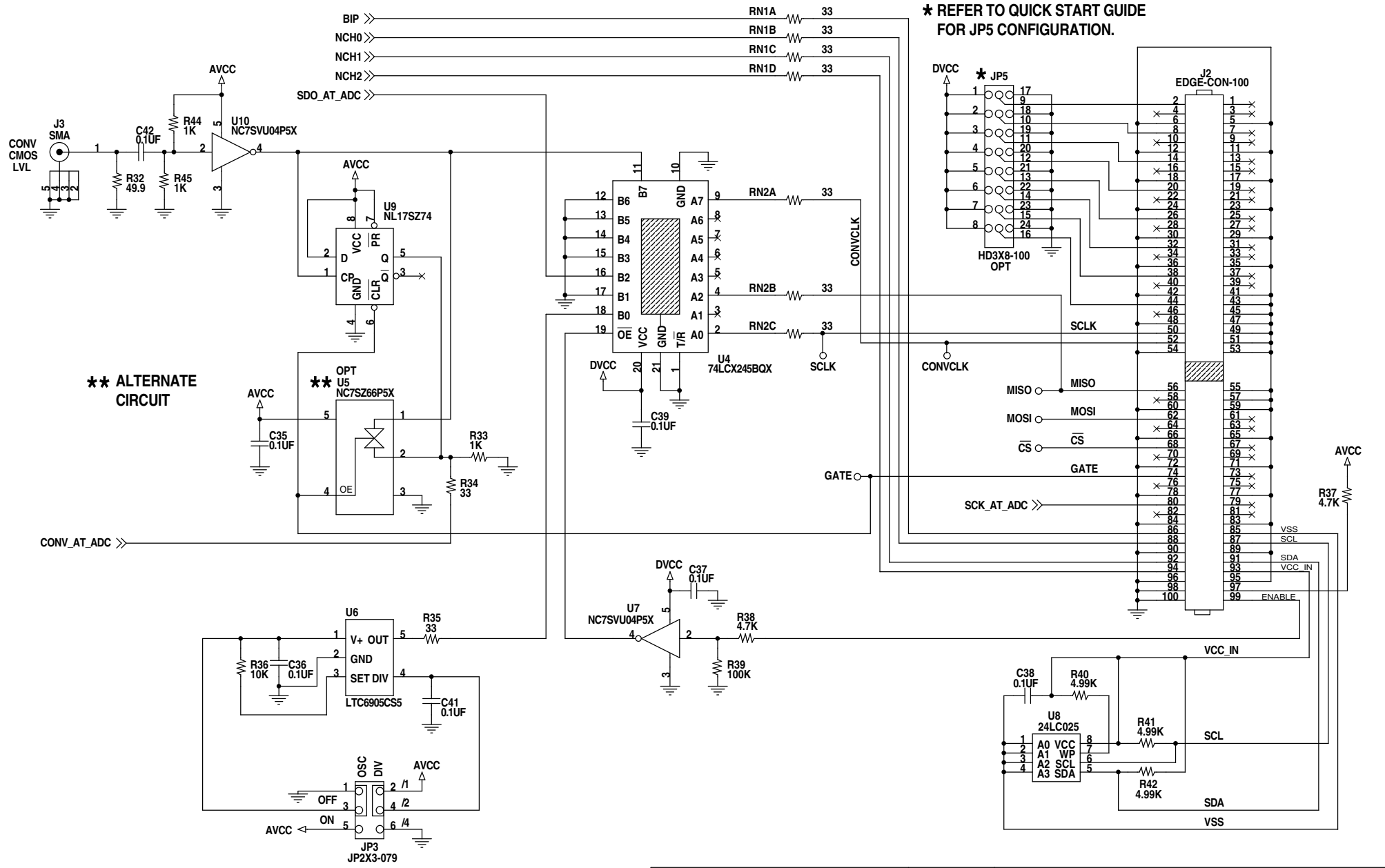


**NOTES: UNLESS OTHERWISE SPECIFIED**

1. ALL RESISTORS ARE IN OHMS, 0402.
2. INSTALL SHUNTS ON JP1-JP3 PIN 1&3, PIN 2&4.

CUSTOMER NOTICE		CONTRACT NO.	
LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.  THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.		APPROVALS	
		DRAWN: KIM T.	
		CHECKED:	
		APPROVED:	
		ENGINEER: MARK T.	
DESIGNER:		1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 Fax: (408)434-0507 LTC Confidential-For Customer Use Only	
TITLE: SCHEMATIC			
14-BIT, 6-CHANNEL, 600KSPS SIMULTANEOUS ADC		DATE: Friday, January 27, 2006	
SIZE <b>A</b>	DWG NO. <b>DC887A-1 * LTC1408CUH</b>	REV <b>A-1</b>	
SHEET 1 OF 2			



**\* REFER TO QUICK START GUIDE FOR JP5 CONFIGURATION.**

**\*\* ALTERNATE CIRCUIT**

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		APPROVED:	
<p>DESIGNER:</p>		ENGINEER: MARK T.	<p>SIZE: A</p> <p>DWG NO. <b>DC887A-1 * LTC1408CUH</b></p>
		DESIGNER:	
			SHEET 2 OF 2