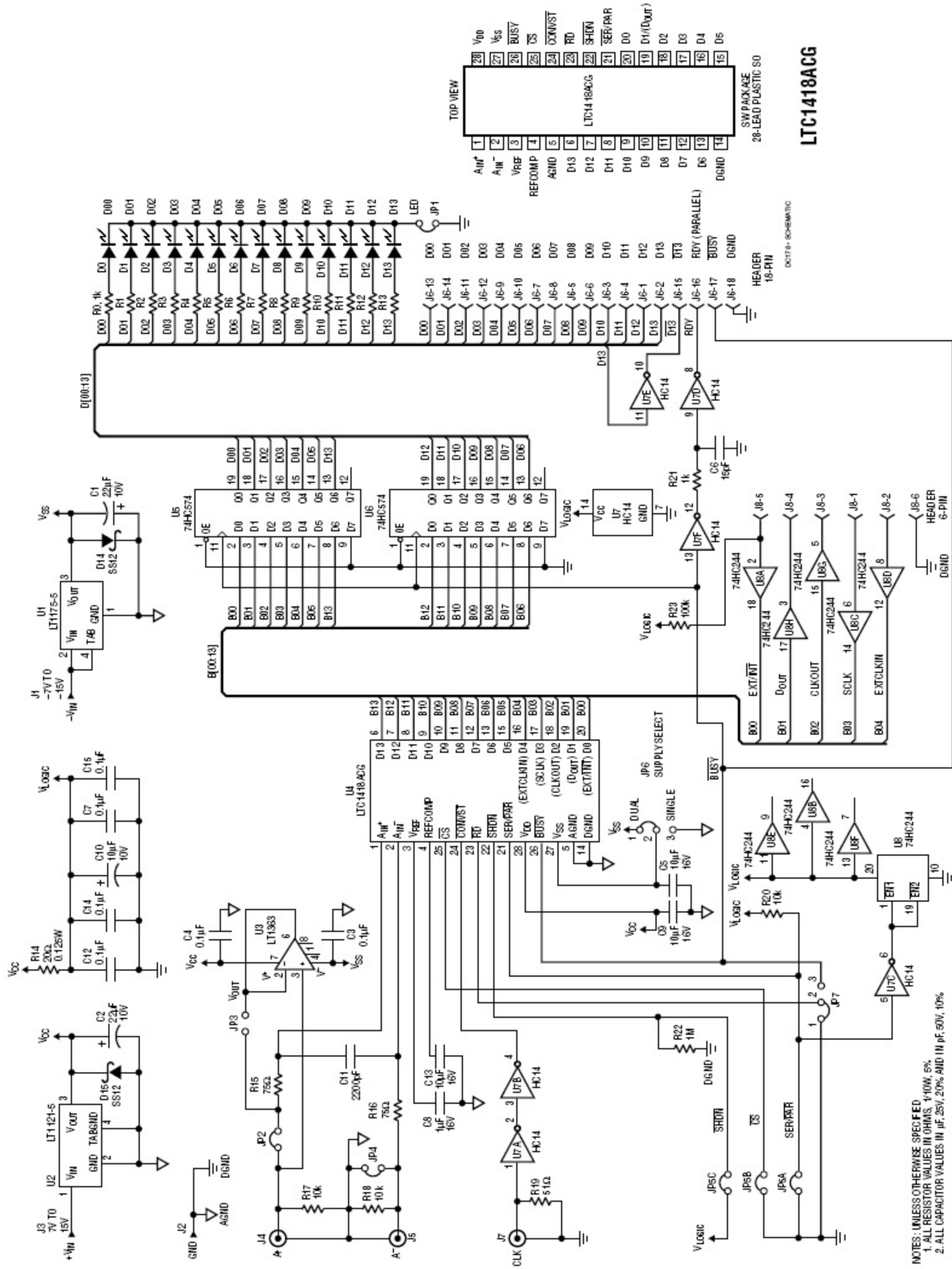


PACKAGE AND SCHEMATIC DIAGRAMS

LTC1418 Demonstration Board Features Analog Input Signal Buffer, 200ksps Parallel and Serial Data Output 14-Bit ADC, Data Latches and LED Binary Data Display. Latched Conversion Data is Available on the 18-Pin Header, J6



NOTES: UNLESS OTHERWISE SPECIFIED
 1. ALL RESISTOR VALUES IN OHMS, 1% 10W, 5%
 2. ALL CAPACITOR VALUES IN μ F, 25V, 20% AND IN pF, 50V, 10%