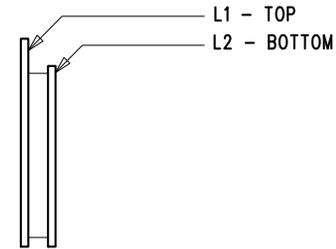
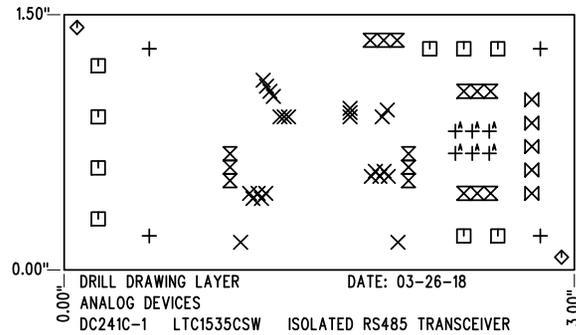


REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	1	PRODUCTION	ERIC B.	03-26-18

LAYER STRUCTURE



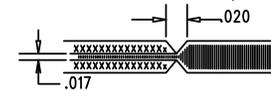
SHOWN FROM TOP SIDE



SIZE	QTY	SYM	PLATED	TOL
0.094	4	+	YES	+/-0.003"
0.012	24	X	YES	+/-0.003"
0.064	9	□	YES	+/-0.003"
0.07	2	◇	NO	+/-0.003"
0.035	15	⊗	YES	+/-0.003"
0.045	5	⊗	YES	+/-0.003"
0.04	6	+ ^A	YES	+/-0.003"

NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 2 LAYERS WITH 2 OZ. CU ON EACH.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



UNLESS OTHERWISE SPECIFIED		APPROVALS		ANALOG DEVICES	
DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX" = ±0.01" 0.XXX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION		PCB DES.	KIM T.	1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 WWW.ONLOG.COM FOR CUSTOMER USE ONLY	
		APP ENG.	ERIC B.	TITLE: FABRICATION DRAWING	
				ISOLATED RS485 TRANSCEIVER	
				SIZE	IC NO.
				N/A	LTC1535CSW
				DEMO CIRCUIT 241C	
		SCALE = NONE		REV	1
				FILENAME:	DC241C-1.PCB
				SHT	1 OF 1