


**ANALOG GROUND PLANE**

**NOTES: UNLESS OTHERWISE SPECIFIED**

1. ALL RESISTORS ARE IN OHMS.
2. INSTALL SHUNTS ON JP1, JP3-JP7 PIN 1 AND 2; ON JP2 PIN 2 AND 3; ON JP8 PIN 1 AND 3, PIN 2 AND 4; ON JP9 PIN 1 AND 3, PIN 4 AND 6.

CONTRACT NO.		 <b>LINEAR TECHNOLOGY</b>	1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 Fax: (408)434-0507 LTC Confidential-For Customer Use Only	
APPROVALS			TITLE: <b>3V, 16-BIT 150KSPS ADC</b>	
DRAWN: KIM T.		ENGINEER: GUYH.	DESIGNER:	REV <b>A</b>
CHECKED:		DATE: Tuesday, June 25, 2002		SHEET 1 OF 1
APPROVED:		SIZE <b>A</b>		DWG NO. <b>DC547A-LTC1864LAIMS8</b>