

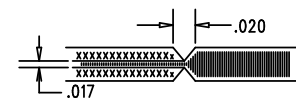
SHOWN FROM COMPONENT SIDE


REVISIONS			
REV	DESCRIPTION	APPR	DATE
A-1	2ND PROTOTYPE	GP	08-22-07

SIZE	QTY	SYM	PLATED	TOL
0.025	57	+	YES	+/-0.003"
0.07	2	X	NO	+/-0.003"
0.188	4	□	YES	+/-0.003"
0.035	9	◇	YES	+/-0.003"
0.094	4	⊗	YES	+/-0.003"
0.015	14	⊗	YES	+/-0.003"

#### NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600
- MATERIAL: EPOXY FIBERGLASS, NEMA GRADE FR-4  
FINISHED THICKNESS TO BE 0.062" +/- .005"  
TOTAL OF 6 LAYERS WITH 2 OZ. CU ON THE  
OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.  
FLAMABILITY RATING: 94 V-0 MINIMUM .
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
0.00" ARE PRIMARY DATUMS.
- DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH  
HOLES WITH COPPER, 0.001" THICK MIN.  
ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
HOLE LOCATION TOLERANCES ARE +/-0.003"  
IN RELATION TO CENTER
- FINISH: SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
GOLD IMMERSION. USE LEAD FREE SOLDER FOR PROTOTYPE.  
FOR SILKSCREEN: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.  
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



APPROVALS			 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY	
	INIT	DATE		
DRAWN			TITLE: FABRICATION DRAWING HIGH EFFICIENCY SEPIC	
CHECK				
DESIGN	KIM T.	08-22-07	SIZE A DEMO DC1225A-1 * LTC1871EMS-1 REV. A-1	
ENGR	GORAN P.	08-22-07		
SCALE = NONE			SHT 1 of 1	