



NOTES: UNLESS OTHERWISE SPECIFIED

- ALL RESISTORS ARE IN OHMS, 0402.
ALL CAPACITORS ARE IN MICROFARADS, 00402.
- INSTALL SHUNT ON JP1 & JP2 PIN 3 AND 4.
- THERE ARE 2 ASSEMBLY VERSIONS:

VERSION	U2	C6	JP3
-A	LTC6101ACMS8	OPT	OPT
-B	LTC6102CMS8	STUFFED	STUFFED

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

CONTRACT NO.

APPROVALS

DRAWN:

CHECKED:

APPROVED:

ENGINEER: JON M.

DESIGNER: KIM T.

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LINEAR TECHNOLOGY

TITLE: SCHEMATIC

CURRENT SENSE TECHNIQUES

SIZE A DWG NO. **DC907A-1** REV A

DATE: Monday, September 12, 2005 SHEET 1 OF 1