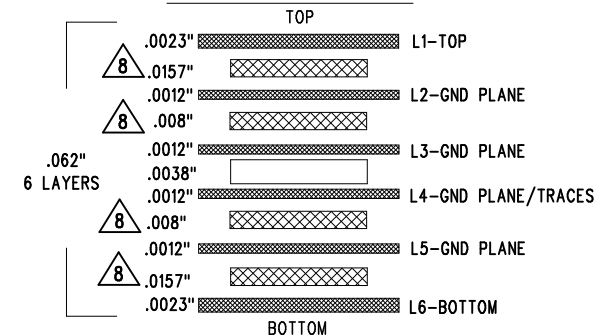


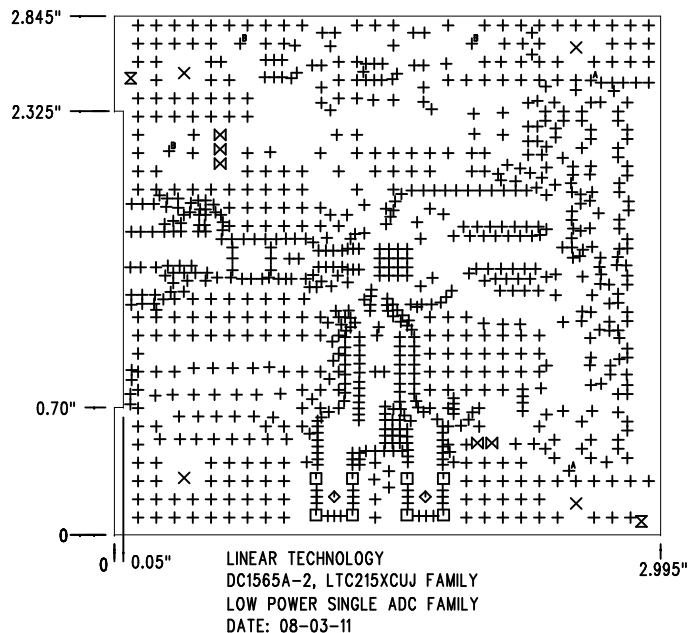
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	2	PRODUCTION	CLARENCE M.	08-03-11

## LAYER STRUCTURE



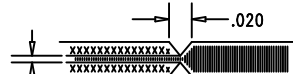
## NOTES : UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600. PCBs ARE TO BE ROHS COMPLIANT.
- MATERIAL: EPOXY FIBERGLASS, NEMA GRADE FR-4.  
2 OZ. COPPER FINISH ON OUTER LAYERS.  
1 OZ. COPPER FINISH ON INNER LAYERS.  
THICKNESS .062 +/- .006 TOTAL OF 6 LAYERS.  
FLAMMABILITY RATING: 94 V-0 MINIMUM .
- SIZE: DIMENSIONS AND TOLERANCES SHOWN.  
0.00 ARE PRIMARY DATUMS.
- DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, .001 INCH THICK MIN. ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
HOLE LOCATION TOLERANCES ARE +/- .003 INCH IN RELATION TO CENTER
- FINISH: SMOBC USING LPI BOTH SIDES COLOR GREEN  
GOLD IMMERSION
- SILKSCREEN : USING WHITE NON-CONDUCTIVE EPOXY INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE BUT YOU MAY MODIFY PAD SIZE TO MEET END FINISH.
- OUTER DIELECTRIC THICKNESS: TARGET 50-OHM +/- 5% USING 30 MIL TRACE  
TARGET 100-OHM DIFFERENTIAL PAIR USING 8 MIL TRACE, 6 MIL GAP  
TRACE CONTROLLED 50 OHM IMPEDANCE LAYER 1 TO LAYER 3.  
AND TRACE CONTROLLED 100 OHM +/- 5% DIFFERENTIAL PAIR IMPEDANCE LAYER 1 TO LAYER 2. (TO BE FINALIZED BY MANUFACTURER)
- SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTRIC CONSTANT DEVIATIONS. PLEASE CONSULT LTC.



SIZE	QTY	SYM	PLATED	TOL
0.01	869	+	YES	+/-0.003
0.187	4	X	YES	+/-0.003
0.065	8	□	YES	+/-0.003
0.06	2	◇	YES	+/-0.003
0.07	2	⊗	NO	+/-0.003
0.035	5	⊗	YES	+/-0.003
0.05	2	⊕ <sup>A</sup>	NO	+/-0.003
0.095	3	⊕ <sup>B</sup>	YES	+/-0.003

10. SCORING FOR PANELIZED PCB:



UNLESS OTHERWISE SPECIFIED		APPROVALS		<p>1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY</p>	
DIMENSIONS ARE IN INCHES		PCB DES.	AK		
TOLERANCES:		APP ENG.	CLARENCE M.	TITLE: FABRICATION DRAWING	
0.XX" = ±0.01"				LOW POWER SINGLE ADC FAMILY	
0.XXX" = ±0.005"					
INTERPRET DIM AND TOL PER ASME Y14.5M-1994				SIZE	IC NO. LTC215XCUJ FAMILY
THIRD ANGLE PROJECTION				N/A	DEMO CIRCUIT 1565A
				SCALE = NONE	REV 2
				FILENAME: DC1565A-2.PCB	SHT 1 OF 1