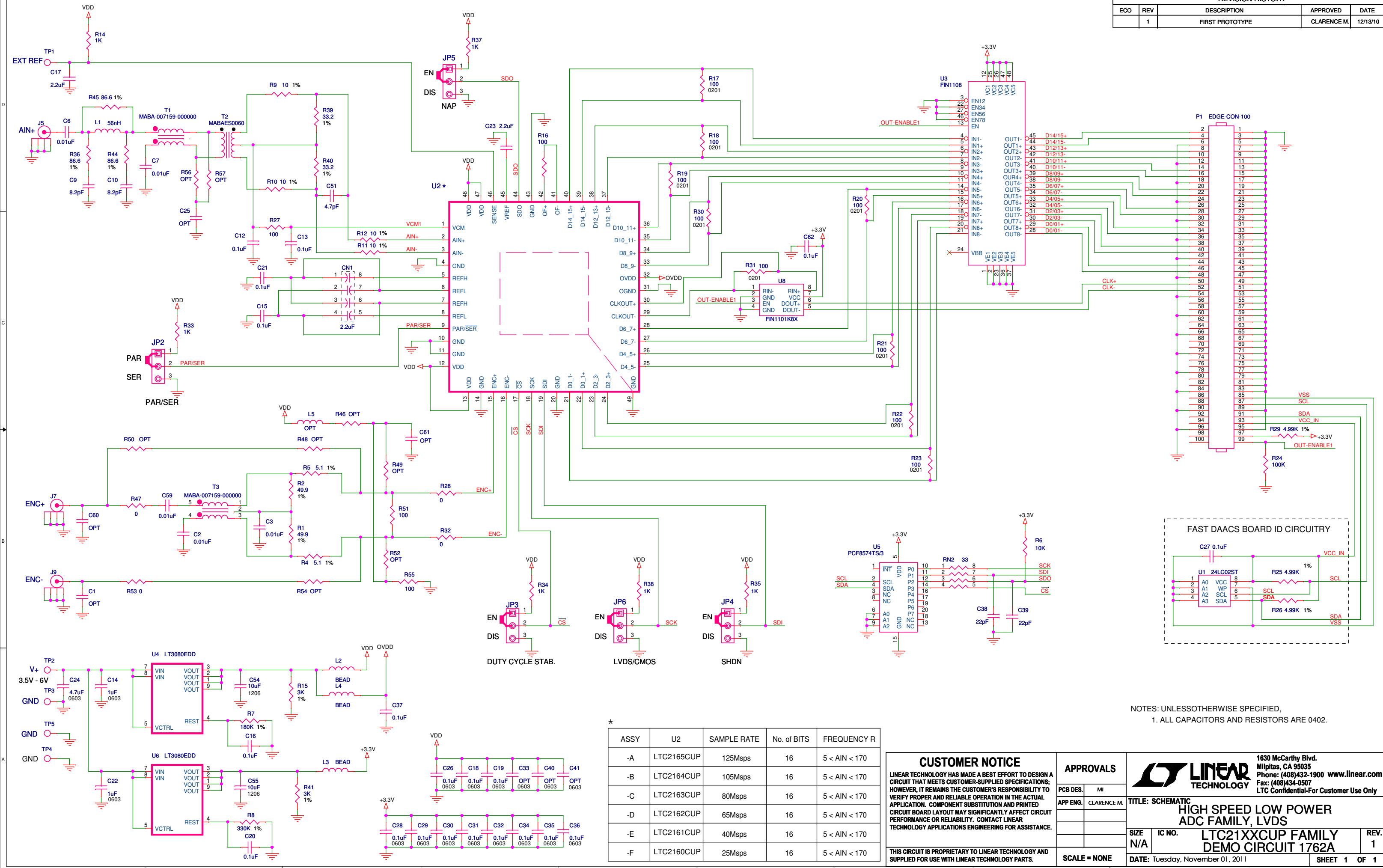


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	1	FIRST PROTOTYPE	CLARENCE M.	12/13/10



NOTES: UNLESS OTHERWISE SPECIFIED,
1. ALL CAPACITORS AND RESISTORS ARE 0402.

ASSY	U2	SAMPLE RATE	No. of BITS	FREQUENCY R
-A	LTC2165CUP	125Msps	16	5 < AIN < 170
-B	LTC2164CUP	105Msps	16	5 < AIN < 170
-C	LTC2163CUP	80Msps	16	5 < AIN < 170
-D	LTC2162CUP	65Msps	16	5 < AIN < 170
-E	LTC2161CUP	40Msps	16	5 < AIN < 170
-F	LTC2160CUP	25Msps	16	5 < AIN < 170

CUSTOMER NOTICE
LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

APPROVALS
PCB DES. MI
APP ENG. CLARENCE M.

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LINEAR TECHNOLOGY

TITLE: SCHEMATIC
HIGH SPEED LOW POWER ADC FAMILY, LVDS

SIZE N/A IC NO. **LTC21XXCUP FAMILY DEMO CIRCUIT 1762A** REV. 1

DATE: Tuesday, November 01, 2011 SHEET 1 OF 1

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

SCALE = NONE