


UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES ON ANGLE ±1
2 PLACE ±.01 3 PLACE ±.005
INTERPRET DIM AND TOL
PER ASME Y14.5M - 1994

APPROVALS						 1630 McCarthy Blvd. Milpitas, CA 95035 PH: (408)432-1900	
DRAWN	INIT	DATE					
CHECK							
DESIGN	JUNE WU	4/12/05					
ENGR	Derek R.	4/12/05					
TITLE:			LTC2203 FAMILY				
			16 BIT HIGH SPEED ADC				
SIZE	A	DEMO	DC919A		REV.	A	
SCALE = NONE			DES-250		SHT 1 of 1		

TOP	
.0028" (LYR1)	Microstrip
.005"	Core Material
.0014" (LYR2)	Microstrip
.046"	Prepreg
.0014" (LYR3)	Microstrip
.005"	Core Material
.0028" (LYR4)	Microstrip

NOTES : Unless Otherwise Specified

1. FAB PER IPC-A-600.
2. MATERIAL: EPOXY FIBERGLASS, NEMA GRADE FR-4.
2 OZ. COPPER FINISHED ON OUTER LAYERS.
1 OZ. COPPER FINISHED ON INNER LAYERS.
THICKNESS .062 +/--.006 TOTAL OF 4 LAYERS.
FLAMABILITY RATING: 94 V-0 MINIMUM .
3. SIZE: DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
4. DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH
HOLES WITH COPPER, .001 INCH THICK MIN. ALL
HOLE SIZES ARE SPECIFIED AFTER PLATING.
HOLE LOCATION TOLERANCES ARE +/--.003
INCH IN RELATION TO CENTER
5. FINISH: SMOBC USING LPI BOTH SIDES COLOR GREEN.
SILVER IMMERSION BOTH SIDES.

6. SILKSCREEN : USING WHITE NON-CONDUCTIVE EPOXY INK.
7. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
8. OUTER DIELECTRIC THICKNESS: TARGET 50- μ M USING 8 MIL TRACE CONTROLLED 50 OHM IMPEDANCE LAYER 1 TO LAYER 2. (TO BE FINALIZED BY MANUFACTURER)
9. SUBJECT TO CHANGE BY MANUFACTURER, DEPENDING ON DIELECTRIC CONSTANT DEVIATIONS. PLEASE CONSULT LTC.