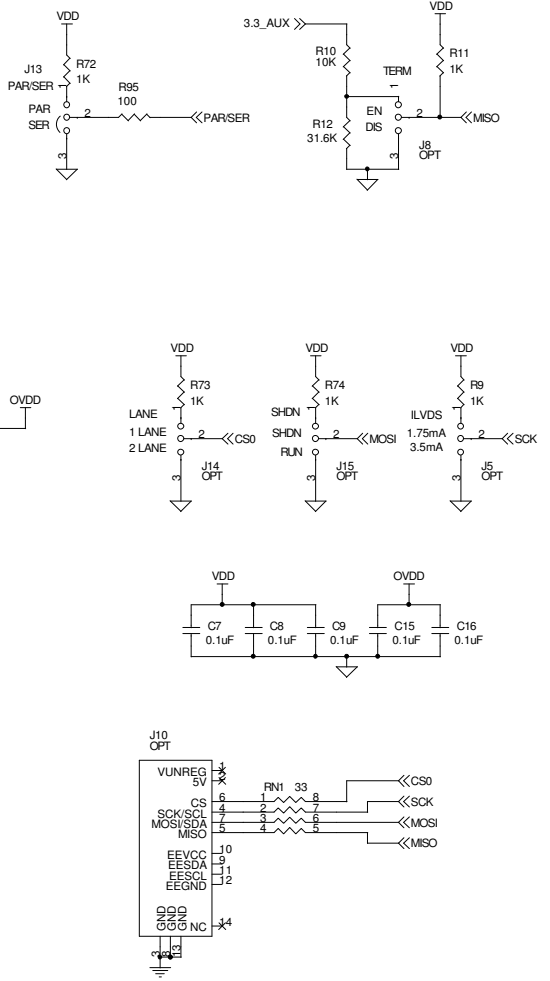
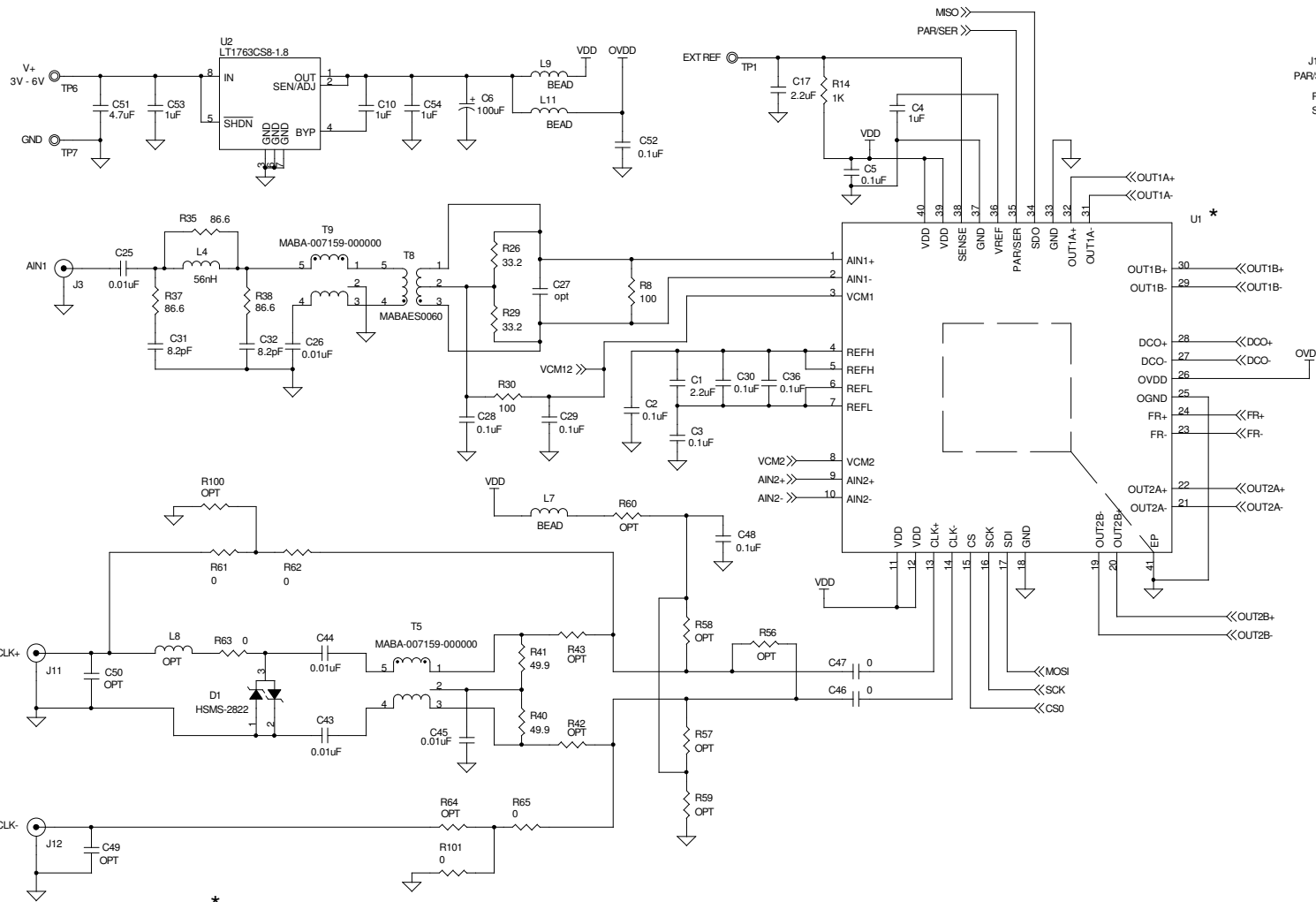



REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	2	PRODUCTION	CLARENCE	01-08-10

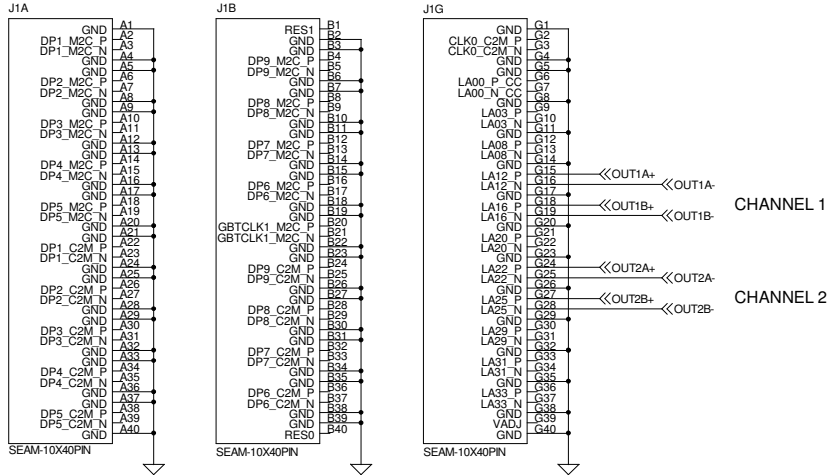
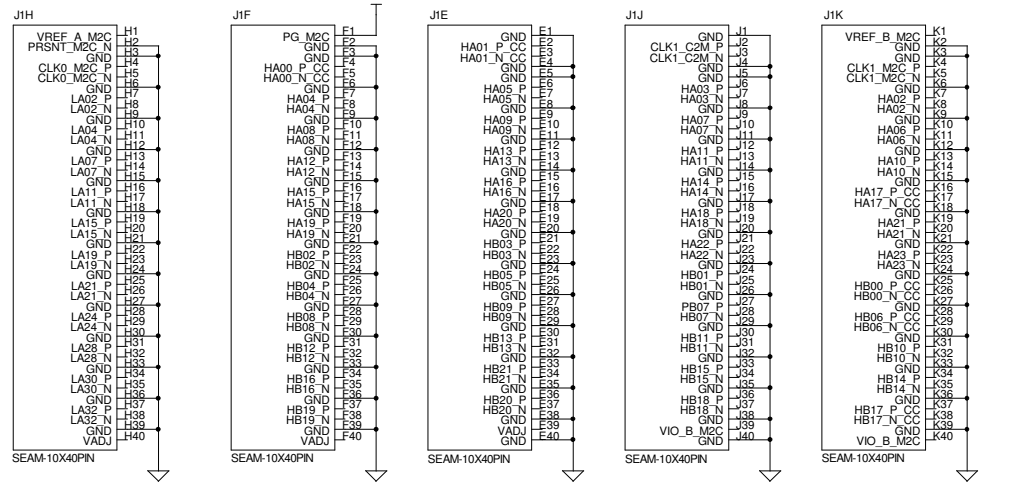
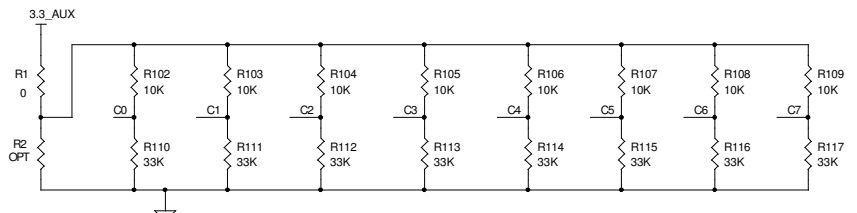
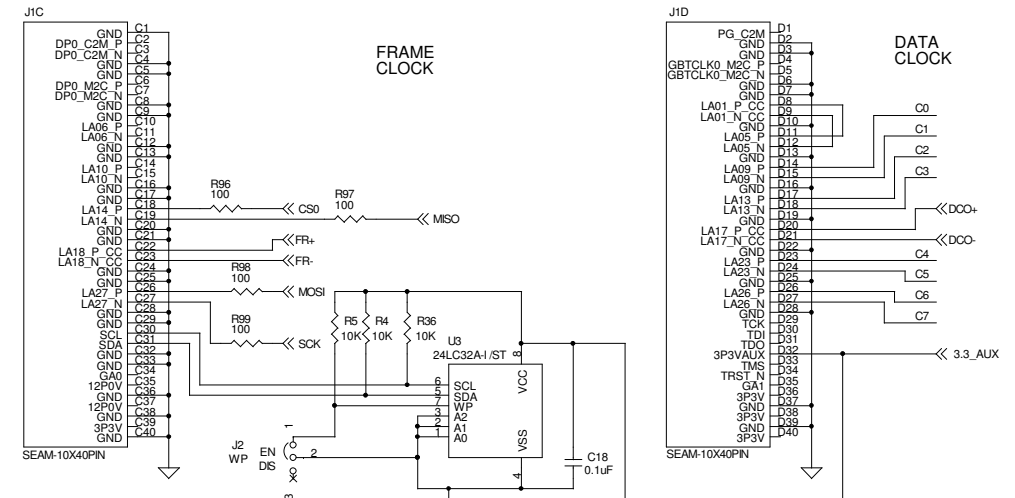
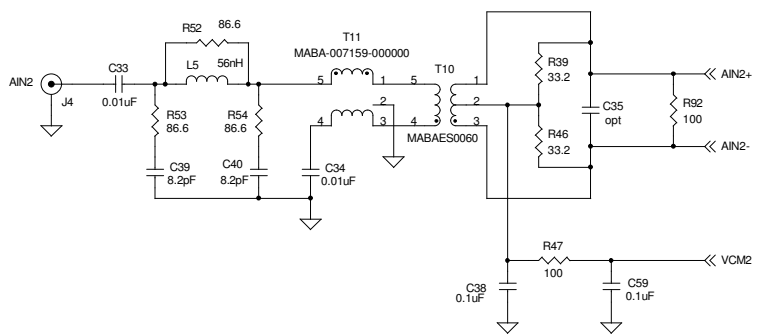


\* ASSY TABLE

ASSY	U1	Bits	MspS	ASSY	U1	Bits	MspS
DC1532A-A	LTC2268IUJ-14	14	125	DC1532A-G	LTC2268IUJ-12	12	125
DC1532A-B	LTC2267IUJ-14	14	105	DC1532A-H	LTC2267IUJ-12	12	105
DC1532A-C	LTC2266IUJ-14	14	80	DC1532A-I	LTC2266IUJ-12	12	80
DC1532A-D	LTC2265IUJ-14	14	65	DC1532A-J	LTC2265IUJ-12	12	65
DC1532A-E	LTC2264IUJ-14	14	40	DC1532A-K	LTC2264IUJ-12	12	40
DC1532A-F	LTC2263IUJ-14	14	25	DC1532A-L	LTC2263IUJ-12	12	25

**CUSTOMER NOTICE**  
 LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.  
 THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

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APPROVALS			
PCB DES. A.K.		TITLE: SCHEMATIC	
ENG. CLARENCE M.		<b>DUAL ADC FAMILY</b> <b>LTC226XIUJ FAMILY</b> <b>DEMO CIRCUIT 1532A</b>	
SIZE			
DATE: Tuesday, January 19, 2010		SHEET 1 OF 2	



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THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.		<b>TITLE: SCHEMATIC</b>		<b>DUAL ADC FAMILY</b>	
		<b>SIZE</b> N/A		<b>IC NO.</b> LTC226XIUJ FAMILY	
		<b>DATE:</b> Tuesday, January 19, 2010		<b>REV</b> 2	
		<b>SHEET</b> 2 OF 2		<b>DEMO CIRCUIT 1532A</b>	