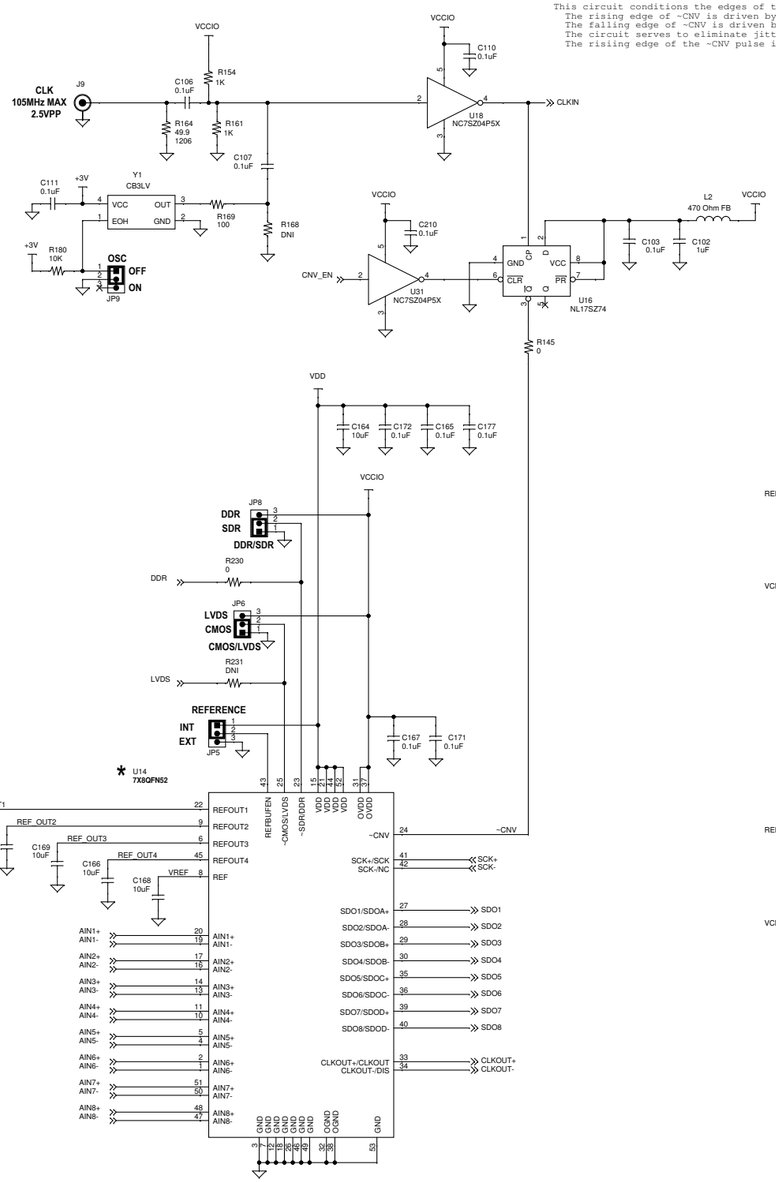
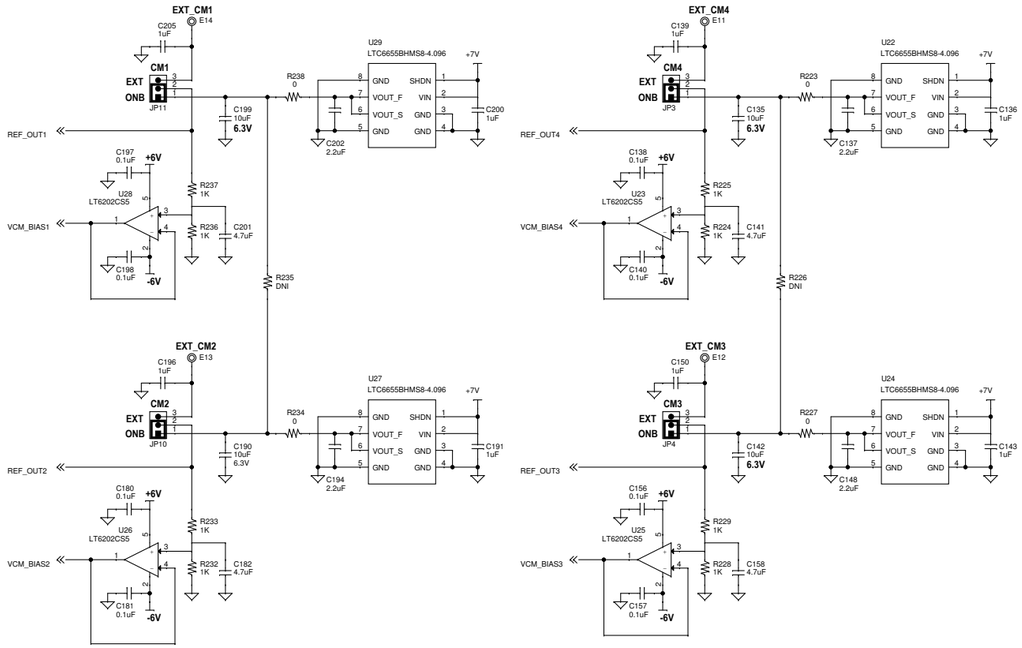
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	0	PRODUCTION	DOUG S.	08-26-13

```
This circuit conditions the edges of the ~CNV pulse as follows:
  The rising edge of ~CNV is driven by a rising edge of CNV_EN.
  The falling edge of ~CNV is driven by a rising edge of CLKIN, so it is wider than 'N' clock cycles.
  The circuit serves to eliminate jitter on the ~CNV pulse due to CPLD jitter.
  The risiing edge of the ~CNV pulse is thus driven by CNV_EN, not CLKIN.
```





*

ASSY	U1	BITS	Msps	R136	R135	R137	R138
-A	LTC2320-16	16	1.5	1K	DNI	1K	DNI
-B	LTC2324-16	16	2				
-C	LTC2325-16	16	5				
-D	LTC2320-14	14	1.5	1K	DNI	DNI	1K
-E	LTC2324-14	14	2				
-F	LTC2325-14	14	5				
-G	LTC2320-12	12	1.5	DNI	1K	1K	DNI
-H	LTC2324-12	12	2				
-I	LTC2325-12	12	5				

NOTES: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS ARE IN OHMS, 0603.
   ALL CAPACITORS ARE IN MICROFARADS, 0603

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS	
PCB DES.	D. STUETZLE
APP ENG.	D. STUETZLE

SCALE = NONE

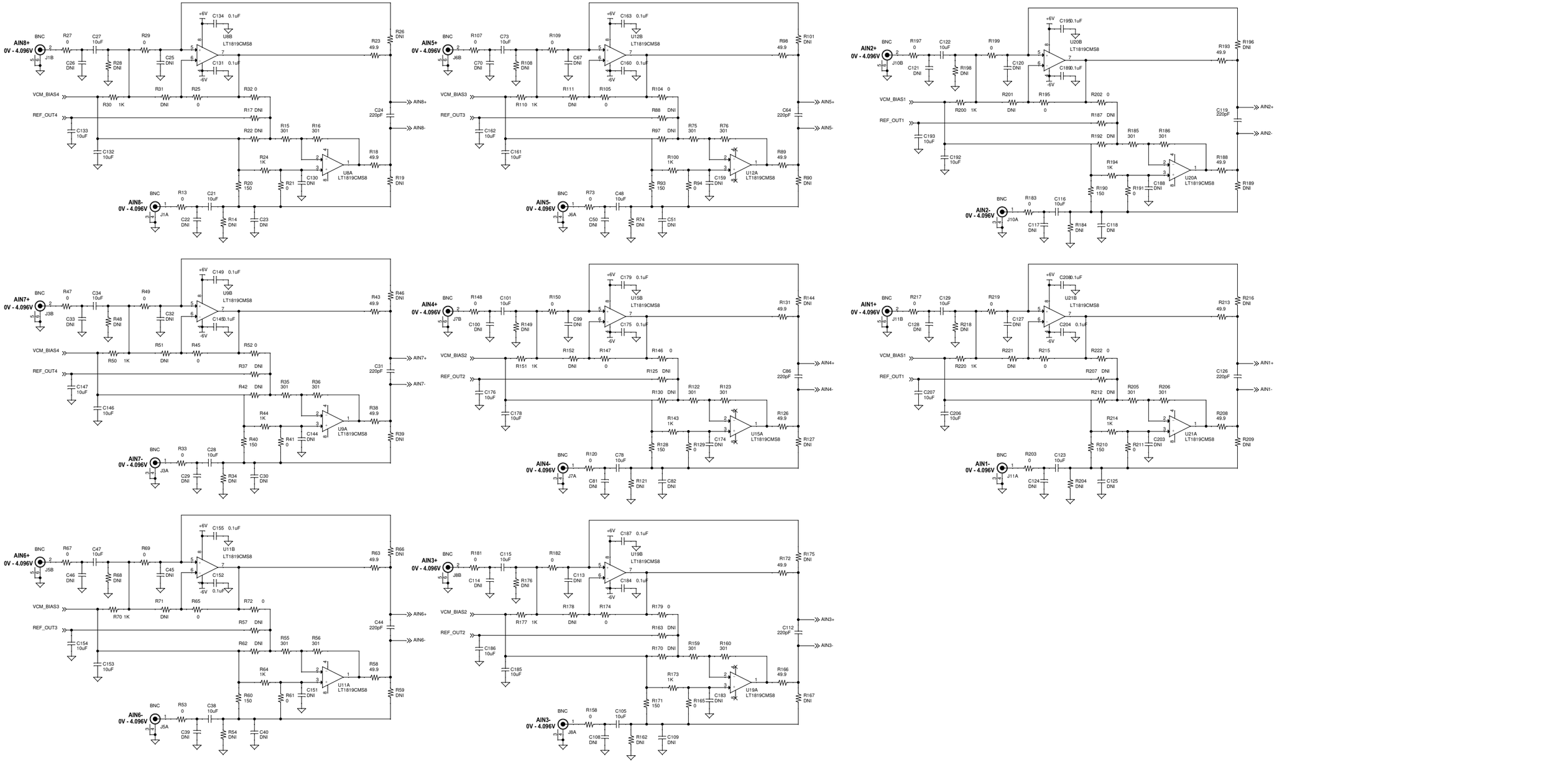
LINEAR TECHNOLOGY
1630 McCarthy Blvd.
Milpitas, CA 95035
Phone: (408)432-1900 www.linear.com
Fax: (408)434-0507
LTC Confidential-For Customer Use Only

TITLE: SCHEMATIC

TRUE DIFFERENTIAL INPUT OCTAL ADC

SIZE	IC NO.	REV.
B	LTC2325-16 FAMILY DEMO CIRCUIT 2395A	1

DATE: Wednesday, June 29, 2016 SHEET 1 OF 3



NOTES: UNLESS OTHERWISE SPECIFIED

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Input Configuration	ADC Input Configuration	C21,27,28,34, 38,47,48,73, 78,101,105,115, 116,122,123,129	R32,52,72,104, 146,179,202,222	R17,37,57,88, 125,163,187,207	R15,35,55,75, 122,159,185,205	R16,36,56,76, 123,160,186,206	R24,44,64,100, 143,173,194,214
Differential	Differential AC coupled	10uF	0	DNI	DNI	0	DNI
Differential	Differential DC coupled	0	0	DNI	DNI	0	DNI
Single-ended	Differential AC coupled	10uF	0	DNI	301	301	DNI
Single-ended	Differential DC coupled	0	0	DNI	301	301	DNI
Single-ended	Bipolar	10uF	0	DNI	DNI	0	0
Single-ended	Unipolar	10uF	DNI	0	301	301	0

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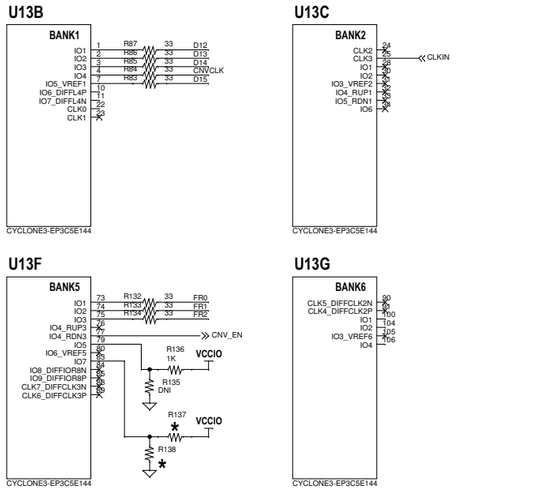
TITLE: SCHEMATIC

TRUE DIFFERENTIAL INPUT OCTAL ADC

SIZE	IC NO.	REV.
B	LTC2325-16 FAMILY DEMO CIRCUIT 2395A	1

DATE: Tuesday, June 28, 2016



NOTES: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS ARE IN OHMS, 0603.
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APPROVALS	
PCB DES.	D. STUETZLE
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TITLE: SCHEMATIC

TRUE DIFFERENTIAL INPUT OCTAL ADC

SIZE	IC NO.	REV.
B	LTC2325-16 FAMILY DEMO CIRCUIT 2395A	1

DATE: Wednesday, February 24, 2016 SHEET 3 OF 3