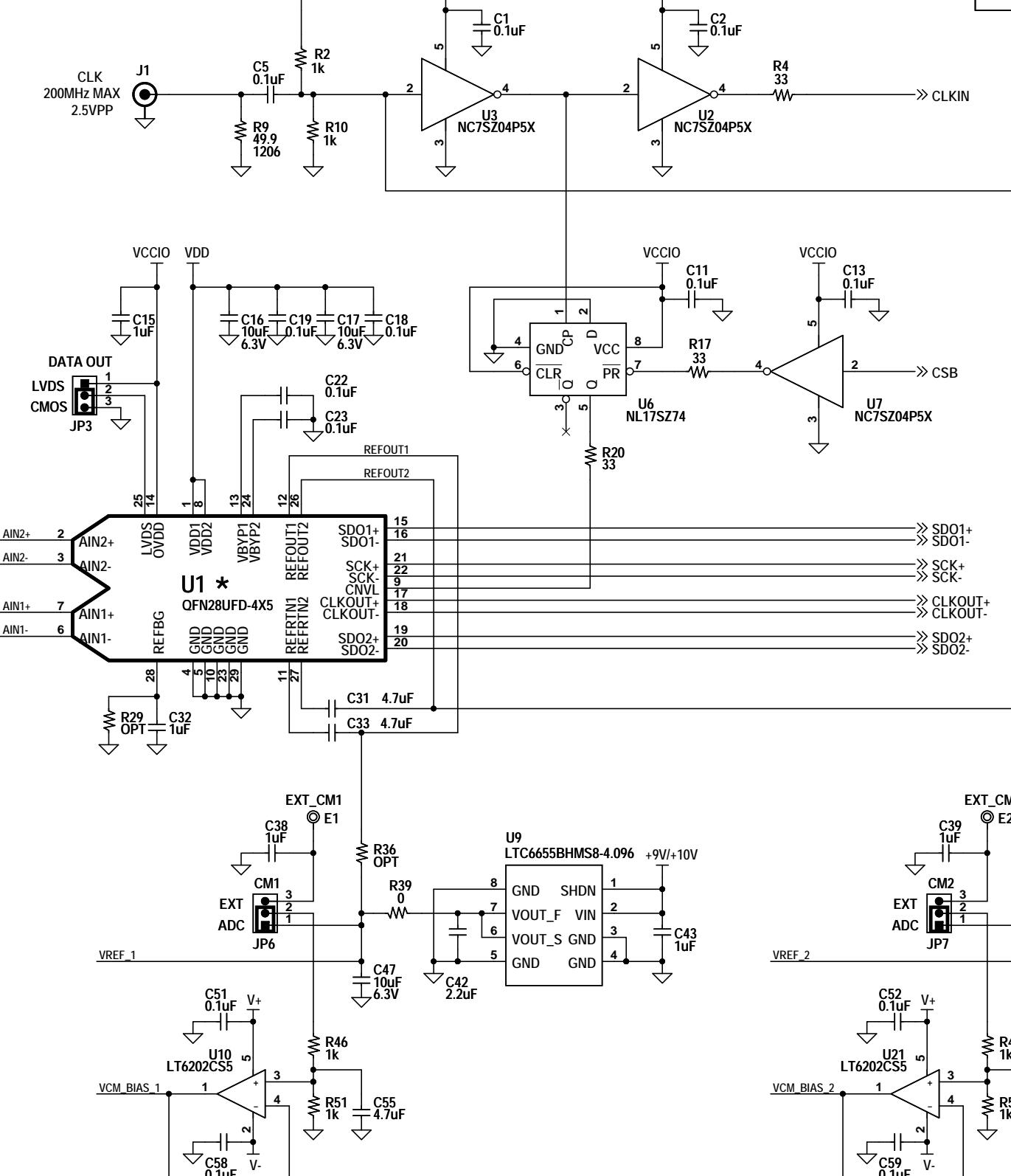
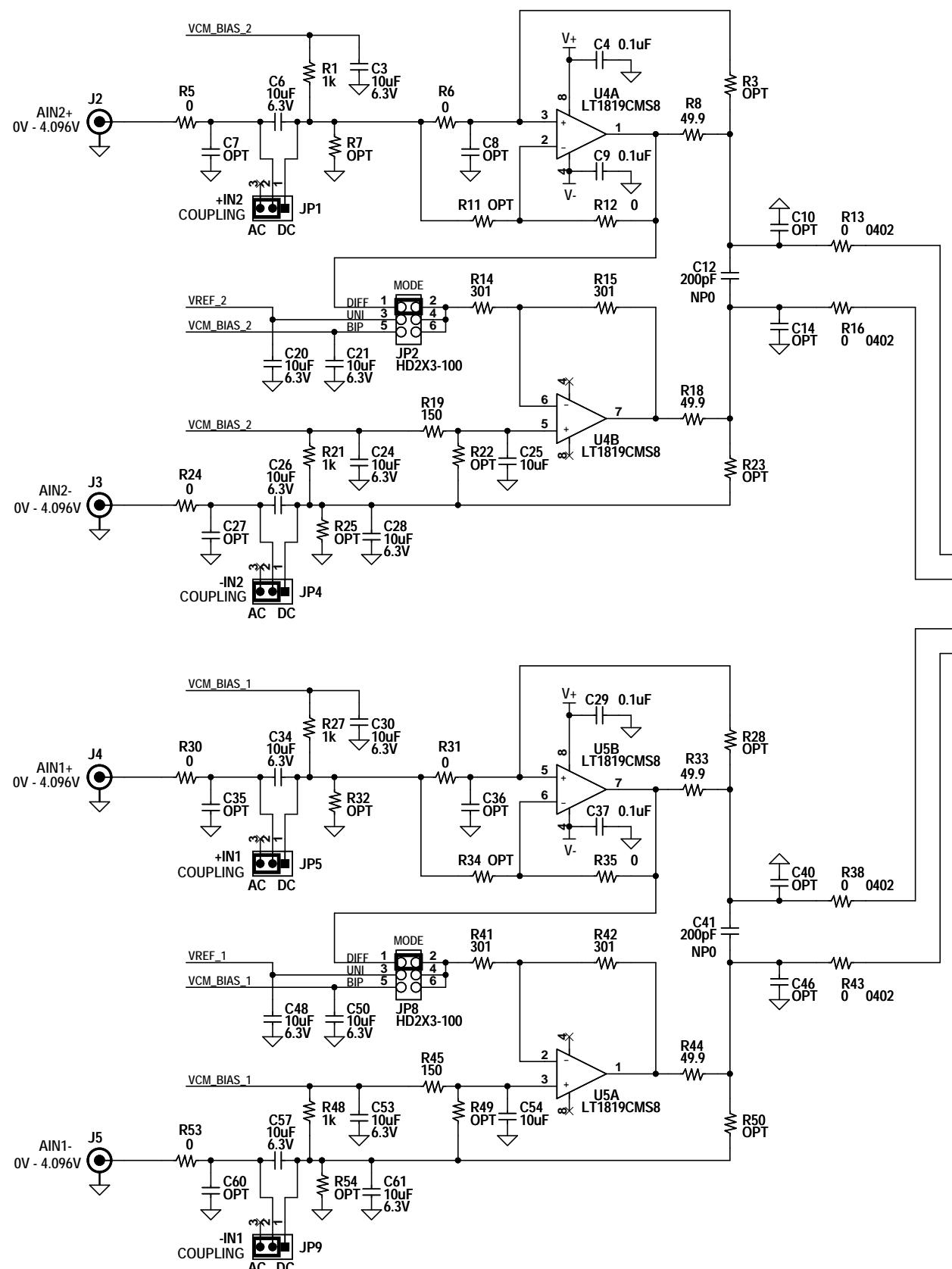


ECO	REV	DESCRIPTION	APPROVED	DATE
	2	PRODUCTION	DOUG S.	04-01-14



ADD 20 MIL SQUARE
PAD OPENING ON
TOP SOLDER MASK
LAYER ON THESE
CONNECTIONS.

*	ASSY	U1	BITS	MspS	R67	R68	R73	R74
A	LTC2323CUFD-16	16	5		1k	OPT		
B	LTC2321CUFD-16	16	2					
C	LTC2323CUFD-14	14	5		OPT	1k		
D	LTC2321CUFD-14	14	2					
E	LTC2323CUFD-12	12	5		1k	OPT	OPT	1k
F	LTC2321CUFD-12	12	2					

NOTES: UNLESS OTHERWISE SPECIFIED

- ALL RESISTORS ARE IN OHMS, 0603.
- ALL CAPACITORS ARE IN MICROFARADS, 0603

CUSTOMER NOTICE

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APPROVALS

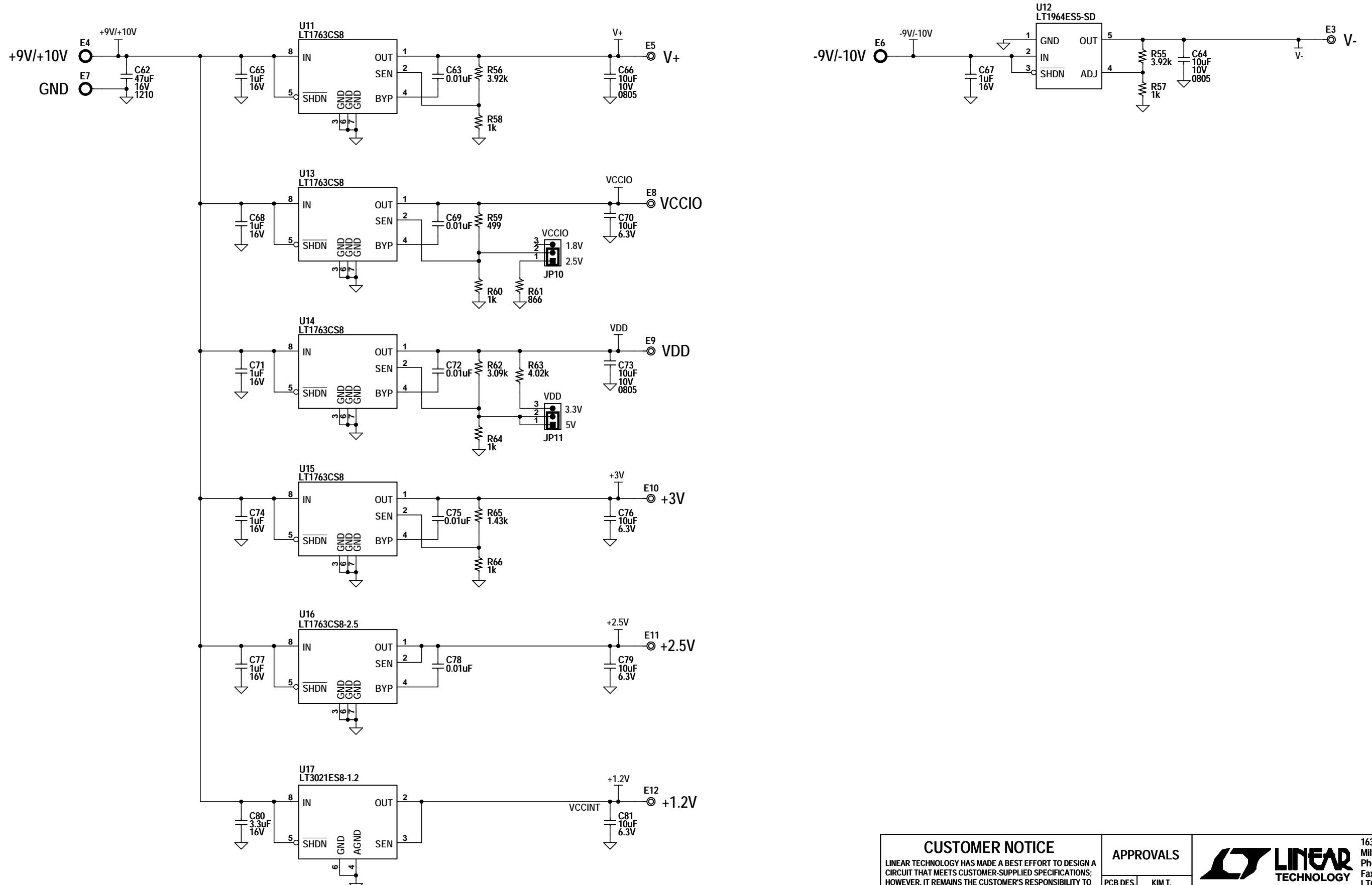
PCB DES.	KIM T.
APP ENG.	DOUG S.

TITLE: SCHEMATIC			
SIZE	IC NO.	LTC232XCUD FAMILY DEMO CIRCUIT 1996A	REV.
B			2

1630 McCarthy Blvd.
Milpitas, CA 95035
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DATE: 04/01/2014, 12:58 PM

SHEET 1 OF 3



CUSTOMER NOTICE

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APPROVALS

PCB DES.	KIM T.
APP ENG.	DOUG S.

LINEAR
TECHNOLOGY

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TITLE: SCHEMATIC

TRUE DIFFERENTIAL INPUT DUAL ADC

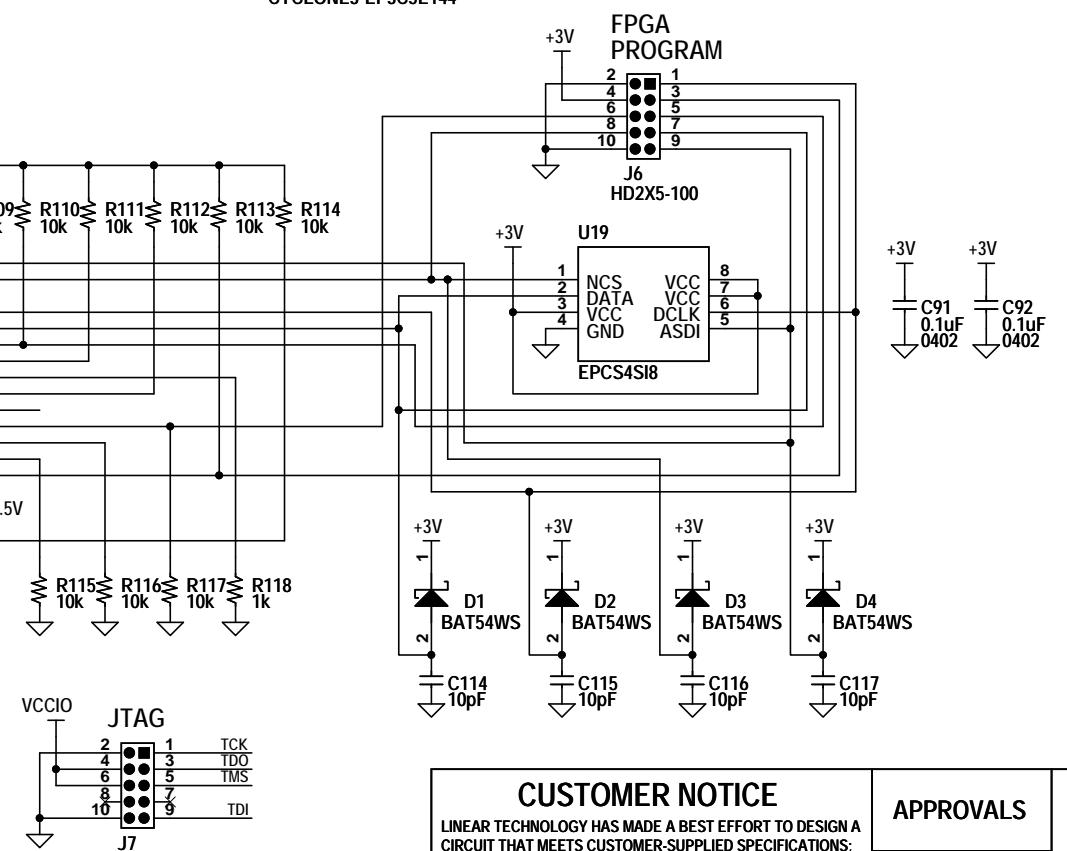
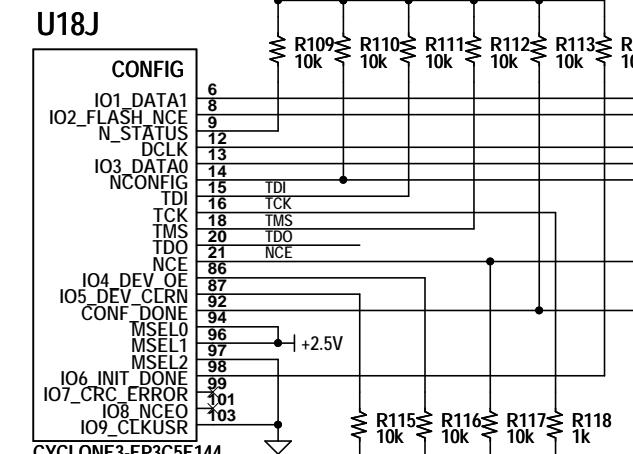
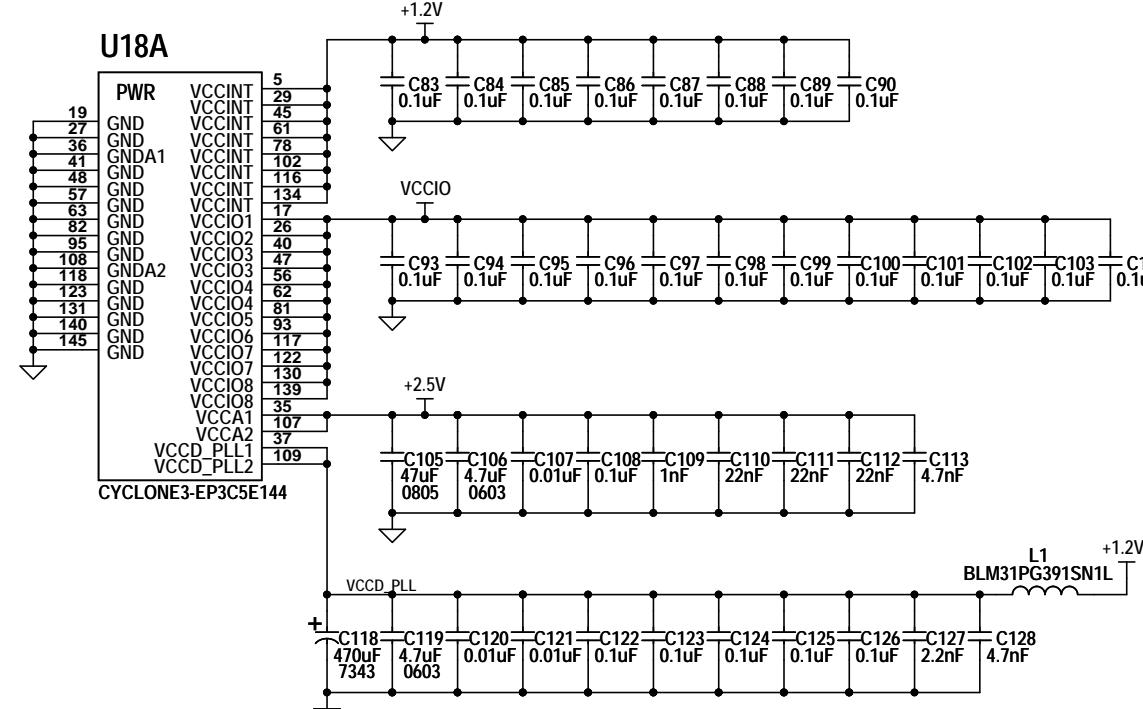
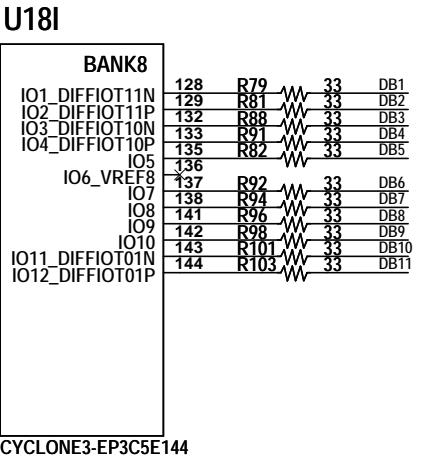
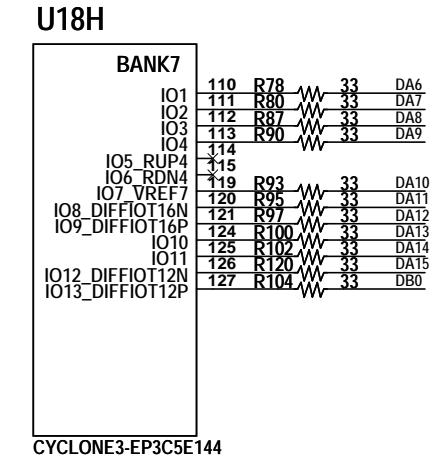
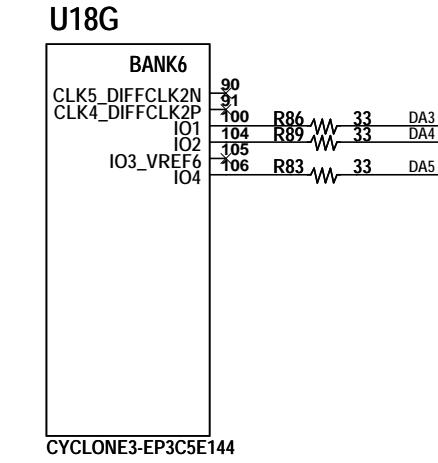
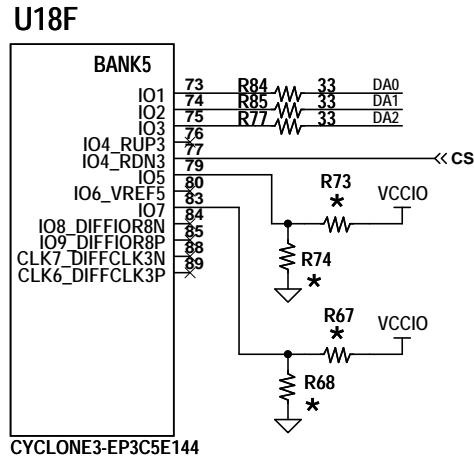
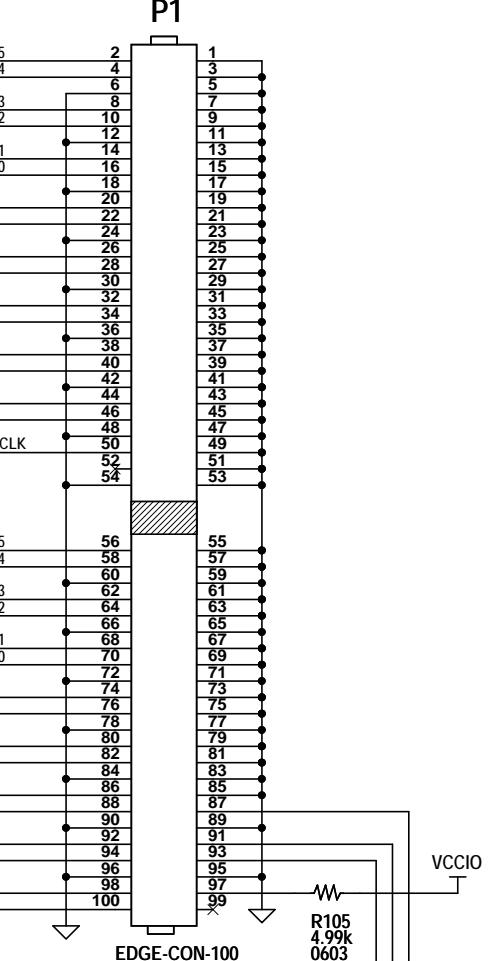
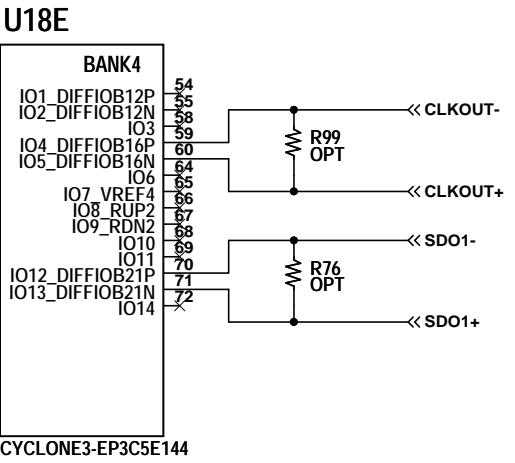
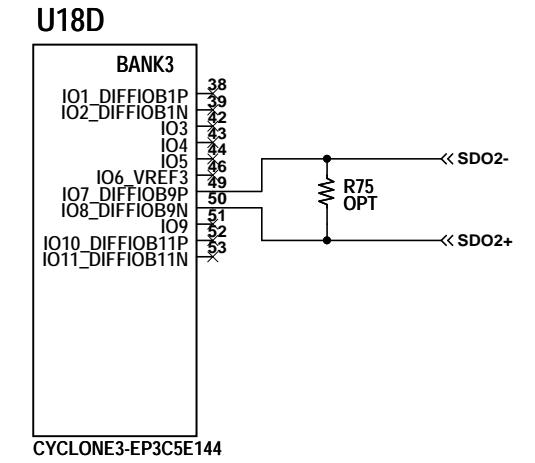
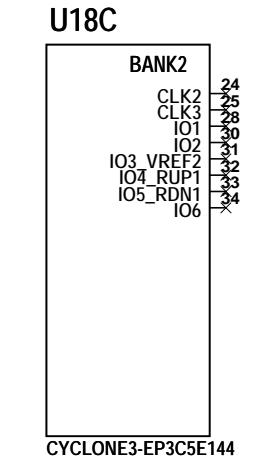
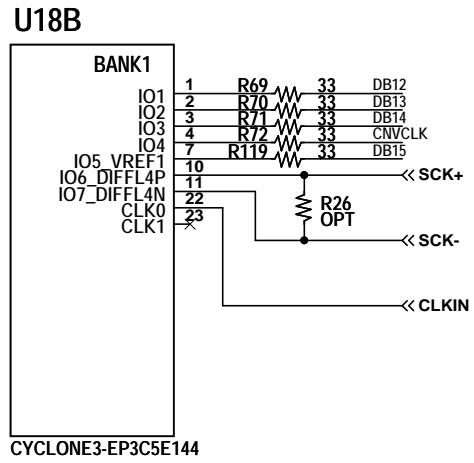
SIZE B IC NO. LTC232XCUD-16/14 FAMILY REV. 2
DEMO CIRCUIT 1996A

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

SCALE = NONE

DATE: 04/01/2014, 01:00 PM

SHEET 2 OF 3



NOTES: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS AND CAPACITORS ON THIS PAGE ARE 0402.
 2. SEE ASSEMBLY TABLE ON PAGE 1 FOR R67, R68, R73, AND R74 VALUES

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APPROVALS

DES.	KIM T.
ENG.	DOUG S.

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