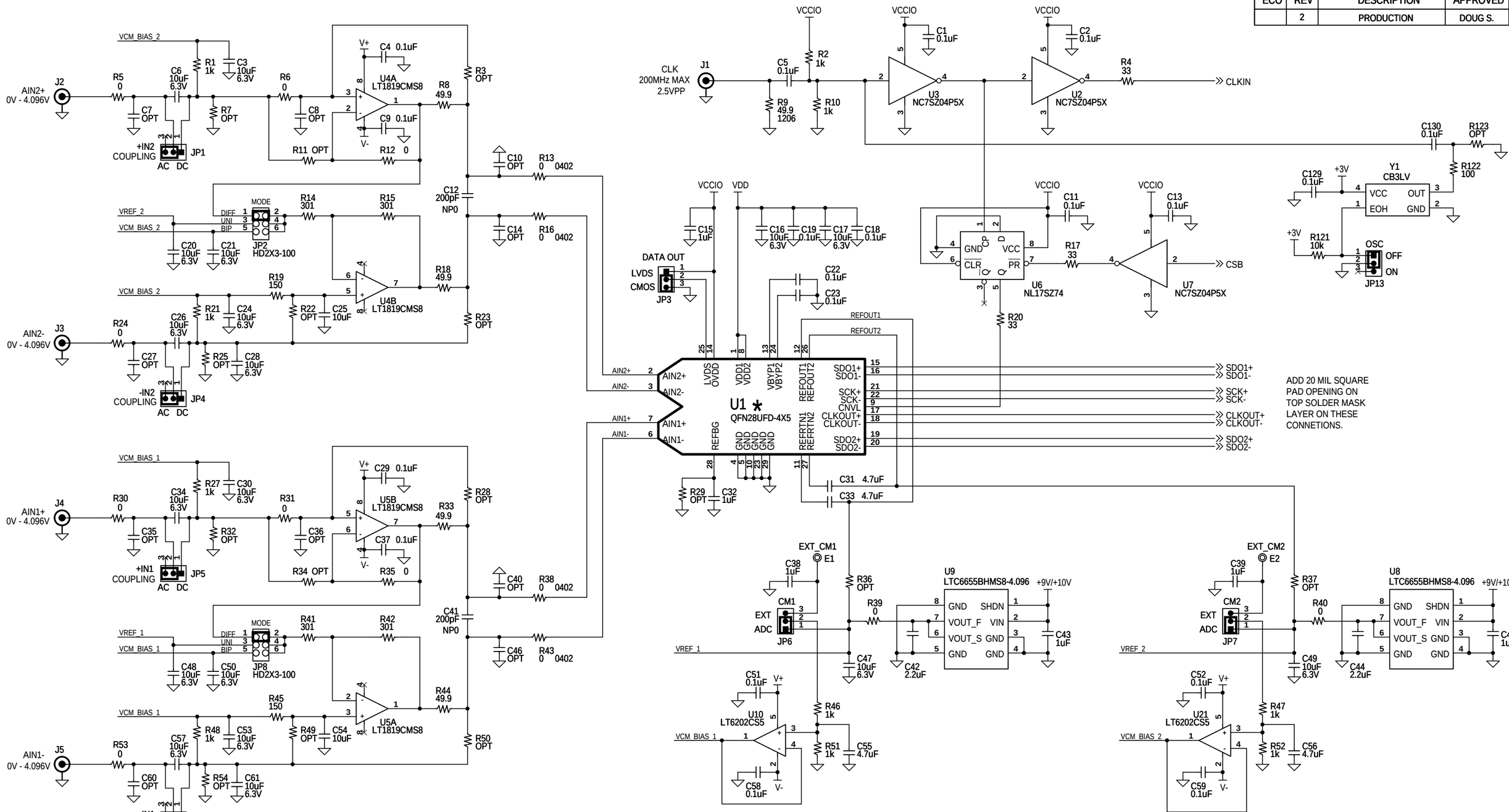
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	2	PRODUCTION	DOUG S.	04-01-14



ADD 20 MIL SQUARE PAD OPENING ON TOP SOLDER MASK LAYER ON THESE CONNETIONS.

*

ASSY	U1	BITS	Msps	R67	R68	R73	R74
A	LTC2323CUFD-16	16	5	1k	OPT	1k	OPT
B	LTC2321CUFD-16	16	2				
C	LTC2323CUFD-14	14	5	OPT	1k		
D	LTC2321CUFD-14	14	2				
E	LTC2323CUFD-12	12	5	1k	OPT	OPT	1k
F	LTC2321CUFD-12	12	2				

NOTES: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS ARE IN OHMS, 0603.
   ALL CAPACITORS ARE IN MICROFARADS, 0603

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

APPROVALS	
PCB DES.	KIM T.
APP ENG.	DOUG S.

SCALE = NONE

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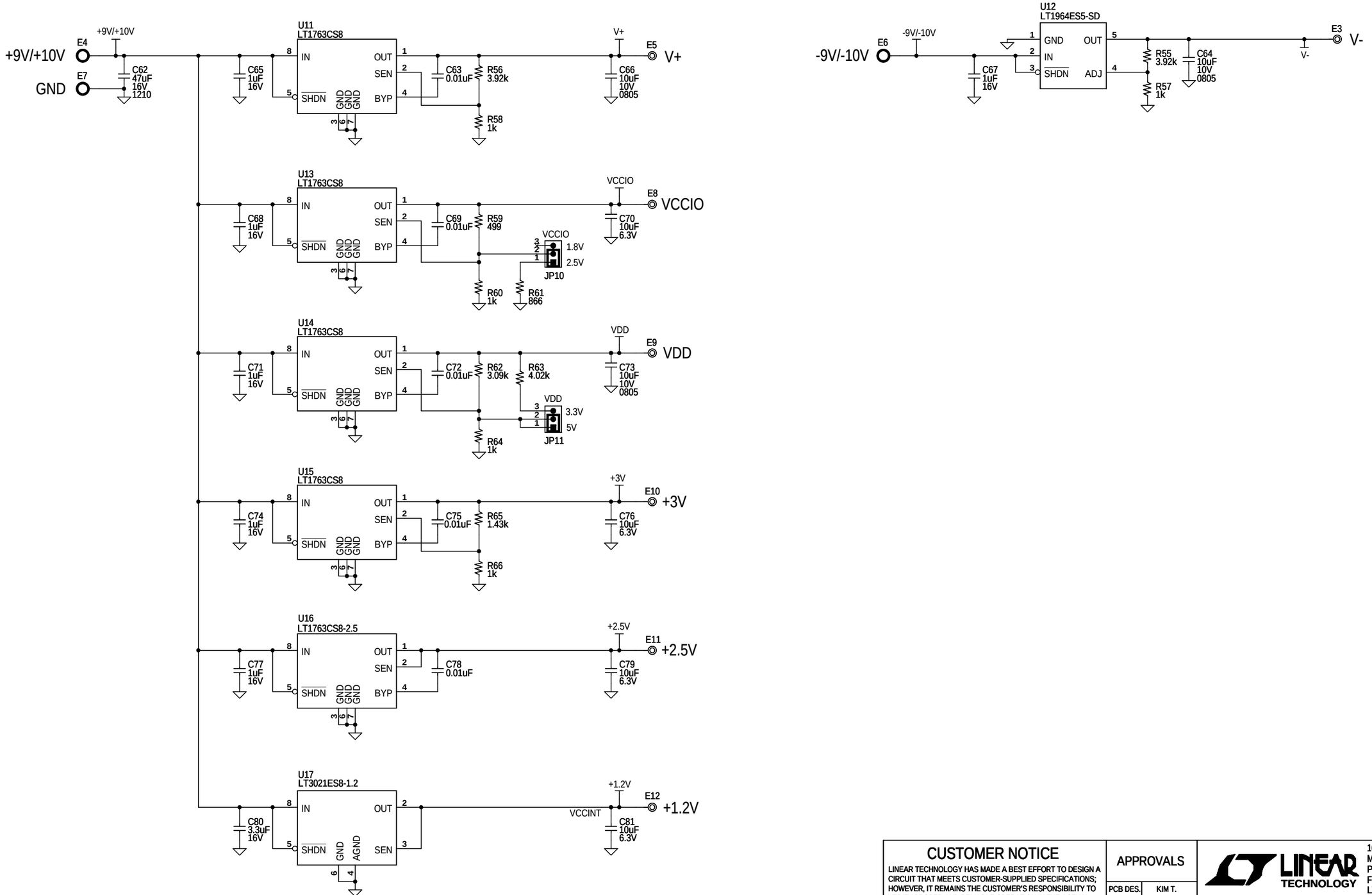
TRUE DIFFERENTIAL INPUT DUAL ADC

SIZE	IC NO.	REV.
B	LTC232XCUFD FAMILY DEMO CIRCUIT 1996A	2

DATE: 04/01/2014, 12:58 PM



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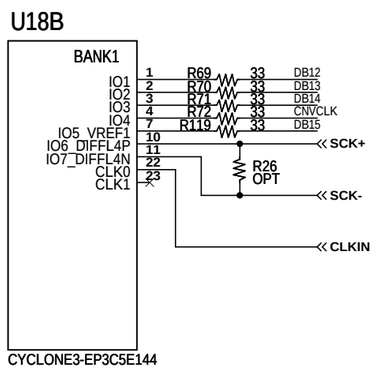
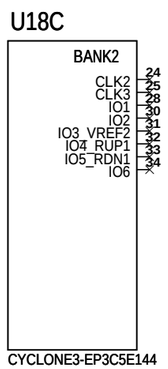
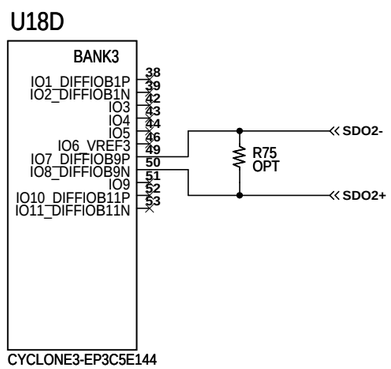
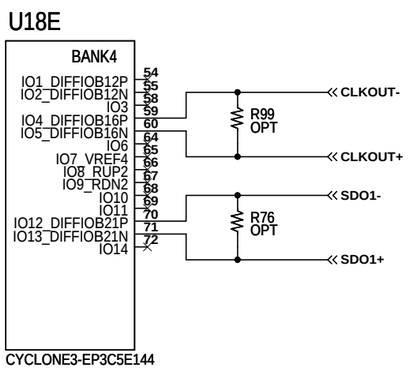
SIZE	IC NO.	REV.
B	LTC232XCUFD-16/-14 FAMILY DEMO CIRCUIT 1996A	2

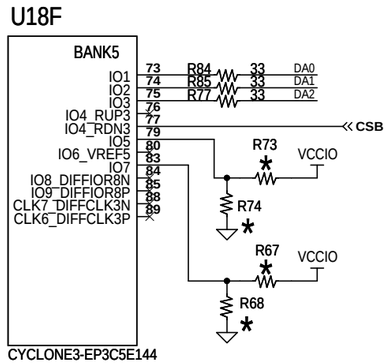
DATE: 04/01/2014, 01:00 PM

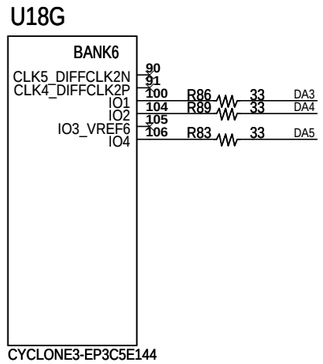


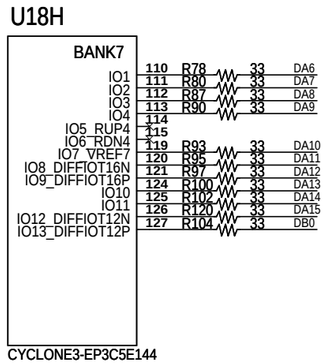


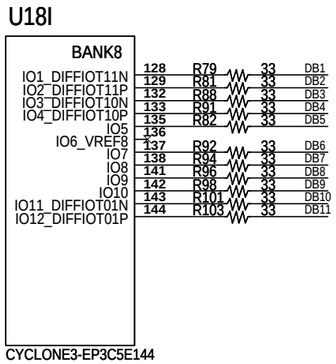


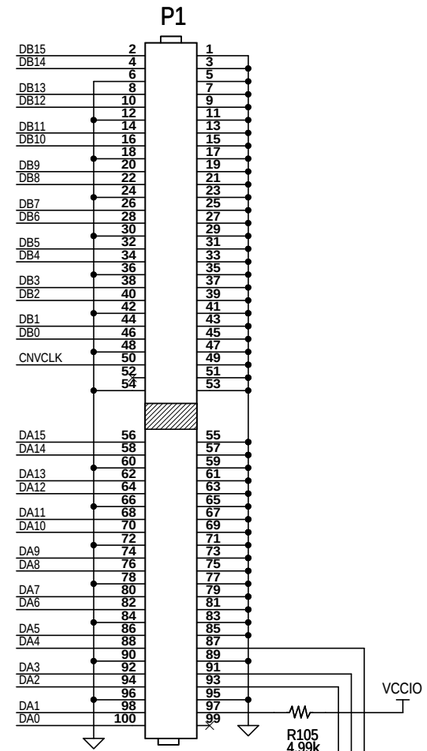


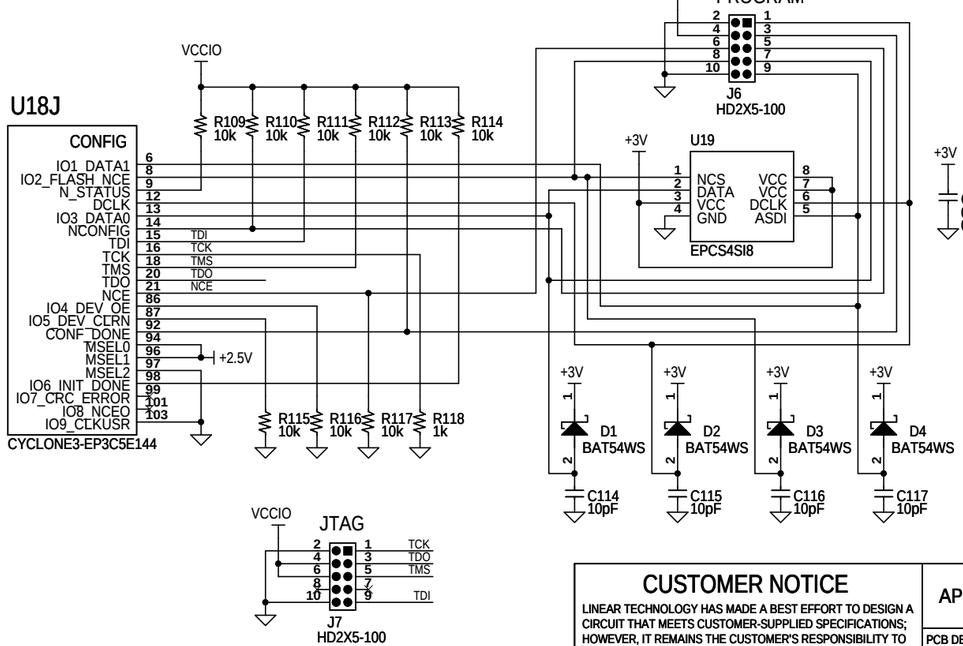


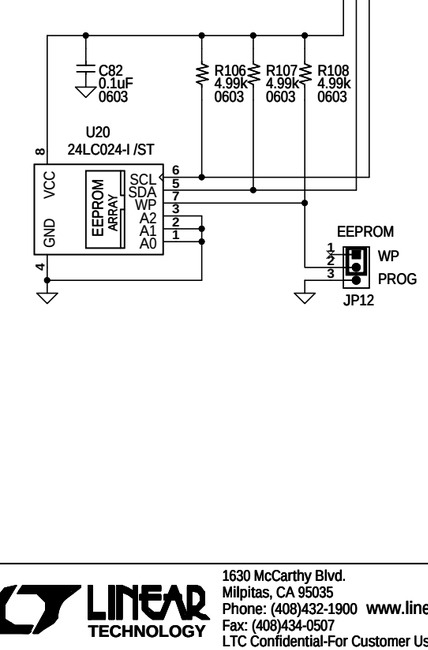












NOTES: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS AND CAPACITORS ON THIS PAGE ARE 0402.
2. SEE ASSEMBLY TABLE ON PAGE 1 FOR R67, R68, R73, AND R74 VALUES.

APPROVALS	
PCB DES.	KIM T.
APP ENG.	DOUG S.

SCALE = NONE

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DATE: 04/01/2014, 12:59 PM SHEET 3 OF 3