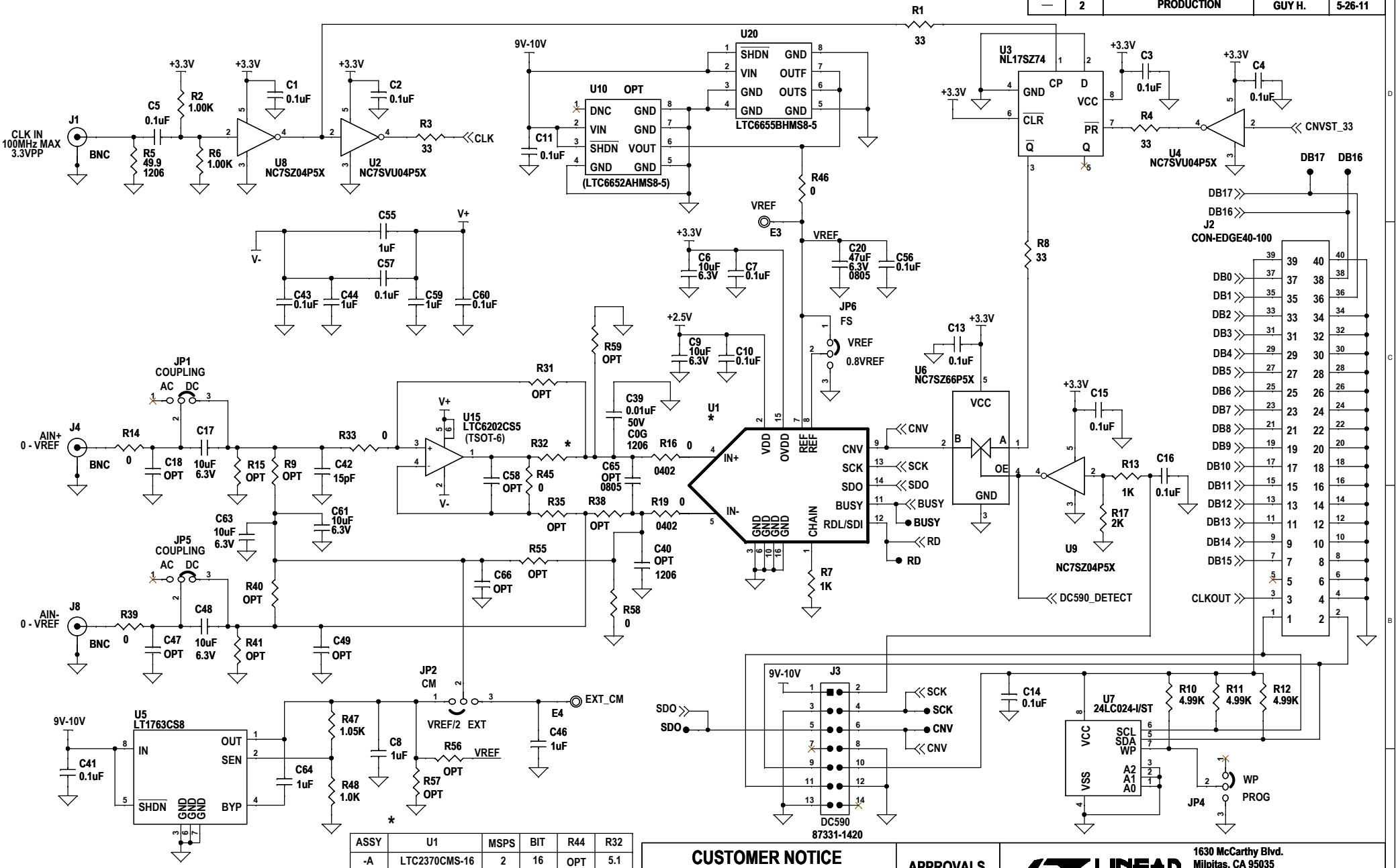
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
—	2	PRODUCTION	GUY H.	5-26-11



ASSY	U1	MSPS	BIT	R44	R32
-A	LTC2370CMS-16	2	16	OPT	5.1
-B	LTC2368CMS-16	1	16	OPT	5.1
-C	LTC2367CMS-16	0.5	16	OPT	10
-D	LTC2364CMS-16	0.25	16	OPT	10
-E	LTC2369CMS-18	1.6	18	300	5.1
-F	LTC2368CMS-18	1	18	300	5.1
-G	LTC2367CMS-18	0.5	18	300	10
-H	LTC2364CMS-18	0.25	18	300	10

NOTE: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS ARE 0603.
   ALL CAPACITORS ARE 0603.
2. INSTALL SHUNTS AS SHOWN.

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

APPROVALS	
PCB DES.	JW
APP ENG.	GUY H.
SCALE = NONE	

LINEAR TECHNOLOGY
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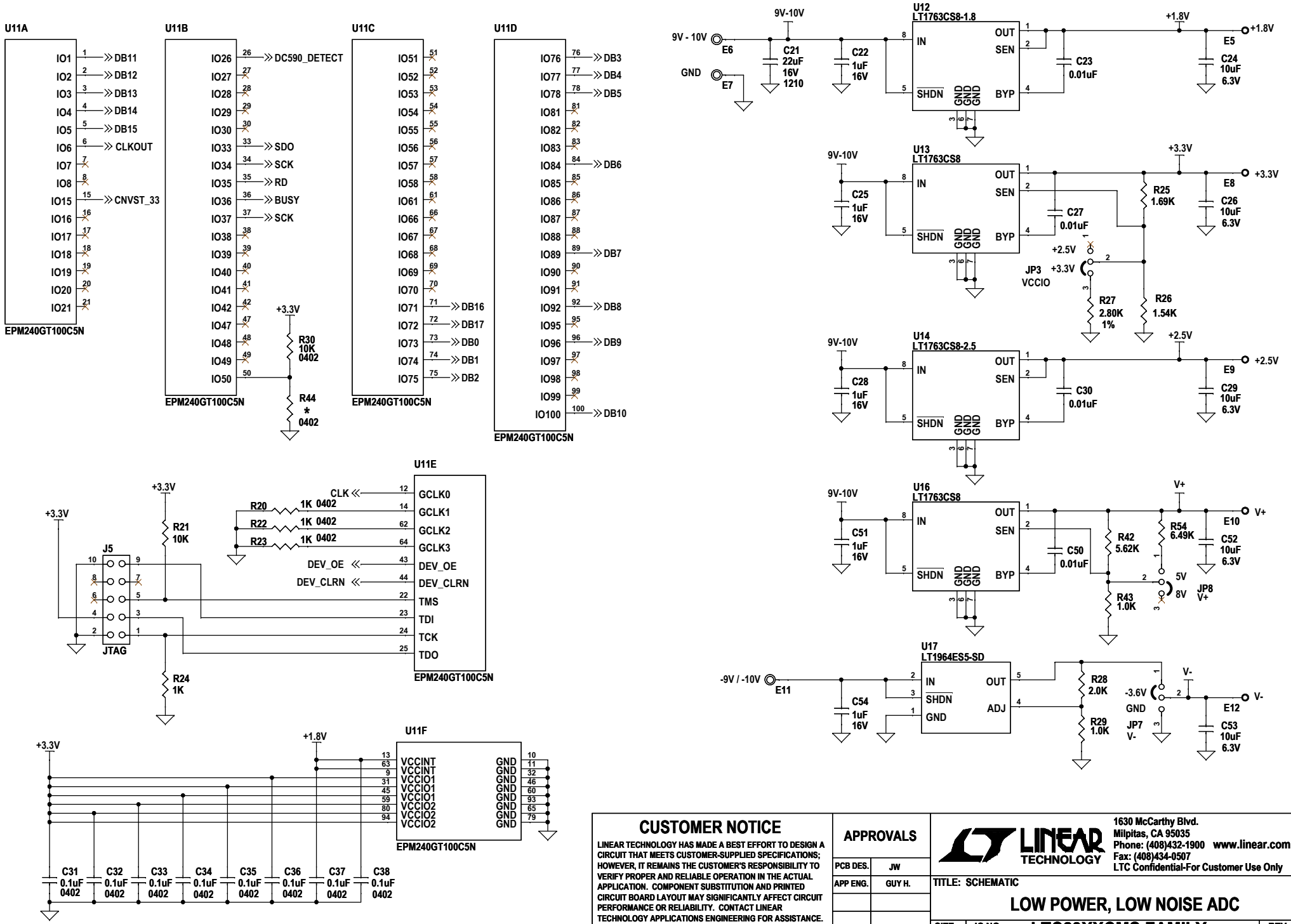
Milpitas, CA 95035
Phone: (408)432-1900 www.linear.com
Fax: (408)434-0507

TITLE: SCHEMATIC

LOW POWER, LOW NOISE ADC

SIZE	IC NO.		REV.
N/A		LTC23XXCMS FAMILY DEMO CIRCUIT 1813A	2
DATE:	05-26-11		SHEET 1 OF 2



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SIZE	IC NO.	REV.
N/A	LTC23XXCMS FAMILY DEMO CIRCUIT 1813A	2
DATE: 05-26-11		SHEET 2 OF 2