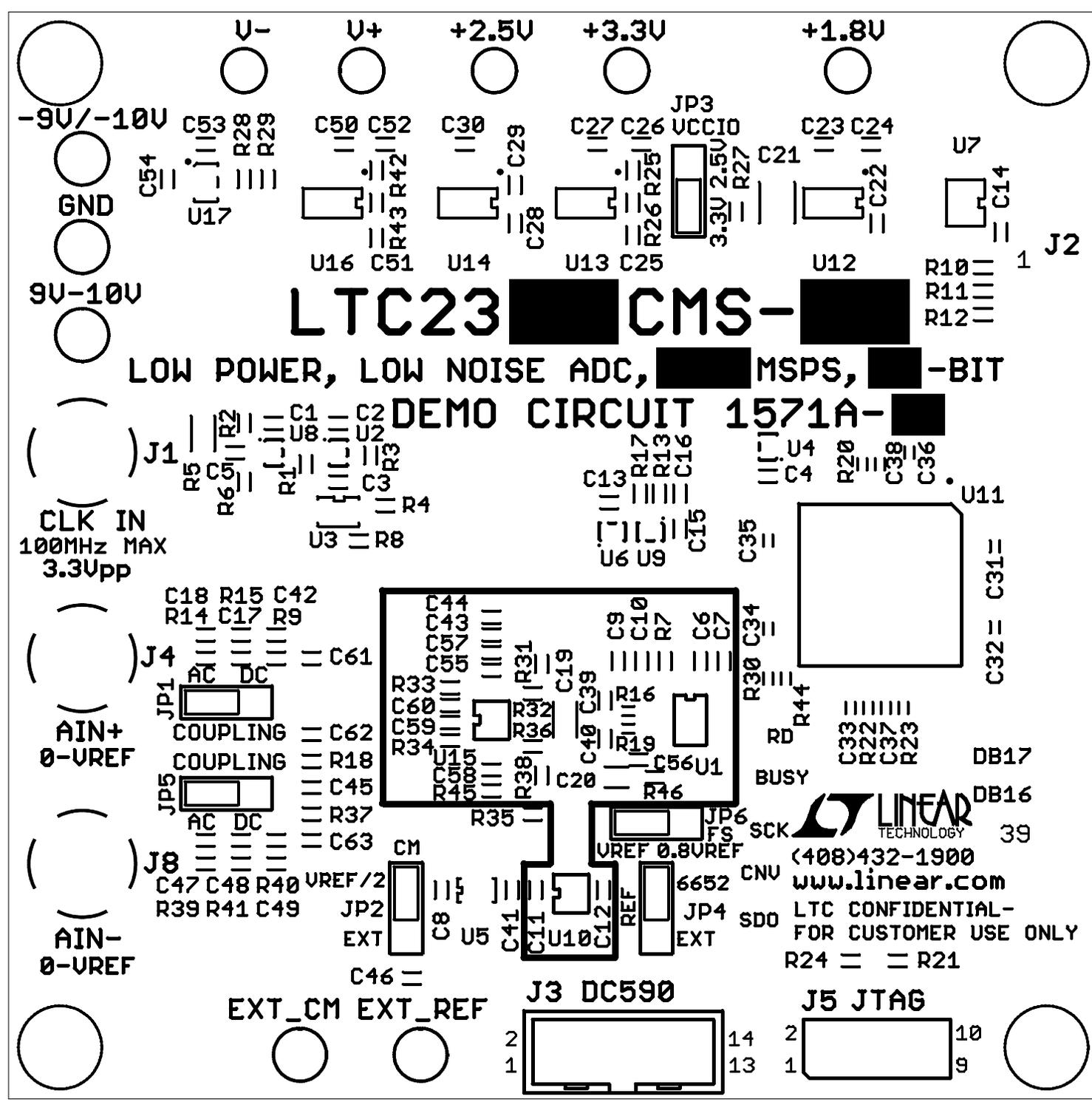


# LTC23 CMS-

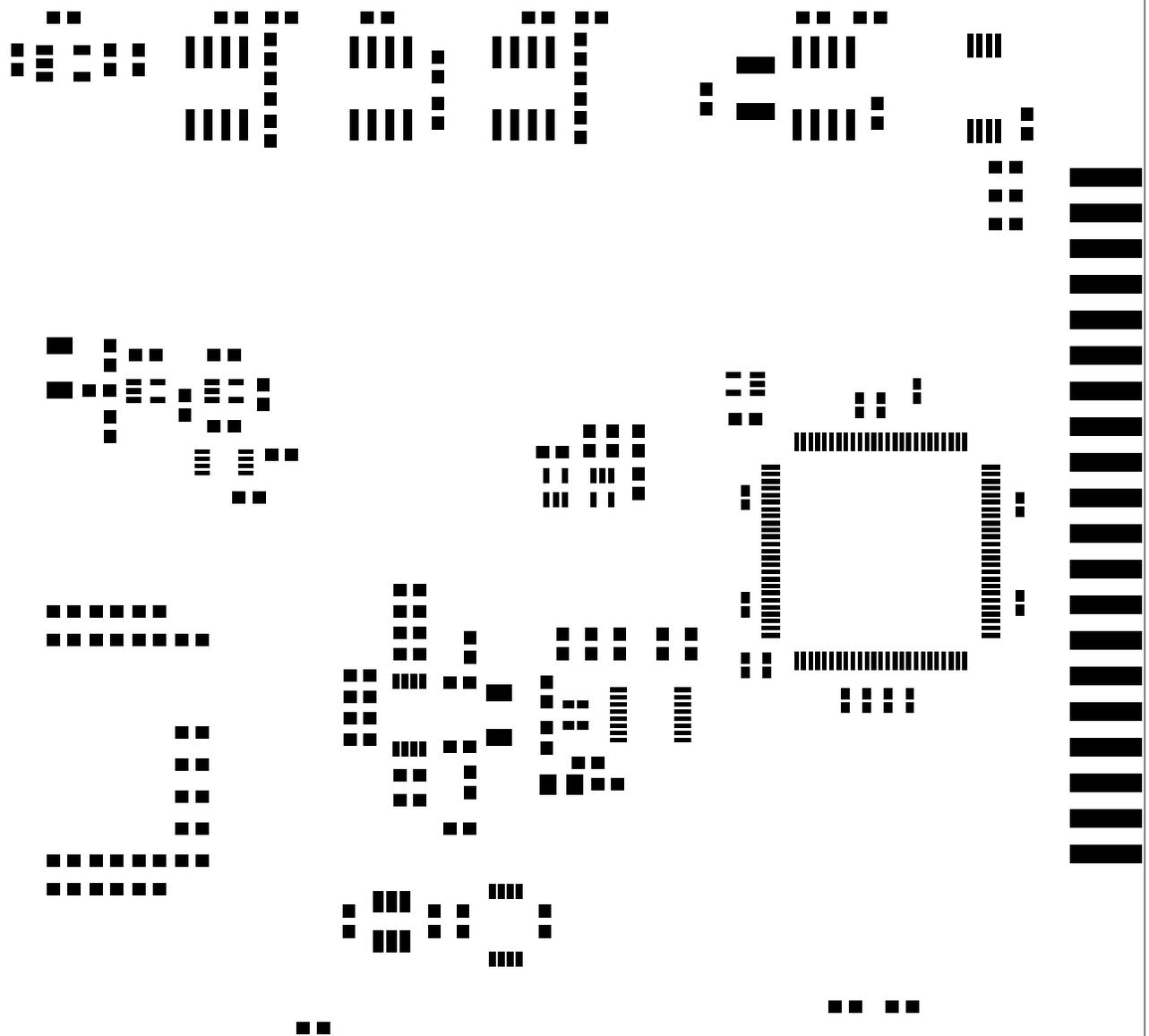
LOW POWER, LOW NOISE ADC, MSPS, -BIT

## DEMO CIRCUIT 1571A-

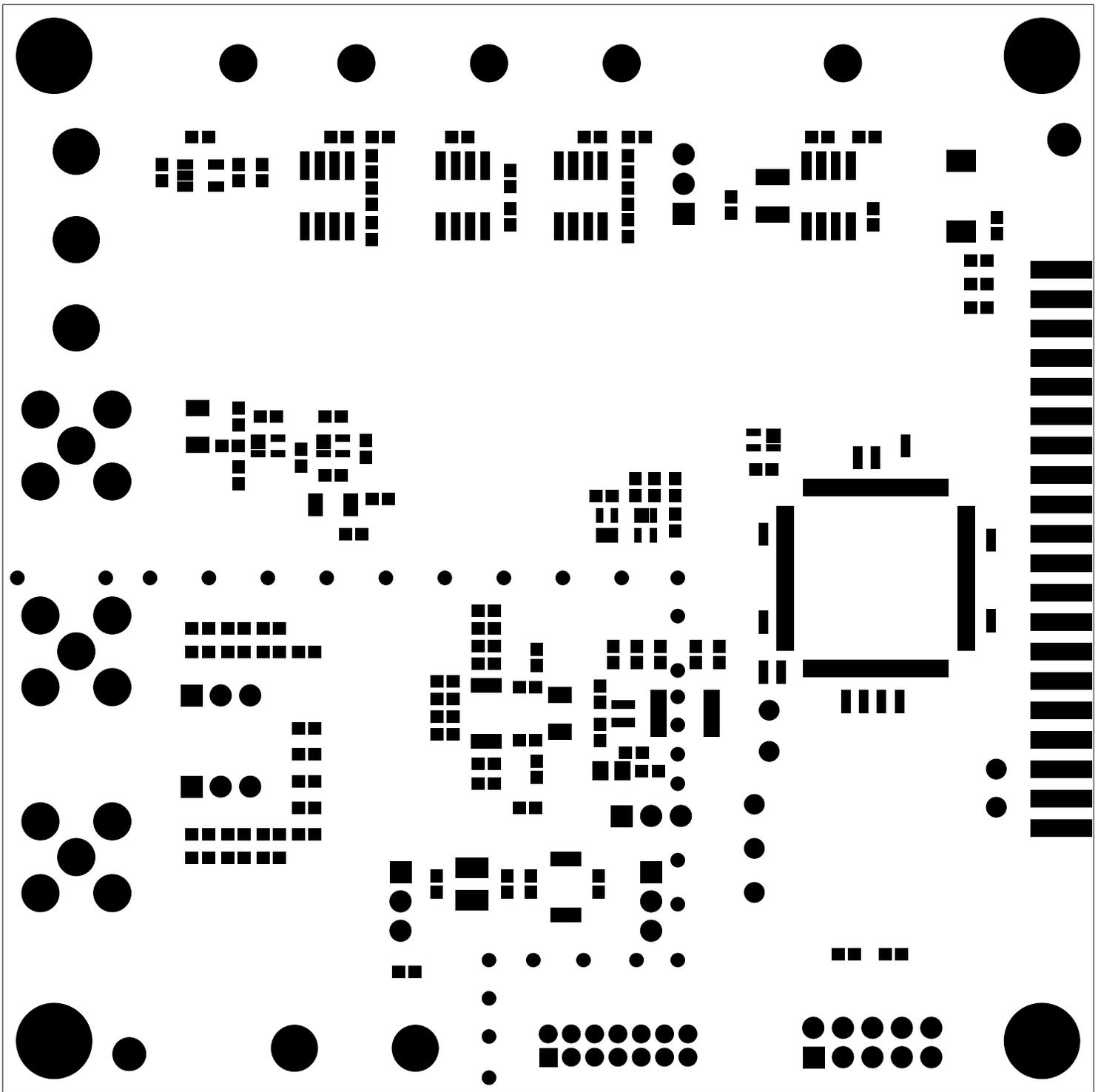


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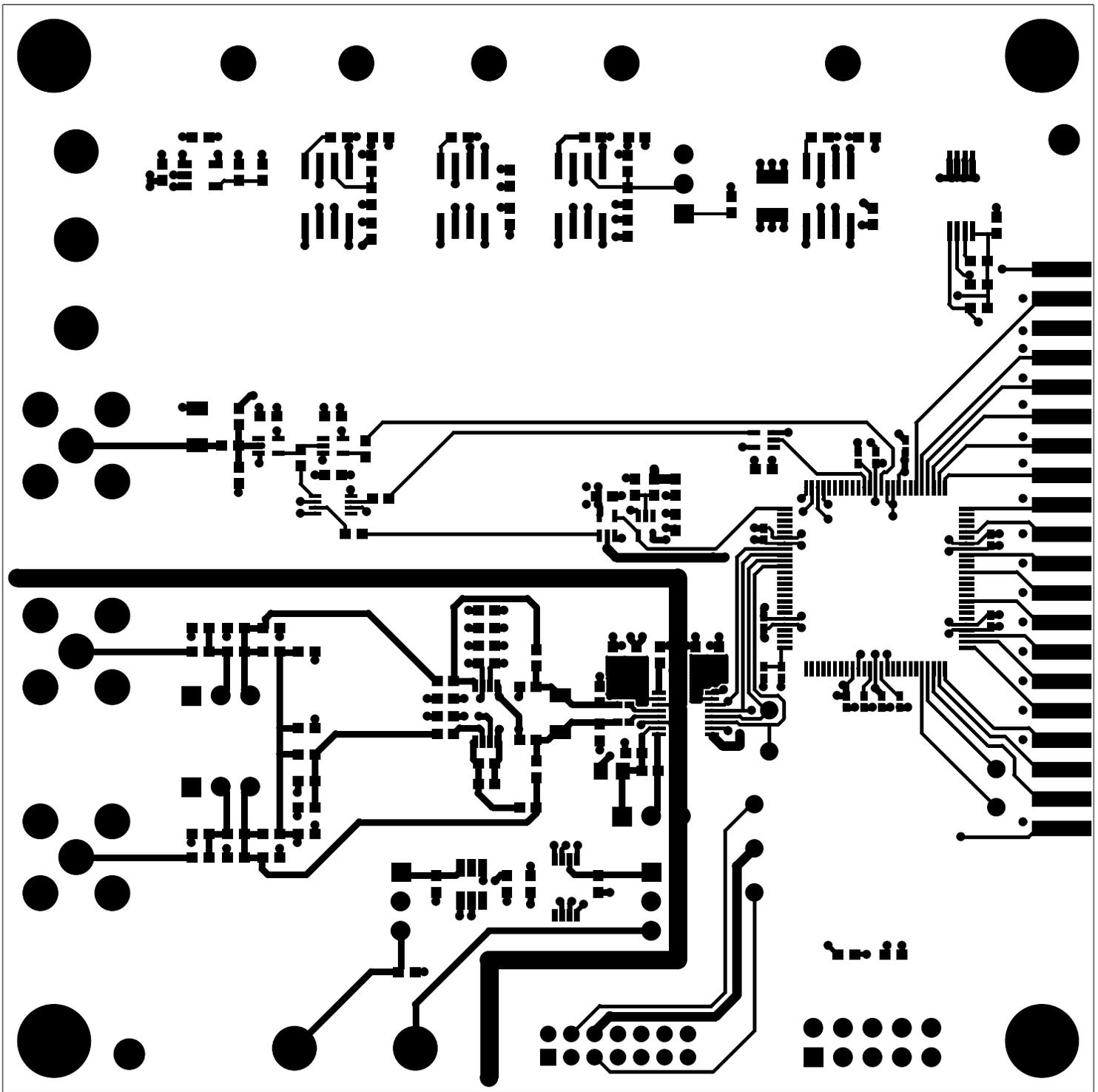
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TITLE		DEMO CIRCUIT	REV		
LOW POWER, LOW NOISE ADC		1571A	3		
LAYER: TOP SILKSCREEN					
DSNR: KIM TRAN			DATE: 3/19/2010 11:15:41a		



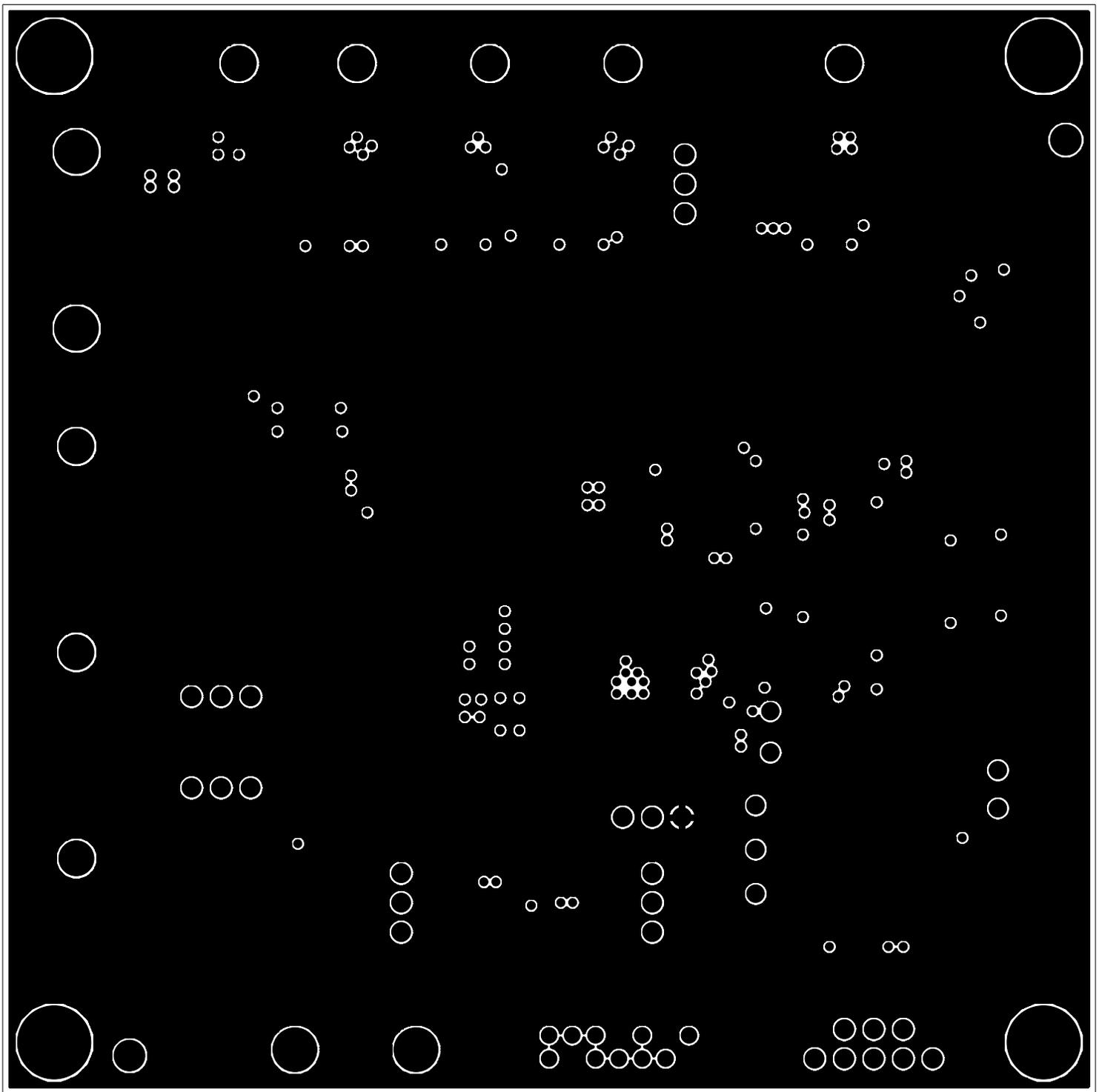
COMPANY LINEAR TECHNOLOGY CORPORATION		
TITLE LOW POWER, LOW NOISE ADC	DEMO CIRCUIT 1571A	REV 3
LAYER: TOP PASTE		
DSNR: KIM TRAN	DATE: 3/19/2010 11:15:41a	



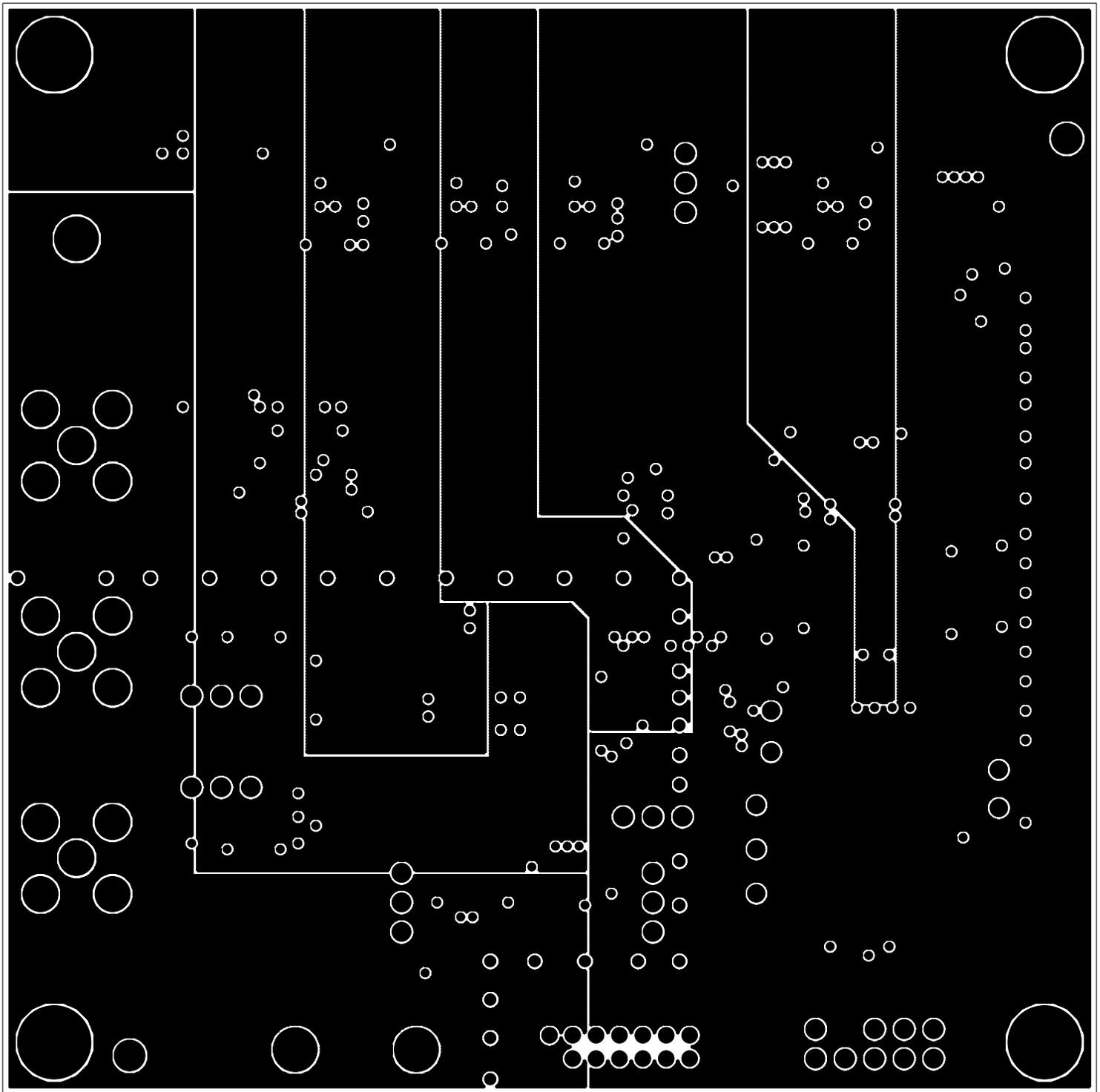
COMPANY LINEAR TECHNOLOGY CORPORATION		
TITLE LOW POWER, LOW NOISE ADC	DEMO CIRCUIT 1571A	REV 3
LAYER: TOP MASK		
DSNR: KIM TRAN	DATE: 3/19/2010 11:15:41a	



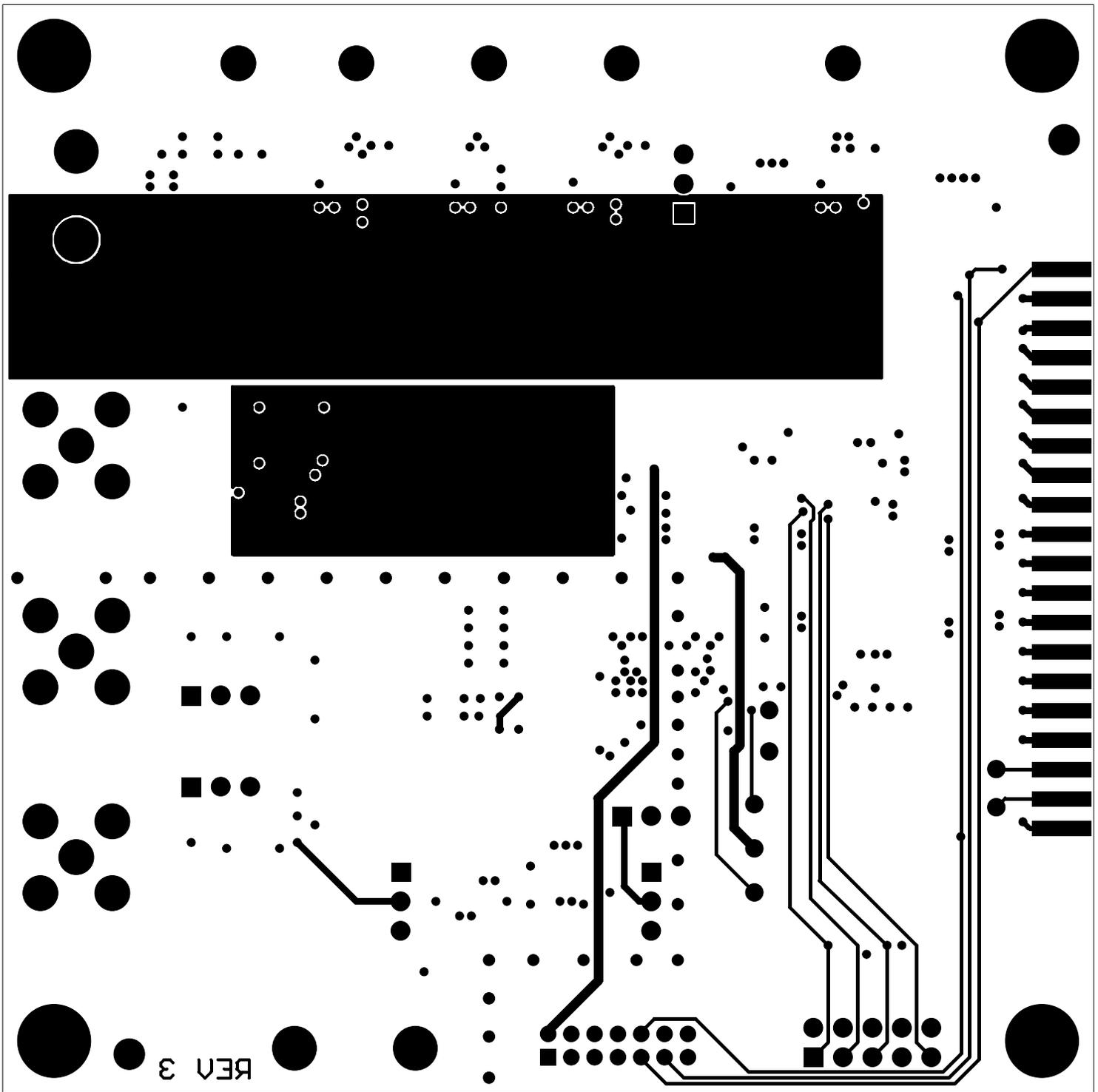
COMPANY			LINEAR TECHNOLOGY CORPORATION		
TITLE		DEMO CIRCUIT	REV		
LOW POWER, LOW NOISE ADC		1571A	3		
LAYER: 1 - TOP LAYER					
DSNR: KIM TRAN		DATE: 3/19/2010 11:15:41a			



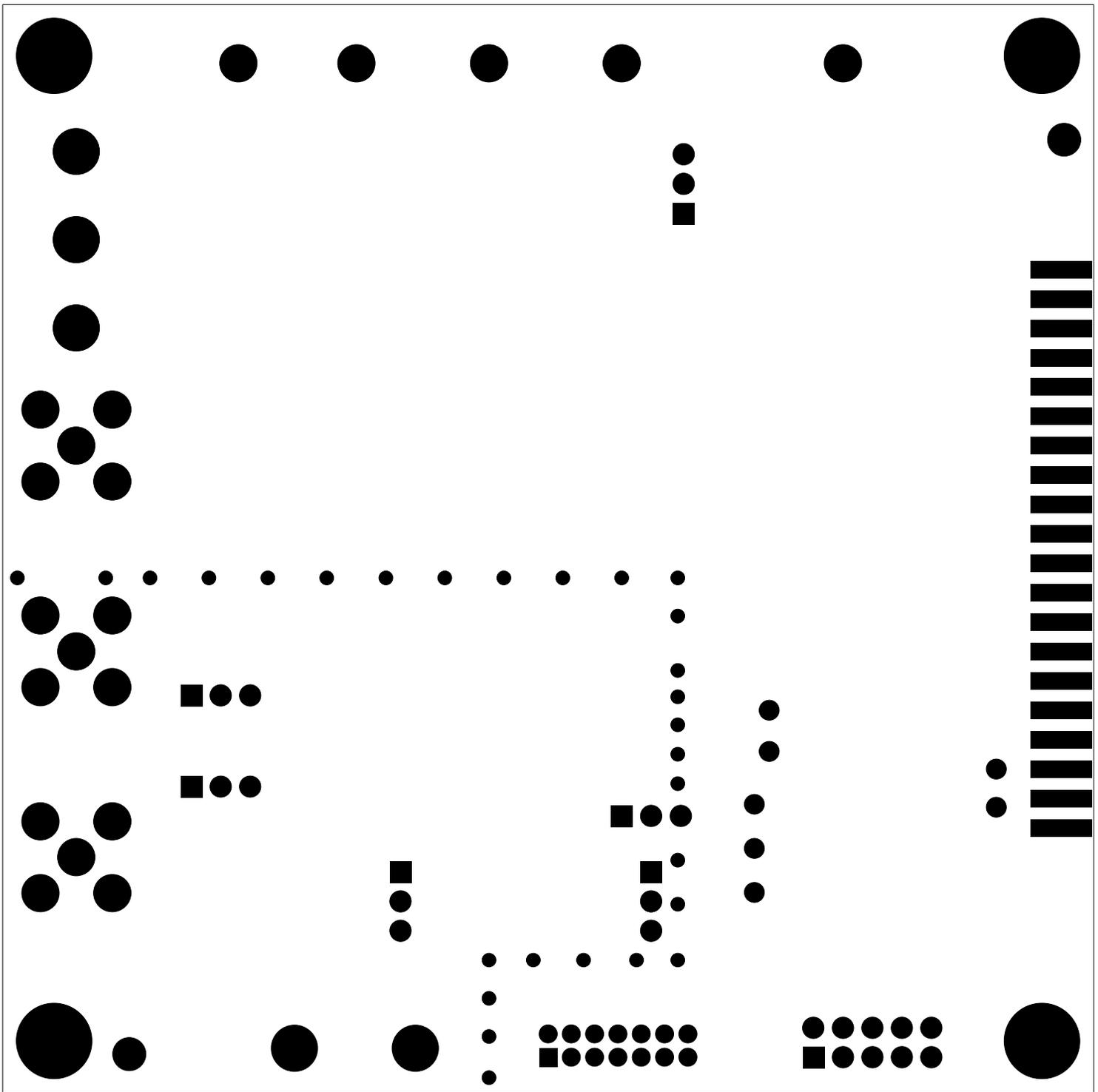
COMPANY			LINEAR TECHNOLOGY CORPORATION		
TITLE		DEMO CIRCUIT	REV		
LOW POWER, LOW NOISE ADC		1571A	3		
LAYER: 2 - GND PLANE					
DSNR: KIM TRAN		DATE: 3/19/2010 11:15:41a			



COMPANY			LINEAR TECHNOLOGY CORPORATION		
TITLE		DEMO CIRCUIT	REV		
LOW POWER, LOW NOISE ADC		1571A	3		
LAYER: 3 - PWR PLANE					
DSNR: KIM TRAN		DATE: 3/19/2010 11:15:41a			



COMPANY			LINEAR TECHNOLOGY CORPORATION		
TITLE		DEMO CIRCUIT		REV	
LOW POWER, LOW NOISE ADC		1571A		3	
LAYER: 4 - BOTTOM LAYER					
DSNR: KIM TRAN			DATE: 3/19/2010 11:15:41a		



COMPANY LINEAR TECHNOLOGY CORPORATION		
TITLE LOW POWER, LOW NOISE ADC	DEMO CIRCUIT 1571A	REV 3
LAYER: BOTTOM MASK		
DSNR: KIM TRAN	DATE: 3/19/2010 11:15:41a	