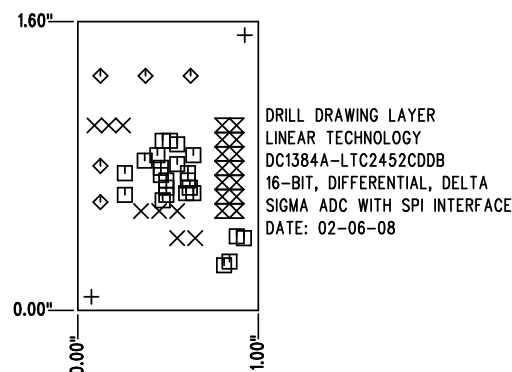
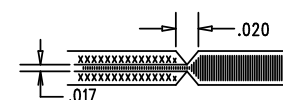


REVISIONS			
REV	DESCRIPTION	APPR	DATE
A	1ST PROTOTYPE	LEO C.	02-06-08


SIZE	QTY	SYM	PLATED	TOL
0.07	2	+	NO	+/-0.003"
0.035	8	×	YES	+/-0.003"
0.012	24	□	YES	+/-0.003"
0.064	5	◇	YES	+/-0.003"
0.03543	14	⊗	YES	+/-0.003"

NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600
2. MATERIAL: EPOXY FIBERGLASS, NEMA GRADE FR-4
FINISHED THICKNESS TO BE 0.062" +/- .005"
TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE
OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
FLAMMABILITY RATING: 94 V-0 MINIMUM .
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
4. DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH
HOLES WITH COPPER, 0.001" THICK MIN.
ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
HOLE LOCATION TOLERANCES ARE +/-0.003"
IN RELATION TO CENTER
5. FINISH: SMOBC USING LPI BOTH SIDES, COLOR GREEN.
GOLD IMMERSION. USE LEAD FREE SOLDER FOR PROTOTYPE.
FOR SILKSCREEN: USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs ARE TO BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCB:



SHOWN FROM COMPONENT SIDE

APPROVALS			 LINEAR TECHNOLOGY 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY
	INIT	DATE	
DRAWN			
CHECK			
DESIGN	KIM T.	02-06-08	
ENGR	LEO C.	02-06-08	TITLE: FABRICATION DRAWING 16-BIT, DIFFERENTIAL, DELTA SIGMA ADC WITH SPI INTERFACE
			SIZE A DEMO DC1384A-LTC2452CDDb REV. A
SCALE = NONE			SHT 1 of 1