



**\* FOR ASSEMBLY**

ASSY	U1
-A	LTC2452CDDB
-B	TBD

**NOTES: UNLESS OTHERWISE SPECIFIED**

- ALL RESISTORS ARE IN OHMS, 0402.  
ALL CAPACITORS ARE 0402.
- INSTALL SHUNT ON JP1 (1&2).

**CUSTOMER NOTICE**

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

CONTRACT NO.

APPROVALS

DRAWN: KIM T.

CHECKED:

APPROVED:

ENGINEER: LEO C.

DESIGNER:



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LTC Confidential-For Customer Use Only

TITLE: SCHEMATIC

16-BIT, DIFFERENTIAL, DELTA SIGMA ADC WITH SPI INTERFACE

SIZE

DWG NO.

**DC1384A \* LTC2452CDDB**

REV

**A**

DATE: Wednesday, February 06, 2008

SHEET 1 OF 1