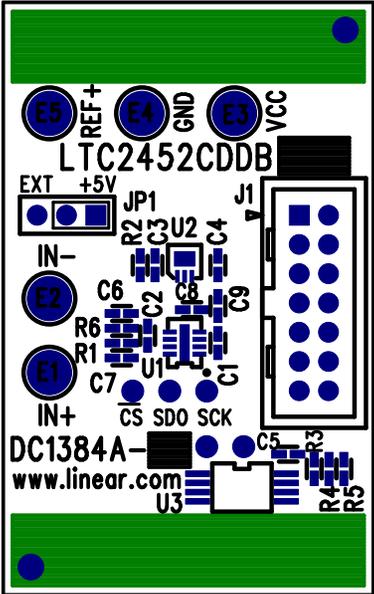


REVISIONS			
REV	DESCRIPTION	APPR	DATE
A	1ST PROTOTYPE	LEO C.	02-06-08



NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. INSTALL SHUNT ON JP1 (1&2).
3. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
4. ASSY PROCESSES SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD.
5. USE BLACK PERMANENT MARKER TO WRITE ON THE WHITE BLOCK AREAS BASED ON THE TABLE BELOW:

ASSY	U1
-A	LTC2452CDDB
-B	TBD

APPROVALS				1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY	
	INIT	DATE		TITLE: ASSEMBLY DRAWING 16-BIT, DIFFERENTIAL, DELTA SIGMA ADC WITH SPI INTERFACE	
DRAWN			SIZE A DEMO DC1384A-LTC2452CDDB REV. A		
CHECK					SCALE = NONE
DESIGN	KIM T.	02-06-08	SHT 1 of 1		
ENGR	LEO C.	02-06-08			