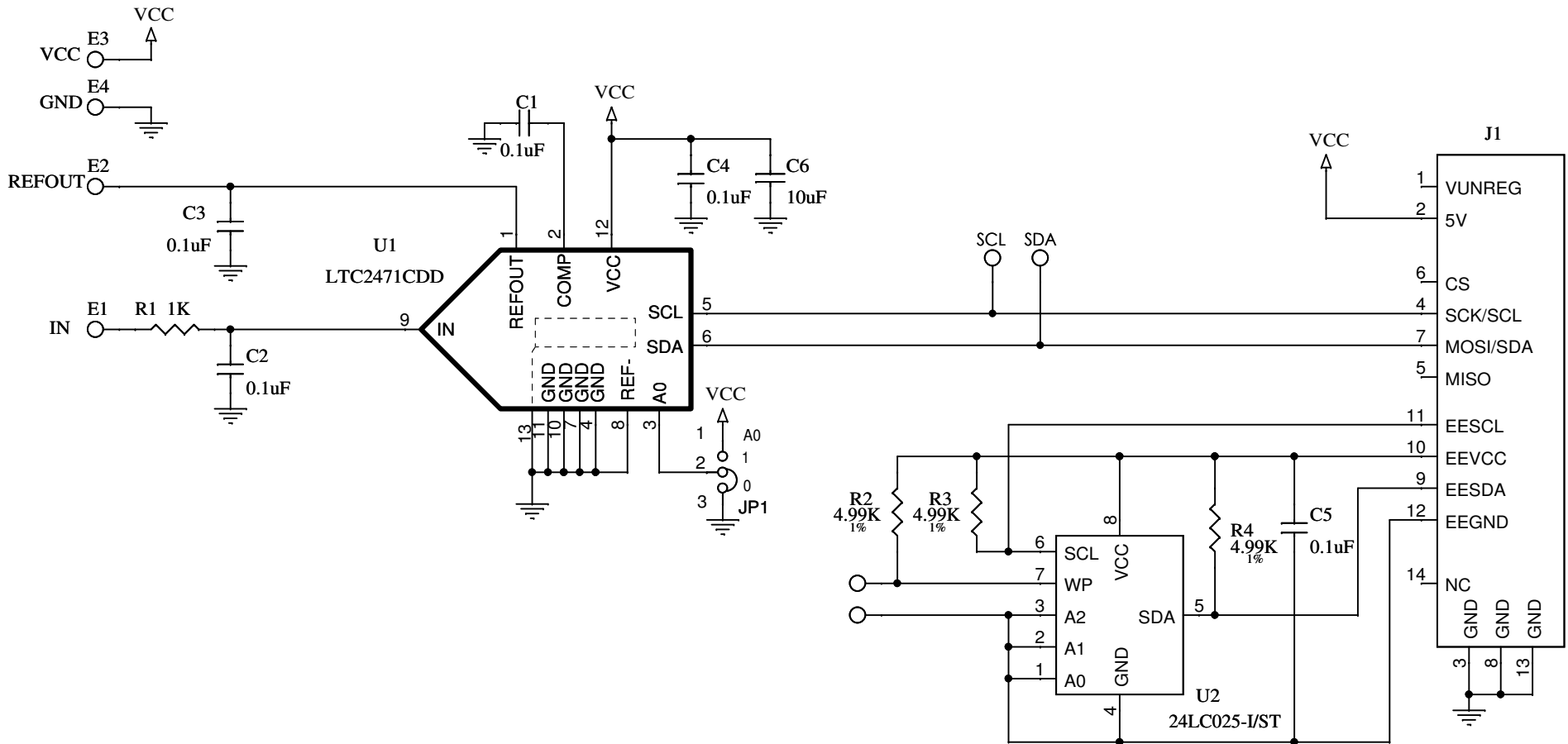


REVISION HISTORY

ECO	REV	DESCRIPTION	APPROVED	DATE
	1	FIRST PROTOTYPE	LEO C.	06/04/10



CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS

PCB DES.	MI
APP ENG.	LEO C.

SCALE = NONE



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TITLE: SCHEMATIC
16-BIT DELTA SIGMA I2C ADC

SIZE	IC NO.	REV.
N/A	LTC2471CDD DEMO CIRCUIT 1718A	1

DATE: Tuesday, June 08, 2010	SHEET 1 OF 1
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