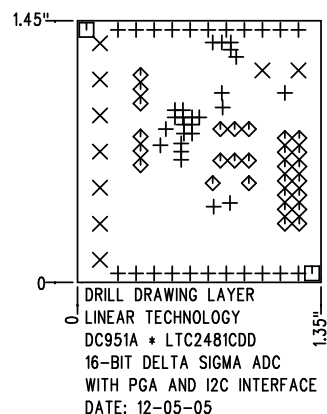
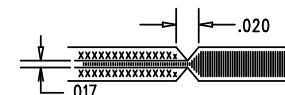


REVISIONS			
REV	DESCRIPTION	APPR	DATE
A	FINAL FABRICATION	MT	12-05-05


SIZE	QTY	SYM	PLTD	TOL
0.015	47	+	YES	+/-0.003"
0.064	9	X	YES	+/-0.003"
0.07	2	□	NO	+/-0.003"
0.035	28	◇	YES	+/-0.003"

NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600
- MATERIAL: EPOXY FIBERGLASS, NEMA GRADE FR-4
FINISHED THICKNESS TO BE 0.062" +/- .005"
TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE
OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
FLAMABILITY RATING: 94 V-0 MINIMUM .
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
- DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH
HOLES WITH COPPER, 0.001" THICK MIN.
ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
HOLE LOCATION TOLERANCES ARE +/-0.003"
IN RELATION TO CENTER
- FINISH: SMOBC USING LPI BOTH SIDES, COLOR GREEN.
WHITE TIN IMMERSION (OMIKRON).
FOR SILKSCREEN: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
- SCORING FOR PANELIZED PCB:



SHOWN FROM COMPONENT SIDE

APPROVALS			 LINEAR TECHNOLOGY 1630 MCCATHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY
	INIT	DATE	
DRAWN			
CHECK			
DESIGN	KIM T.	12-05-05	
ENGR	MARK T.	12-05-05	TITLE: FABRICATION DRAWING 16-BIT DELTA SIGMA ADC WITH PGA AND I2C INTERFACE SIZE A DEMO DC951A * LTC2481CDD REV. A SCALE = NONE DES- 0000 SHT 1 of 1