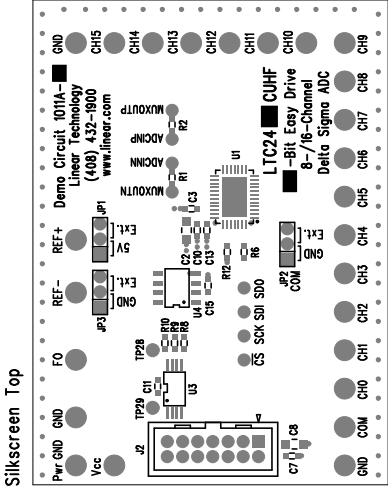


REVISIONS		
REV	APPR	DATE



- NOTES :**
1. Unused SMD components should be free of Solder.
 2. Fill up all vias with solder.

Install Shunts on JP1 = 5V
 JP2 = Gnd
 JP3 = Ext.

Assembly Drawing Top

APPROVALS		
DRAWN	INIT	DATE
CHECK		
DESIGN		
ENGR		
Scale = None		
Design #		
Sht: 1 of 1		

	1630 McCarthy Blvd. Milpitas, CA 95035 PH: (408)432-1900	
	Title: 24-/16-Bit Easy Drive 8-/16-Channel Delta Sigma ADC	
Size:	None	Rev. A
Demo Board:	DC1011A-A/B/C	