



NOTES: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS ARE IN OHMS, 0402.
ALL CAPACITORS ARE IN MICROFARADS, 0402.
2. INSTALL SHUNTS ON JP1-JP3 PIN 1 AND 2.

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

CONTRACT NO.	
APPROVALS	
DRAWN: KIM T.	
CHECKED:	
APPROVED:	
ENGINEER: MARK T.	
DESIGNER:	

		1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 Fax: (408)434-0507 LTC Confidential-For Customer Use Only	
		TITLE: SCHEMATIC	
DUAL 16-BIT RAIL-TO-RAIL VOUT DAC			
SIZE	DWG NO.	REV	
A	DC778A * LTC2602CMS8	A	
DATE:	Wednesday, March 17, 2004	SHEET 1 OF 1	