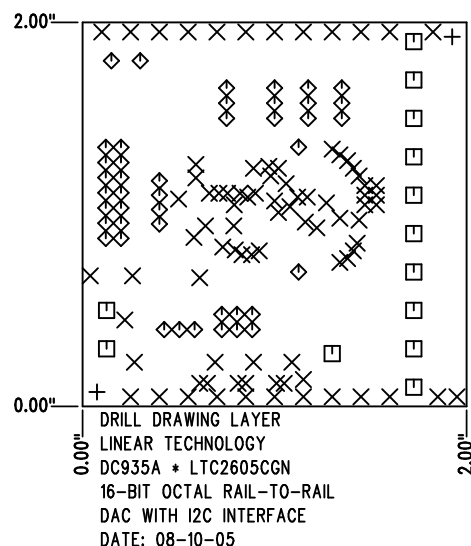


REVISIONS			
REV	DESCRIPTION	APPR	DATE
A	PROTOTYPE RELEASE	MT	06-13-05

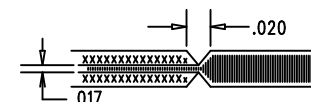


SHOWN FROM COMPONENT SIDE


SIZE	QTY	SYM	PLTD	TOL
0.07	2	+	NO	+/-0.003"
0.015	87	X	YES	+/-0.003"
0.064	13	□	YES	+/-0.003"
0.035	43	◇	YES	+/-0.003"

#### NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600
- MATERIAL: EPOXY FIBERGLASS, NEMA GRADE FR-4  
FINISHED THICKNESS TO BE 0.062" +/- .005"  
TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE  
OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.  
FLAMABILITY RATING: 94 V-0 MINIMUM .
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
0.00" ARE PRIMARY DATUMS.
- DRILLING: DRILL HOLES PER SCHEDULE. PLATE THROUGH  
HOLES WITH COPPER, 0.001" THICK MIN.  
ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
HOLE LOCATION TOLERANCES ARE +/-0.003"  
IN RELATION TO CENTER
- FINISH: SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
WHITE TIN IMMERSION (OMIKRON).  
FOR SILKSCREEN: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
- SCORING FOR PANELIZED PCB:



- PLEASE LOOK AT THE README FILE FOR THE OTHER REQUIREMENTS.

APPROVALS			 <b>LINEAR TECHNOLOGY</b> 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY	
DRAWN	INIT	DATE		
CHECK			<b>TITLE: FABRICATION DRAWING</b> <b>16-BIT OCTAL RAIL-TO-RAIL DAC WITH I2C INTERFACE</b>	
DESIGN	KIM T.	08-10-05		
ENGR	MARK T.	08-10-05	<b>SIZE</b> A <b>DEMO</b> DC935A-LTC2605CGN <b>REV.</b> A	
SCALE = NONE			DES- 0000	SHT 1 of 1