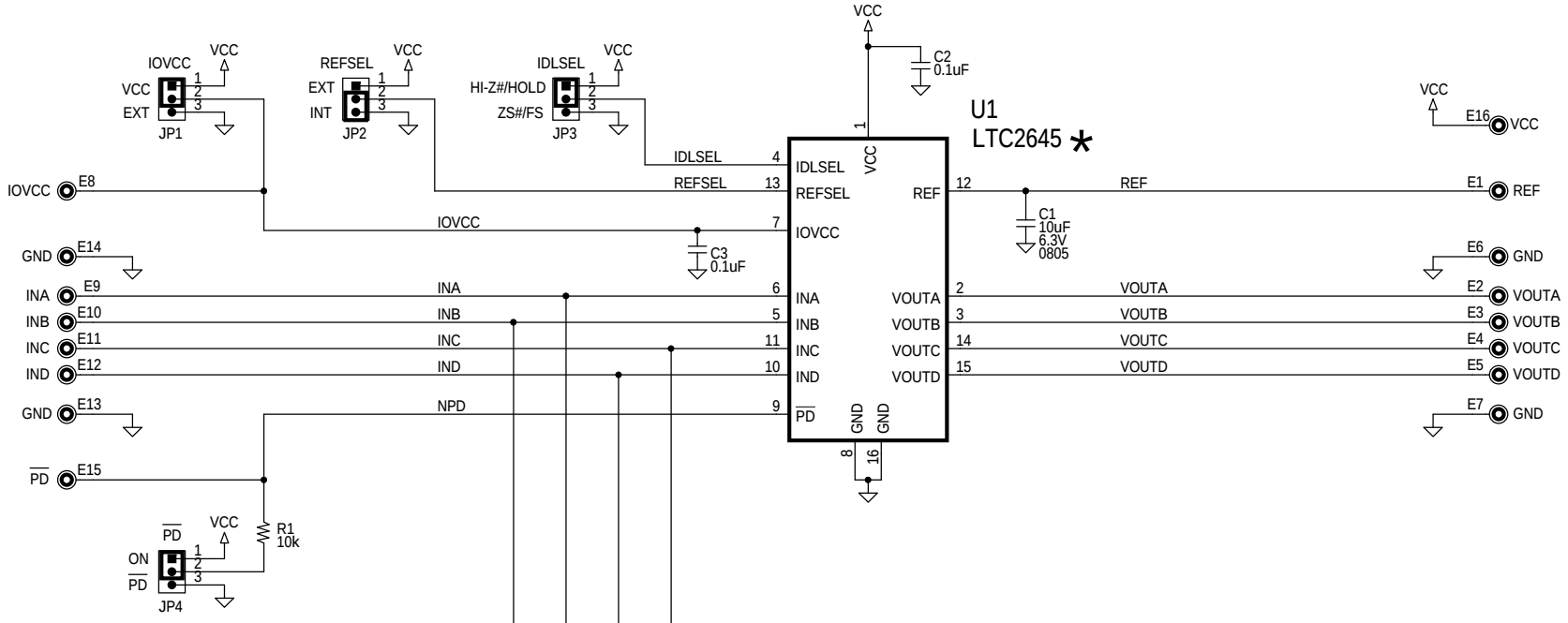
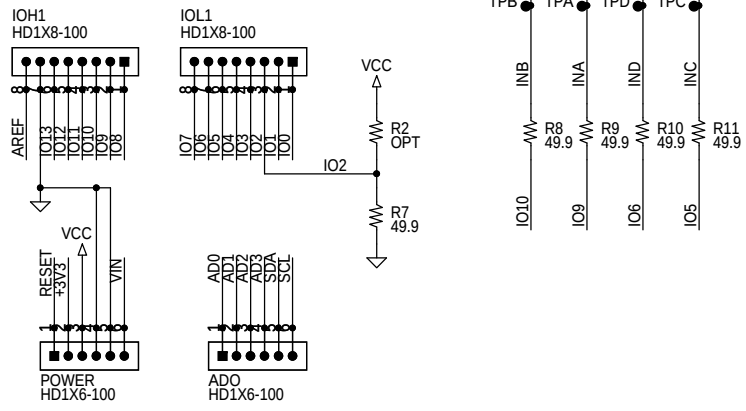
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
—	1	PRODUCTION	MARK T.	02-11-16



LINDUINO HEADERS



TEST PADS ALLOW REMAPPING LINDUINO CONNECTIONS

DACS A AND B CONNECTED TO 16-BIT CAPABLE PWM LINDUINO OUTPUTS

DACS C AND D CONNECTED TO 8-BIT CAPABLE PWM LINDUINO OUTPUTS

NOTE: UNLESS OTHERWISE SPECIFIED

1. ALL RESISTORS AND CAPACITORS ARE 0603.

*	ASSY TYPE	U1	SUFFIX	-BIT
	A	LTC2645-L12	L12	12

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS	
PCB DES.	KIM T.
APP ENG.	MARK T.

SCALE = NONE

LINEAR TECHNOLOGY
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TITLE: SCHEMATIC

QUAD 12/10/8-BIT PWM TO VOUT DACS

SIZE	IC NO.	REV.
N/A	LTC2645CMS FAMILY DEMO CIRCUIT 2197A	1

DATE: Thursday, February 11, 2016

SHEET 1 OF 1