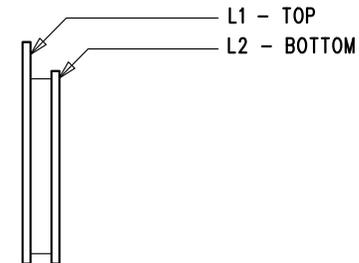


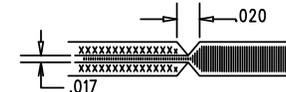
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	1	1ST PROTOTYPE	MARK T.	02-07-14

### LAYER STRUCTURE

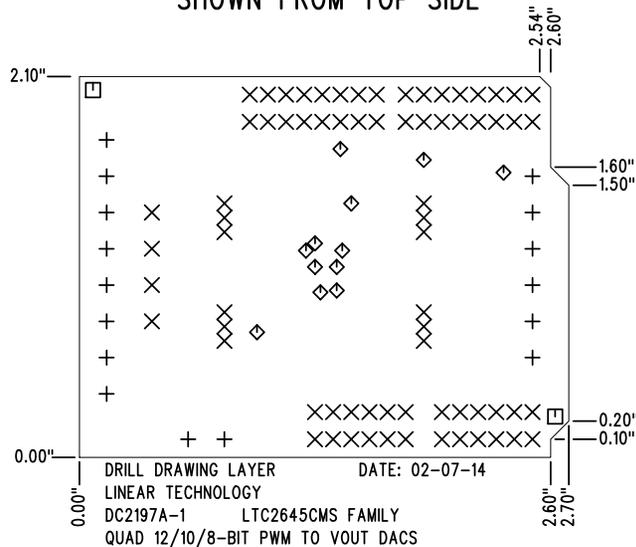


### NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR  
-FINISHED THICKNESS TO BE 0.062" +/- .005"  
-TOTAL OF 2 LAYERS WITH 2 OZ. CU ON EACH.  
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.  
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
-GOLD IMMERSION BOTH SIDES.  
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE. PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



### SHOWN FROM TOP SIDE



SIZE	QTY	SYM	PLATED	TOL
0.063	16	+	YES	+/-0.003"
0.035	72	X	YES	+/-0.003"
0.07	2	□	NO	+/-0.003"
0.012	12	◇	YES	+/-0.003"

UNLESS OTHERWISE SPECIFIED		APPROVALS		LINEAR TECHNOLOGY	
DIMENSIONS ARE IN INCHES		PCB DES.	KIM T.	1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 www.linear.com LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY	
TOLERANCES: 0.XX" = ±0.01" 0.XXX" = ±0.005"		APP ENG.	MARK T.		
INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION				TITLE: FABRICATION DRAWING	
				QUAD 12/10/8-BIT PWM TO VOUT DACS	
				SIZE	IC NO.
				N/A	LTC2645CMS-L12
					DEMO CIRCUIT 2197A
				REV	1
				SCALE = NONE	FILENAME: DC2197A-1.PCB
					SHT 1 OF 1