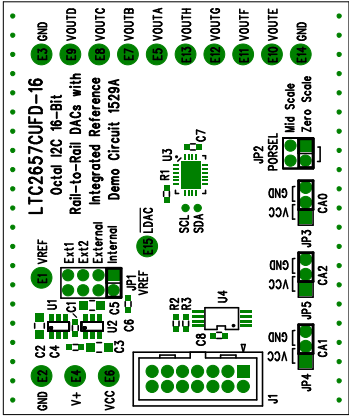


REVISIONS			
REV	DATE	APPR	DATE

NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.

Install Shunts on JP1 = Internal
 JP2 = Zero Scale
 JP3 = GND
 JP4 = GND
 JP5 = GND



Silkscreen Top

Assembly Drawing Top

APPROVALS			
DRAWN	INIT	DATE	
CHECK	kl		
DESIGN			
ENGR			
Title: Octal I2C 16-Bit Rail-to-Rail LTC2657CUFD-16, DACs with Integrated Reference			
Size: Demo Board: Demo Circuit 1529A Rev. A			
Design # Sht: 1 of 1			



LINEAR

TECHNOLOGY

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