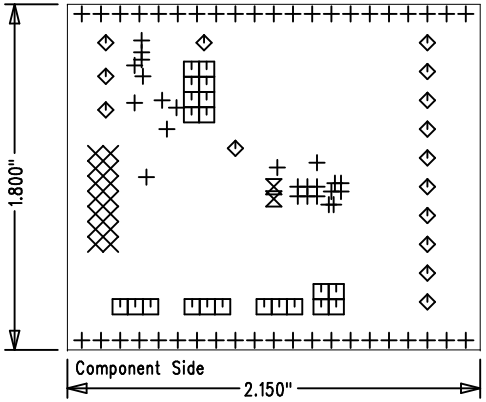
REVISIONS			
REV		APPR	DATE

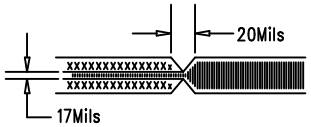
ART01 = LAYER 1 (COMPONENT SIDE)
ART02 = LAYER 2 (GND)
ART03 = LAYER 3 (GND)
ART04 = LAYER 4 (SOLDER SIDE)



SIZE	QTY	SYM	PLATED	TOL
10	66	+	YES	+/-0.0
35	14	X	YES	+/-0.0
40	21	□	YES	+/-0.0
65	15	◇	YES	+/-0.0
20	2	⧖	YES	+/-0.0

NOTES: Unless Otherwise Specified:

1. PCBS Are To Be RoHS Compliant
2. Fab Per IPC-A-600
3. Material: Epoxy Fiberglass, NEMA Grade FR-4, 4 Layers
   .062 +/- .005 Inch Thickness With
   2 OZ. Copper Finished On All Outer Layers,
   1 OZ. Copper On All Internal Layers.
   Flammability Rating: 94 V-2 Minimum.
4. Size: Cut To Dimensions And Tolerances Shown.
   0.00 Are Primary Datums.
5. Finish: SMOBC Using Wet Mask, Or LPI Both Sides, Color Green.
   Silkscreen Both Sides With White Non-conductive Ink.
   Gold Immersion Both Sides (Not Required For Prototype Boards)
6. Drilling: Drill Holes Per Schedule. Plate Through
   Holes With Copper, .001 Inch Thick Min. All
   Hold Sizes Are Specified After Plating.
   Hole Location Tolerances Are +/-.003" In Relation To Center.
7. Do Not Alter Artwork To Add Logo Or Date Code,
   May Modify Pad Size To Meet End Finish.
8. Scoring: (Not Required For Prototype Boards)



Fabrication Drawing

APPROVALS		
	INIT	DATE
DRAWN	kl	
CHECK		
DESIGN		
ENGR		
Scale = None		

LINEAR TECHNOLOGY
1630 McCarthy Blvd.
Milpitas, CA 95035
PH: (408)432-1900

Title: LTC2657CUFD-16, Octal I2C 16-Bit Rail-to-Rail DACs with Integrated Reference		
Size:	Demo Board: Demo Circuit 1529A	Rev. A
Design #		Sht: 1 of 1