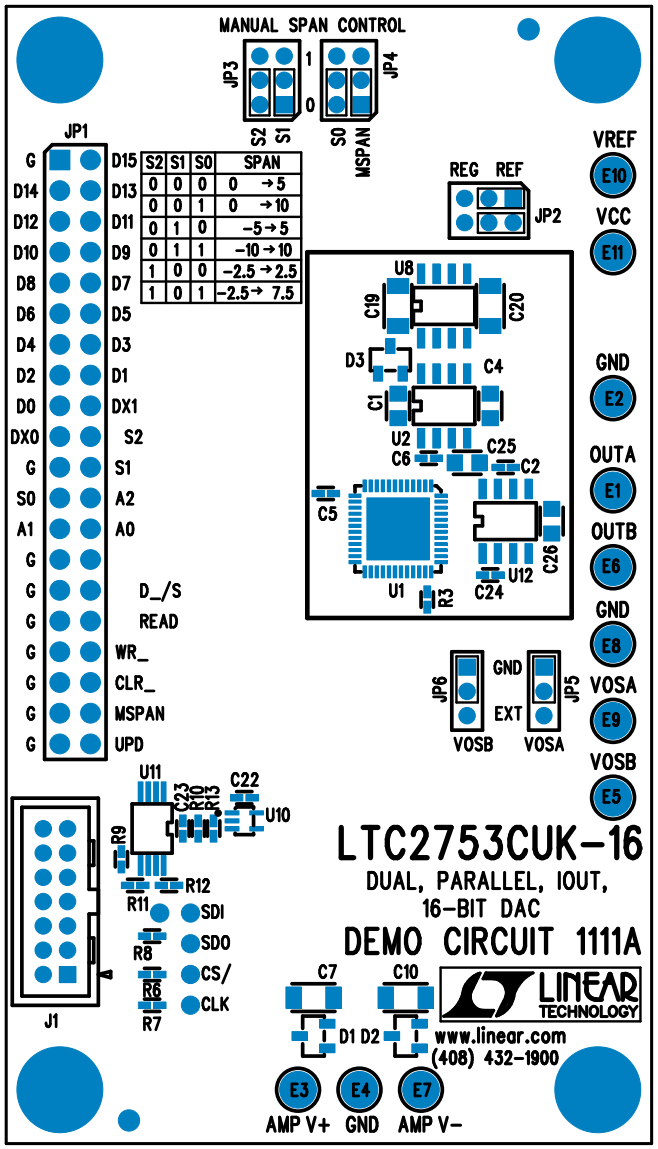
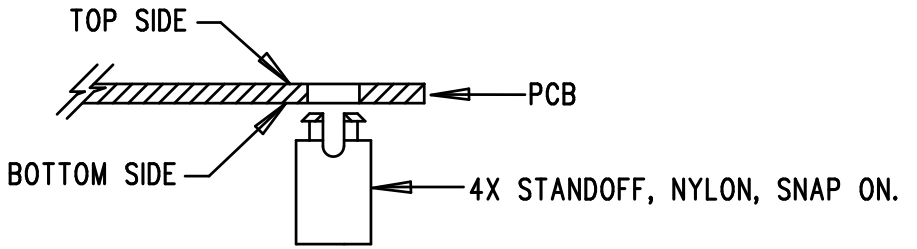


S2	S1	S0	SPAN
0	0	0	0 → 5
0	0	1	0 → 10
0	1	0	−5 → 5
0	1	1	−10 → 10
1	0	0	−2.5 → 2.5
1	0	1	−2.5 → 7.5

REVISIONS			
REV	DESCRIPTION	APPR	DATE
A-1	2ND PROTOTYPE	MT	09-24-06

NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. INSTALL SHUNTS ON JUMPERS AS SHOWN.
3. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
4. ASSY PROCESSES SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD.
5. INSTALL 4 STANDOFFS AT 4 CORNERS AS SHOWN BELOW:



APPROVALS		
	INIT	DATE
DRAWN		
CHECK		
DESIGN	KIM T.	09-24-06
ENGR	MARK T.	09-24-06

SCALE = NONE

LINEAR TECHNOLOGY
1630 MCCARTHY BLVD
MILPITAS, CA 95035
PH: (408)432-1900
LTC CONFIDENTIAL-
FOR CUSTOMER USE ONLY

TITLE: TOP ASSEMBLY DRAWING

DUAL, PARALLEL, IOUT, 16-BIT DAC

SIZE	DEMO	REV.
A	DC1111A-1 * LTC2753CUK-16	A-1

SHT 1 of 2