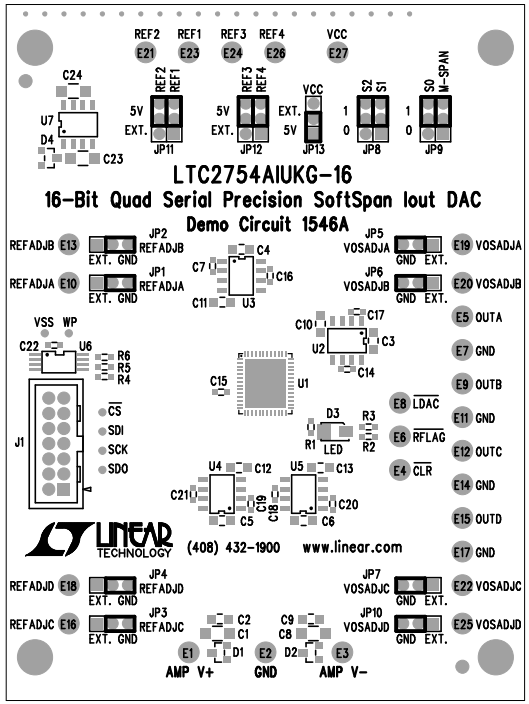
REVISIONS			
REV		APPR	DATE



Silkscreen Top

NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.

Install Shunts on JP1 = GND
JP2 = GND
JP3 = GND
JP4 = GND
JP5 = GND
JP6 = GND
JP7 = GND
JP8 = 1, S1, S2
JP9 = 1, S0, M-SPAN
JP10 = GND
JP11 = 5V, REF1, REF2
JP12 = 5V, REF3, REF4
JP13 = 5V

Assembly Drawing Top

APPROVALS		
	INIT	DATE
DRAWN	kl	
CHECK		
DESIGN		
ENGR		
Scale = None		

LINEAR TECHNOLOGY
1630 McCarthy Blvd.
Milpitas, CA 95035
PH: (408)432-1900

Title:	LTC2754AIUKG-16 16-Bit Quad Serial Precision SoftSpan Iout DAC	
Size:	Demo Circuit 1546A	Rev. A
Design #		Sht: 1 of 1