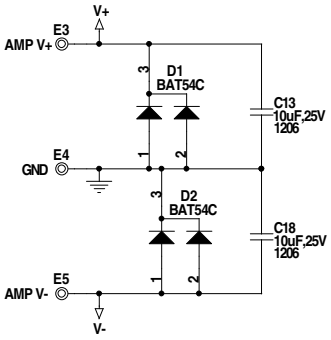
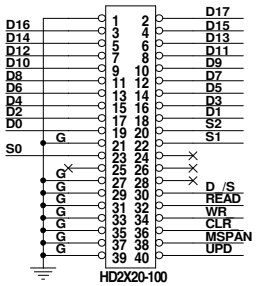
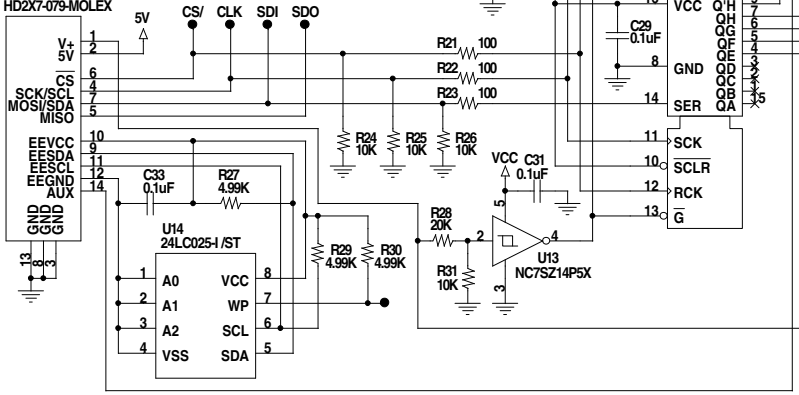
NOTES: UNLESS OTHERWISE SPECIFIED

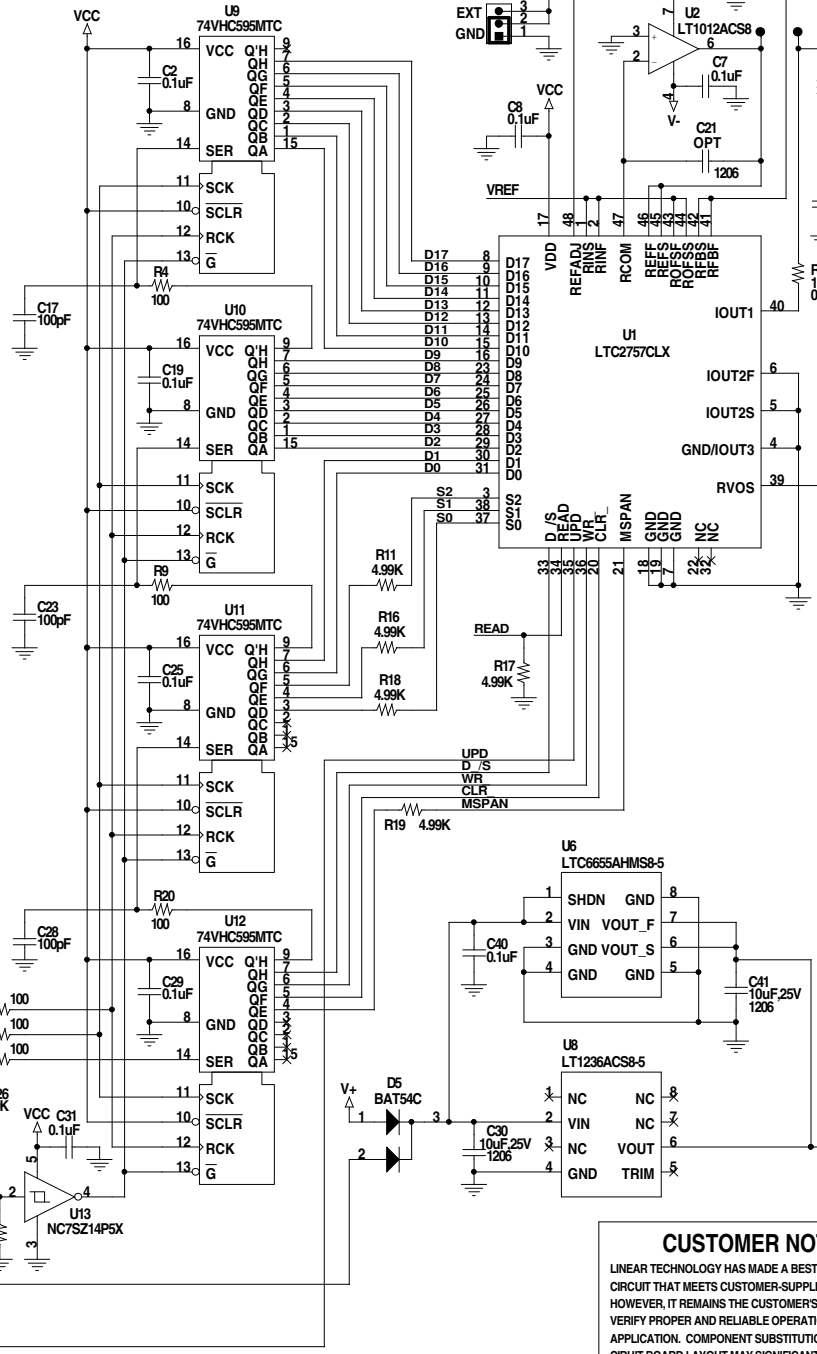
1. ALL RESISTORS ARE IN OHMS, 0402.
   ALL CAPACITORS ARE IN MICROFARADS, 0402.
2. INSTALL SHUNTS ON JUMPERS AS SHOWN.

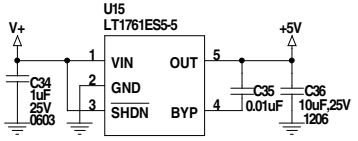


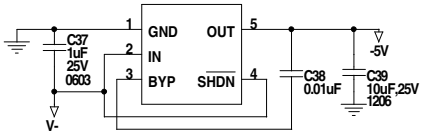
PARALLEL INTERFACE

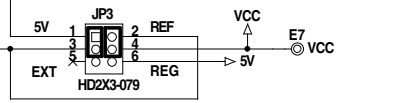




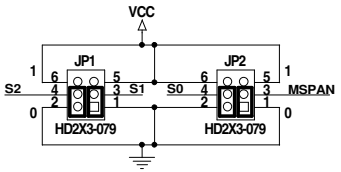








MANUAL SPAN CONTROL



S2	S1	S0	SPAN
0	0	0	0 → 5
0	0	1	0 → 10
0	1	0	-5 → 5
0	1	1	-10 → 10
1	0	0	-2.5 → 2.5
1	0	1	-2.5 → 7.5

CUSTOMER NOTICE

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

CONTRACT NO.	
APPROVALS	
DRAWN:	
CHECKED:	
APPROVED:	
ENGINEER: LEO C.	
DESIGNER: KIM T.	

LINEAR TECHNOLOGY
1630 McCarthy Blvd.
Milpitas, CA 95035
Phone: (408)432-1900
Fax: (408)434-0507
LTC Confidential-For Customer Use Only

TITLE: SCHEMATIC

18-BIT PARALLEL INPUT, CURRENT OUTPUT DAC WITH READBACK

SIZE	DWG NO.	REV
A	DC1485A-2 * LTC2757CLX	2
DATE: Wednesday, July 29, 2009		SHEET 1 OF 1