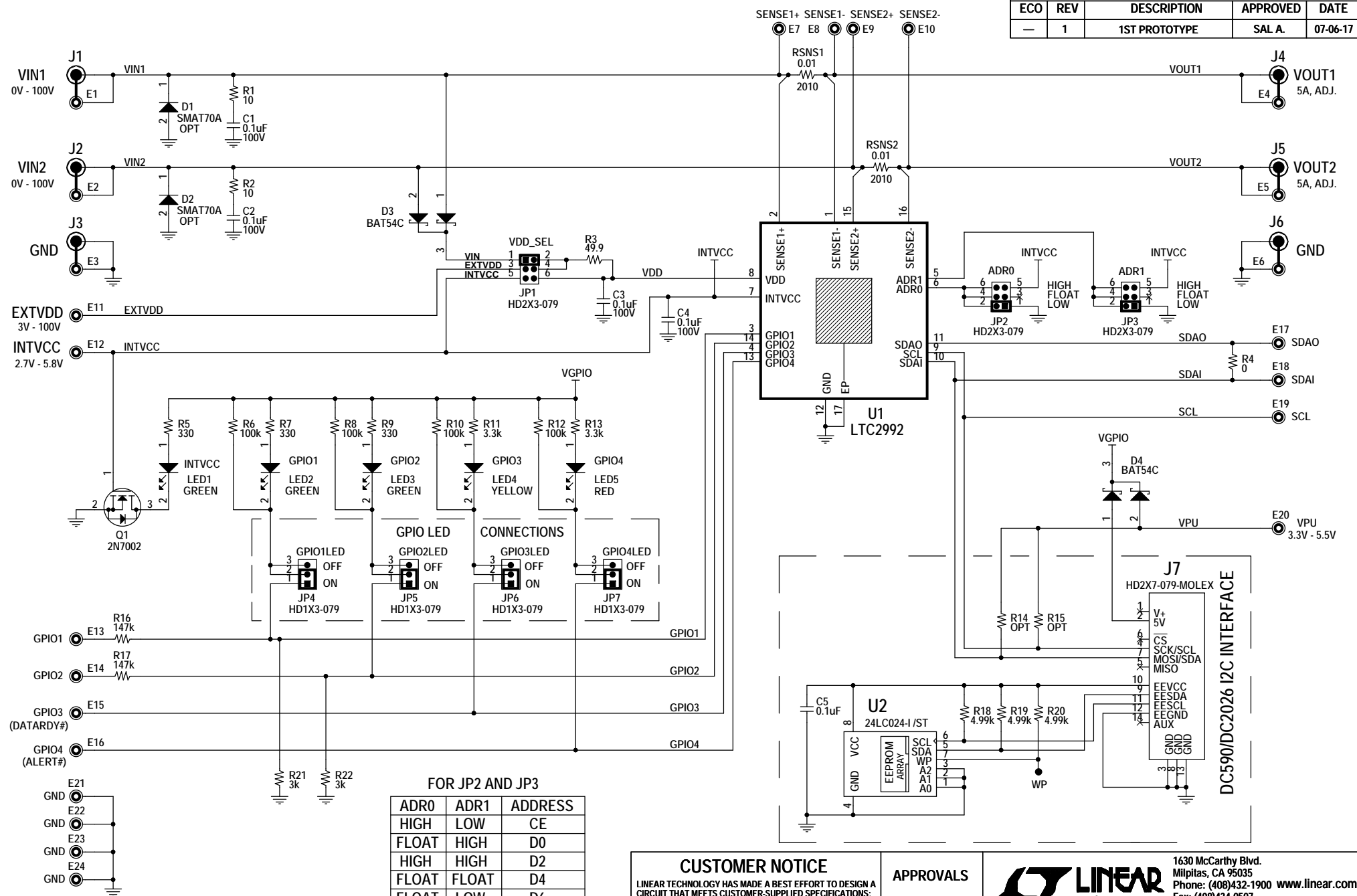


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	1	1ST PROTOTYPE	SAL A.	07-06-17



FOR JP2 AND JP3

ADR0	ADR1	ADDRESS
HIGH	LOW	CE
FLOAT	HIGH	D0
HIGH	HIGH	D2
FLOAT	FLOAT	D4
FLOAT	LOW	D6
LOW	HIGH	D8
HIGH	FLOAT	DA
LOW	FLOAT	DC
LOW	LOW	DE *

* DEFAULT

NOTES: UNLESS OTHERWISE SPECIFIED
 1. ALL RESISTORS ARE IN OHMS, 0603.
 ALL CAPACITORS ARE IN MICROFARADS, 1206.
 2. INSTALL SHUNTS AS SHOWN.

CUSTOMER NOTICE LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.		APPROVALS PCB DES. KIM T. APP ENG. SAL A.		 1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 www.linear.com Fax: (408)434-0507 LTC Confidential-For Customer Use Only	
THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.		SCALE = NONE			
TITLE: SCHEMATIC		SIZE: N/A		IC NO. LTC2992IDE	
DATE: Thursday, July 06, 2017		REV. 1		SHEET 1 OF 1	



DUAL WIDE RANGE I2C POWER MONITOR
DEMO CIRCUIT 2561A