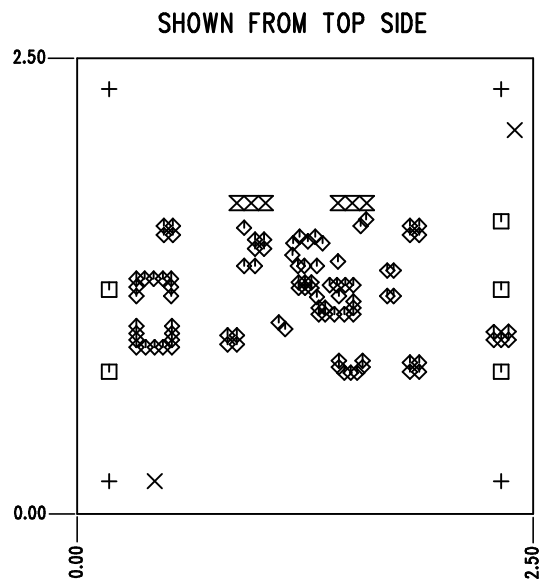
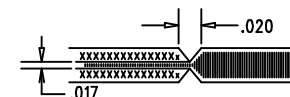


| REVISION HISTORY |     |             |           |          |
|------------------|-----|-------------|-----------|----------|
| ECO              | REV | DESCRIPTION | APP. ENG. | DATE     |
| -                | 1   | PRODUCTION  | JIM N.    | 08-10-11 |

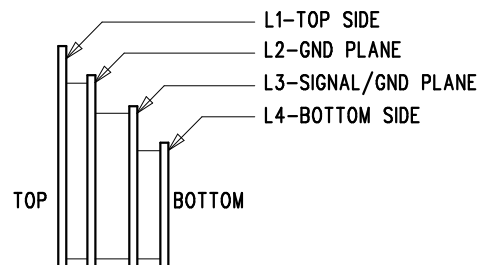


## NOTES: UNLESS OTHERWISE SPECIFIED

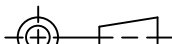

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.  
-FINISHED THICKNESS TO BE 0.062+/- .005  
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.  
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.  
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
-GOLD IMMERSION BOTH SIDES.  
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.  
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



## LAYER STRUCTURE



| SIZE  | QTY | SYM | PLATED | TOL      |
|-------|-----|-----|--------|----------|
| 0.19  | 4   | +   | YES    | +/-0.003 |
| 0.07  | 2   | X   | NO     | +/-0.003 |
| 0.094 | 5   | □   | YES    | +/-0.003 |
| 0.01  | 94  | ◇   | YES    | +/-0.003 |
| 0.035 | 6   | ⊗   | YES    | +/-0.003 |

|   |   |        |   |            |
|---|---|--------|---|------------|
| UNLESS OTHERWISE SPECIFIED<br><br>DIMENSIONS ARE IN INCHES<br>TOLERANCES:<br>0.XX" = ±0.01"<br>0.XXX" = ±0.005"<br><br>INTERPRET DIM AND TOL<br>PER ASME Y14.5M-1994<br>THIRD ANGLE PROJECTION<br><br> | APPROVALS   |        |  <b>LINEAR<br/>TECHNOLOGY</b><br><br>1830 MCCARTHY BLVD<br>MILPITAS, CA 95035<br>PH: (408)432-1900<br>www.linear.com<br>LTC CONFIDENTIAL-<br>FOR CUSTOMER USE ONLY |            |
|   | PCB DES.  | LT     |   |            |
|   | APP ENG.  | JIM N. |   |            |
|   |   |        |   |            |
|   |   |        |   |            |
|   | TITLE: FABRICATION DRAWING<br>15V, 300mA, SYNCHRONOUS STEP-DOWN DC/DC<br>CONVERTER WITH 1.8uA QUIESCENT CURRENT |        |   |            |
|   | SIZE  | IC NO. | LTC3103EDD  | REV        |
|   | N/A   |        | DEMO CIRCUIT 1883A  | 1          |
|   | SCALE = NONE  |        | FILENAME: DC1883A-1.PCB   | SHT 1 OF 1 |