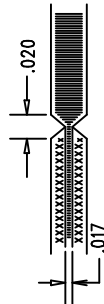




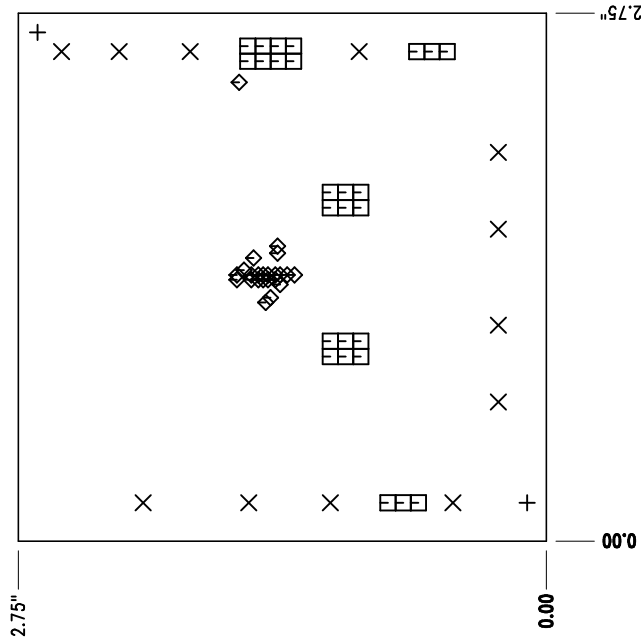
The diagram shows a cross-section of a multi-layer printed circuit board (PCB). It consists of several layers: a top copper layer labeled 'Top Side', followed by an insulating core layer labeled 'Inner Layer 2', another insulating core layer labeled 'Inner Layer 3', and a bottom copper layer labeled 'Bottom Side'. Arrows point from the text labels to their corresponding layers in the cross-section.

NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL: --EPOXY FIBERGLASS, NEMA GRADE FR-4
--FINISHED THICKNESS TO BE 0.062" +/- .005"
--TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS
AND 1 OZ. CU ON THE INNER LAYERS.
--FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
4. DRILLING: --DRILL HOLES PER SCHEDULE. PLATE THROUGH
HOLES WITH COPPER, 0.001" THICK MIN.
--ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
--HOLE LOCATION TOLERANCES ARE +/-0.003"
IN RELATION TO CENTER
5. FINISH: --SMOBC USING LPI BOTH SIDES, COLOR GREEN.
--GOLD IMMERSION BOTH SIDES.
(LEAD FREE SOLDER CAN BE USED FOR PROTOTYPE)
--FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs ARE TO BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCB:



UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES: 0.XX" = $\pm 0.01"$ 0.XXX" = $\pm 0.005"$ INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION	APPROVALS		 LINEAR TECHNOLOGY 1630 HOCARTWAY BLVD MILPITAS, CA 95035 PH: (408)432-9000 www.linear.com LTC CONFIDENTIAL - FOR CUSTOMER USE ONLY
	PCB DES.	JW	
	APP ENG.	FRANK H.	
	TITLE: FABRICATION DRAWING VERY LOW VIN STEP-UP CONVERTER WITH MAXIMUM POWER POINT CONTROL AND LDO		
	SIZE	IC NO.	REV
	N/A	LTC3105EDD	2
	FILENAME: 1587A.PCB		SHT 1 OF 1



SIZE	QTY	SYM	PLATED	TOL
0.07	2	+	NO	+/-0.003
0.094	12	X	YES	+/-0.003
0.04	26	□	YES	+/-0.003
0.01	23	◇	YES	+/-0.003