



REVISIONS			
REV	DESCRIPTION	APPR	DATE
A	FINAL FABRICATION	JZ	06-28-06

NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. INSTALL SHUNT ON JP1 PIN 1 AND 2, JP2 PIN 1&3, 2&4 AS SHOWN .
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILL OF MATERIALS. LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER. MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
5. ASSY PROCESSES SHALL INCLUDE: REFLOW SOLDER TOP SIDE SMD.
6. FOR BOARD ASSEMBLY PROCESS, SEE TABLE BELOW:

*	U1	JP2	C8	R1	R3	R4	R6	R7	R8	VOUT
DC863A-A	LTC3203BEDD	OPT	4.7PF	267K	280K	105K	0	OPT		3.3V@500mA
DC863A-B	LTC3203EDD-1	INSTALLED	OPT	OPT	OPT	3.32M	OPT	402K	357K	4.5V OR 5V@500mA BURST MODE
DC863A-C	LTC3203BEDD-1	INSTALLED	OPT	OPT	OPT	3.32M	OPT	402K	357K	4.5V OR 5V@500mA

APPROVALS

	INIT	DATE
DRAWN		
CHECK		
DESIGN	KIM T.	06-28-06
ENGR	JULIAN Z.	06-28-06

SCALE = NONE



LINEAR
TECHNOLOGY

1630 MCCARTHY BLVD
MILPITAS, CA 95035
PH: (408)432-1900
LTC CONFIDENTIAL-
FOR CUSTOMER USE ONLY

TITLE: TOP ASSEMBLY DRAWING

500mA LOW NOISE, HIGH EFFICIENCY CHARGE PUMP

SIZE A DEMO DC863A-LTC3203 REV. A

SHT 1 of 2