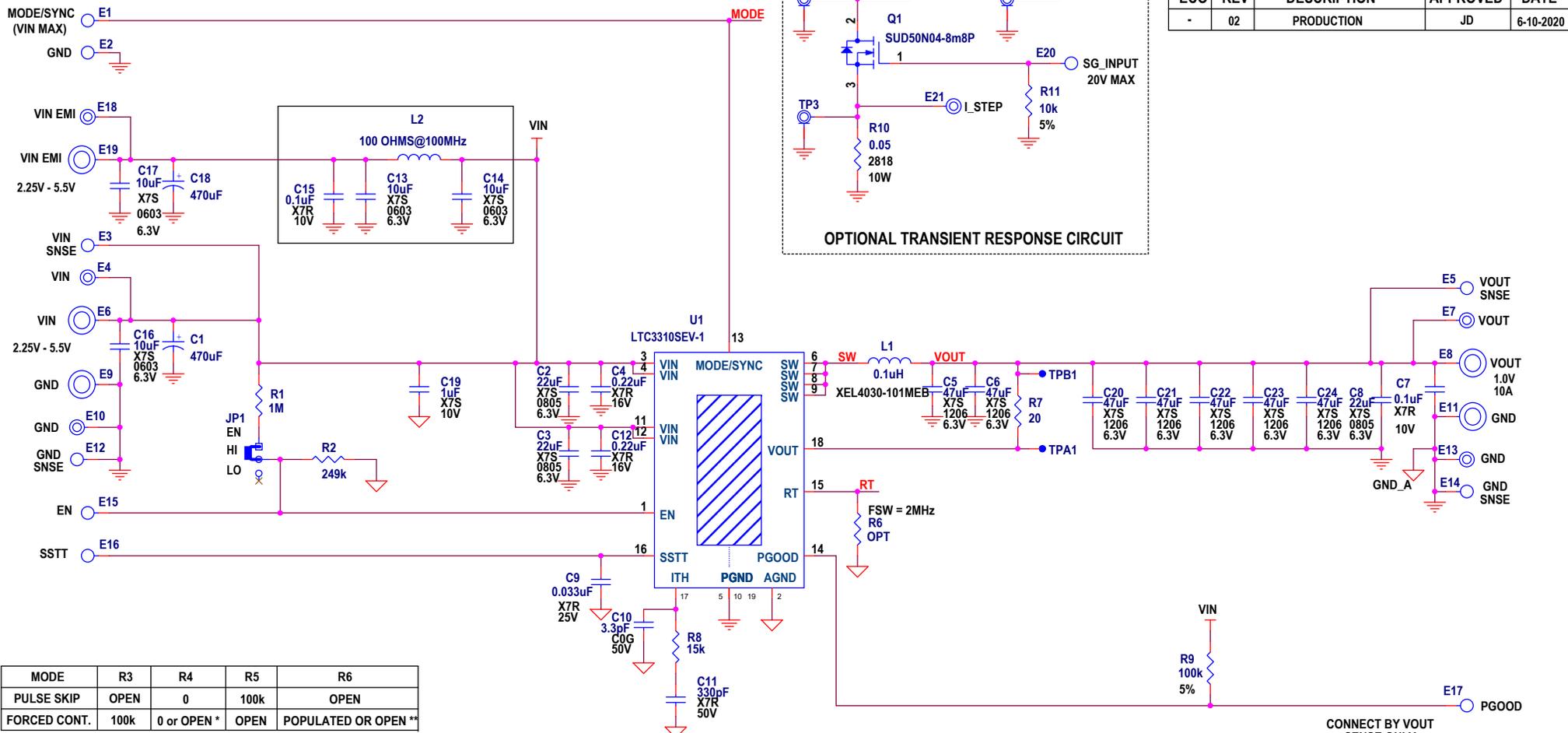
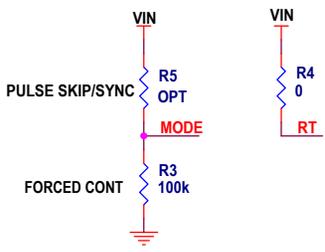


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	02	PRODUCTION	JD	6-10-2020



MODE	R3	R4	R5	R6
PULSE SKIP	OPEN	0	100k	OPEN
FORCED CONT.	100k	0 or OPEN *	OPEN	POPULATED OR OPEN **

* IF R6 IS POPULATED R4 MUST BE OPEN.
 ** MODE/SYNC PIN IS CLOCK OUTPUT WHEN R6 IS POPULATED.



NOTES: UNLESS OTHERWISE SPECIFIED
 1. RESISTORS: OHMS, 0402, 1%, 1/16W
 2. CAPACITORS: 0402

PCA ADDITIONAL PARTS	
MP1	STANDOFF,NYLON,SNAP-ON,0.50"
MP2	STANDOFF,NYLON,SNAP-ON,0.50"
MP3	STANDOFF,NYLON,SNAP-ON,0.50"
MP4	STANDOFF,NYLON,SNAP-ON,0.50"
LB1	LABEL
PCB1	PCB,DC3021A REV02

CUSTOMER NOTICE
 LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

APPROVALS	
PCB DES.	NC
APP ENG.	JD
IC NO. LTC3310S-1	
SKU NO.	DC3021A

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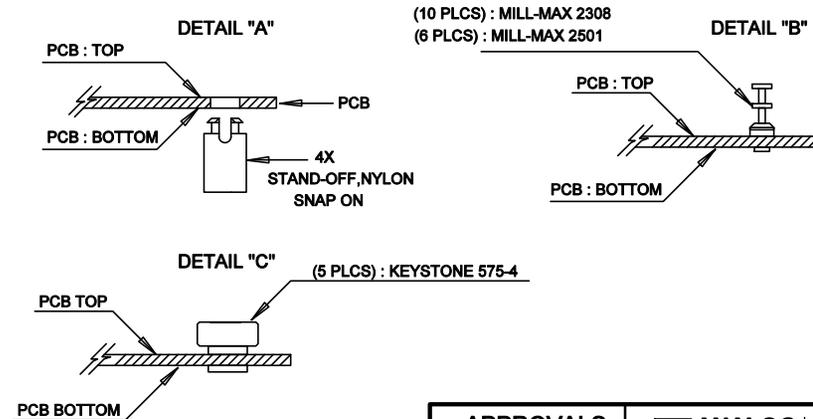
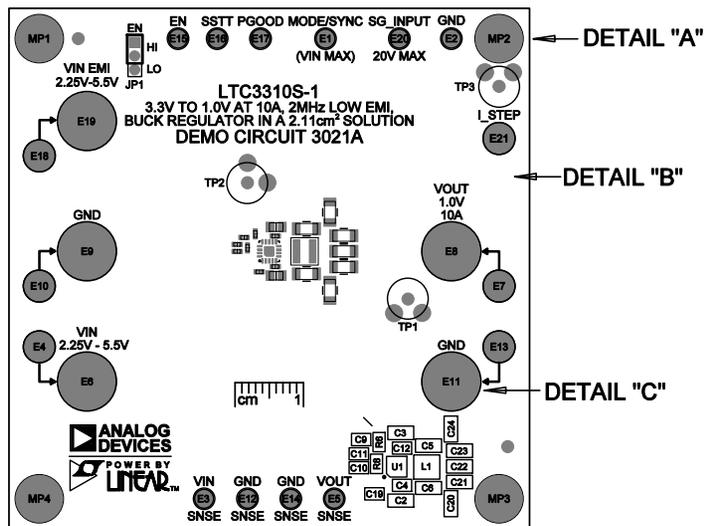
**TITLE: DEMO CIRCUIT SCHEMATIC,
 3.3V TO 1.0V AT 10A, 2MHz LOW EMI,
 BUCK REGULATOR IN A 2.11cm² SOLUTION**

SIZE:	PCA ASSY DWG:	SCHMATIC NO. AND REVISION:
SCALE = NONE	705-DC3021A_REV02	710-DC3021A_REV02
DATE: Monday, June 15, 2020		SHEET 1 OF 1

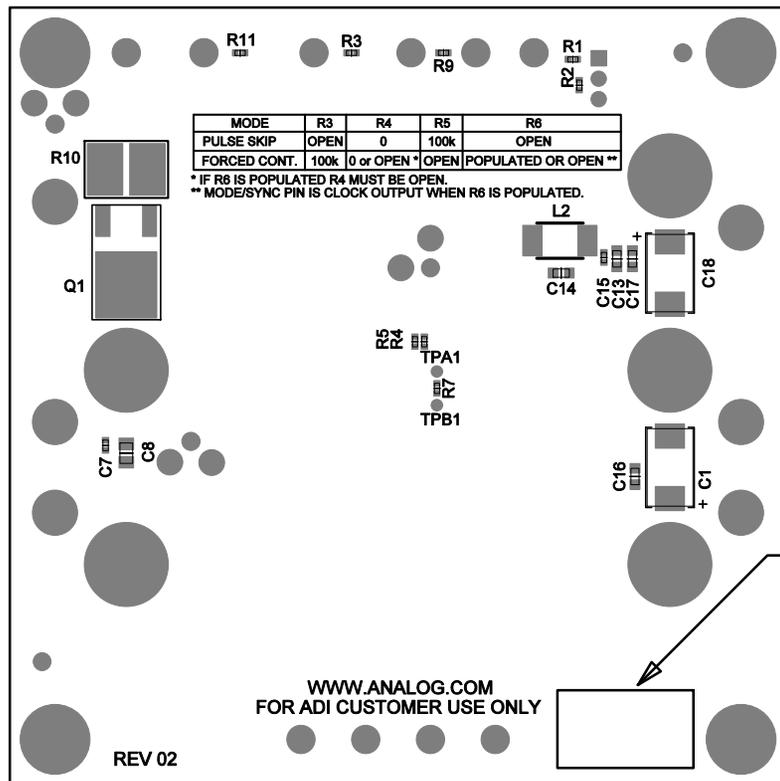
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	02	PRODUCTION	JD	6/10/2020

NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. ASSEMBLY REFLOW PROFILE SHALL BE IN ACCORDANCE WITH J-STD-020 WITH MAXIMUM SOLDER TEMPERATURE OF 250 DEGREES CELSIUS.
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILL OF MATERIALS
LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER.
MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. INSTALL SHUNTS AS SHOWN ON ASSY DRAWING.
5. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
6. APPLY ASSEMBLY STAMP OR QA STAMP TO BOTTOM OF BOARD (UNSHOWY AREA).
7. INSTALL TURRETS, STAND-OFFS AS SHOWN BELOW:
8. APPLY DEMO S/N AT AREA ON BOTTOM SIDE AS SHOWN ON SHEET 2.



APPROVALS		ANALOG DEVICES		POWER BY LINEAR™	
PCB DES	NC	FOR ADI CUSTOMER USE ONLY			
APP ENG	JD	TITLE: TOP ASSEMBLY DRAWING 3.3V TO 1.0V AT 10A, 2MHz LOW EMI, BUCK REGULATOR IN A 2.11cm² SOLUTION			
		SIZE	IC NO.	LTC3310S-1	REV.
		N/A		DEMO CIRCUIT 3021A	02
SCALE = NONE					SHT 1 OF 1

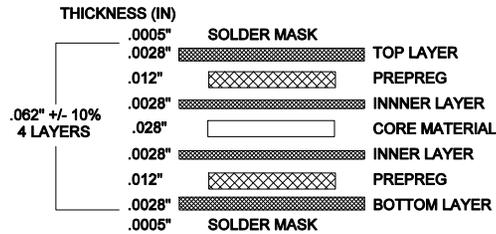


8 DEMO S/N LABEL
APPLY IN THIS AREA

APPROVALS		  FOR ADI CUSTOMER USE ONLY									
PCB DES.	NC	TITLE: BOTTOM ASSEMBLY DRAWING 3.3V TO 1.0V AT 10A, 2MHz LOW EMI, BUCK REGULATOR IN A 2.11cm ² SOLUTION									
APP ENG.	JD										
		<table border="1" data-bbox="1354 1404 1890 1485"> <tr> <td>SIZE</td> <td>IC NO.</td> <td>LTC3310S-1</td> <td>REV</td> </tr> <tr> <td>N/A</td> <td></td> <td>DEMO CIRCUIT 3021A</td> <td>02</td> </tr> </table>	SIZE	IC NO.	LTC3310S-1	REV	N/A		DEMO CIRCUIT 3021A	02	
SIZE	IC NO.	LTC3310S-1	REV								
N/A		DEMO CIRCUIT 3021A	02								
SCALE = NONE		SHT 2 of 2									

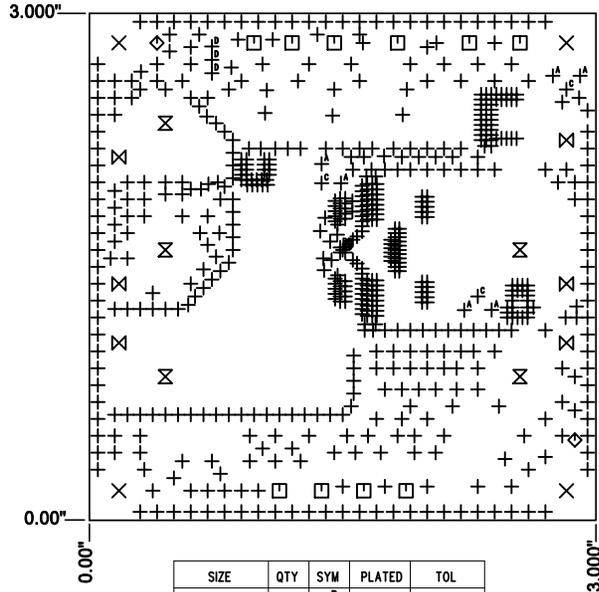
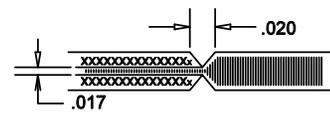
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	02	PRODUCTION	JD	6/10/2020

LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

- FABRICATION:** FABRICATION AND ACCEPTANCE TO MEET THE REQUIREMENTS OF IPC-A-600 AND IPC/ANSI-MLL950-C CLASS 2, AOL .25 GENERAL INSPECTION LEVEL II.
- MATERIAL:** - EPOXY FIBERGLASS, NEMA GRADE FR-4
 - FINISHED THICKNESS TO BE 0.062" +/- .005"
 - TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE ALL LAYERS
 - FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE:** CUT TO DIMENSIONS AND TOLERANCES SHOWN 0.00" ARE PRIMARY DATUMS.
- DRILLING:** - DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
 - ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
 - HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER.
- FINISH:** - SMOBC USING LPI BOTH SIDES, COLOR GREEN. TENT BOTH SIDES.
 - GOLD IMMERSION BOTH SIDES.
 - FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK** e.g. TO ADD LOGO OR DATE CODE. PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.**
- SCORING FOR PANELIZED PCB:**



SIZE	QTY	SYM	PLATED	TOL
0.011	5	⊖ ^B	YES	+/-0.003
0.012	620	+	YES	+/-0.003
0.04	3	⊖ ^D	YES	+/-0.003"
0.04134	3	⊖ ^E	YES	+/-0.003"
0.04724	6	⊖ ^A	YES	+/-0.003"
0.065	10	□	YES	+/-0.003"
0.07	2	◇	NO	+/-0.003"
0.095	6	⊗	YES	+/-0.003"
0.19	4	⊗	YES	+/-0.003"
0.205	5	⊗	YES	+/-0.003

ADDITIONAL REQUIREMENT FOR PROTOTYPE FAB ONLY:

- OUTGOING INSPECTION REPORT (BASED ON ACTUAL MEASUREMENTS AND CROSS SECTION).

ADDITIONAL REQUIREMENTS FOR PRODUCTION FAB ONLY:

- PROVIDE COMPLIANCE CERTIFICATES FOR RoHS, REACH AND CONFLICT-FREE MINERALS.
- SOLDERABILITY BOARD WITH TEST RESULTS.
- OUTGOING INSPECTION REPORT (BASED ON ACTUAL MEASUREMENTS AND CROSS SECTION).
- VACUUM PACKED WITH DESICCANT.
- FULL PANEL WITH NO REJECT.

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES

TOLERANCES:
0.XX" = ±0.01"
0.XXX" = ±0.005"

INTERPRET DIM AND TOL
PER ASME Y14.5M-1994
THIRD ANGLE PROJECTION

APPROVALS	
PCB DES	NC
APP ENG	JD
SCALE = NONE	

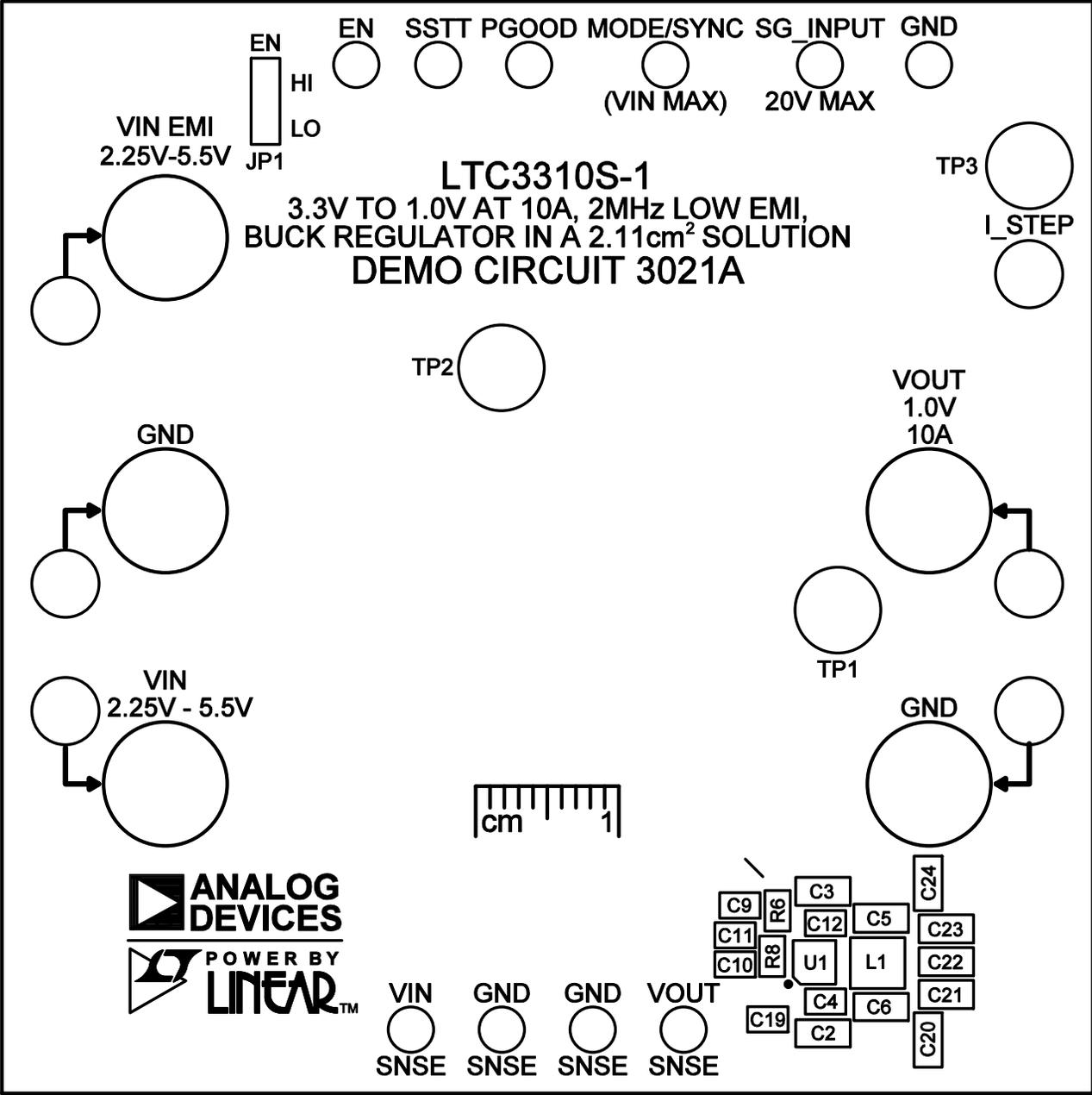
ANALOG DEVICES | **POWER BY LINEAR™**
FOR ADI CUSTOMER USE ONLY

TITLE: FABRICATION DRAWING
3.3V TO 1.0V AT 10A, 2MHz LOW EMI,
BUCK REGULATOR IN A 2.11cm² SOLUTION

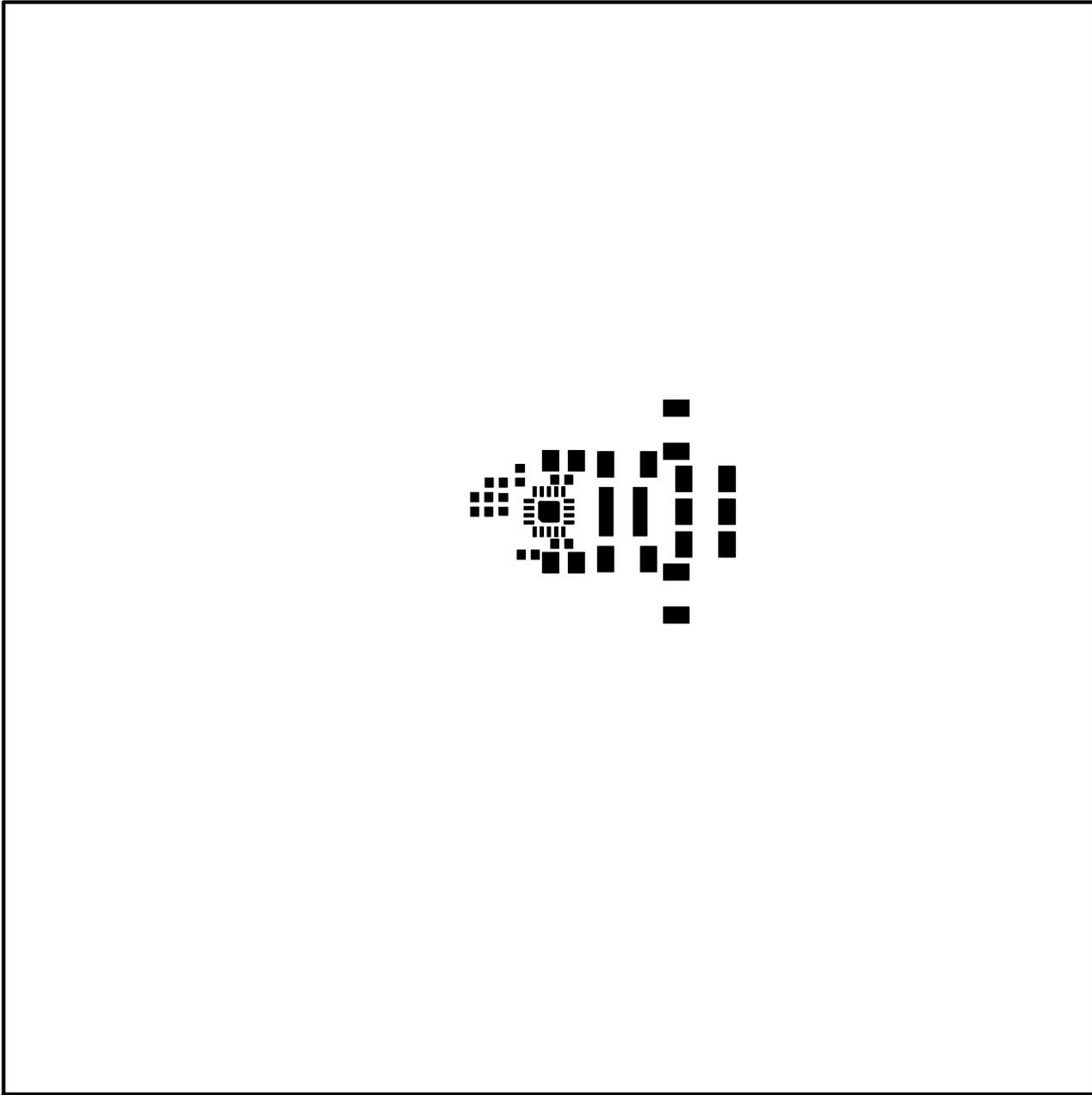
SIZE	IC NO. LTC3310S-1	REV
N/A	DEMO CIRCUIT 3021A	02

SCALE = NONE

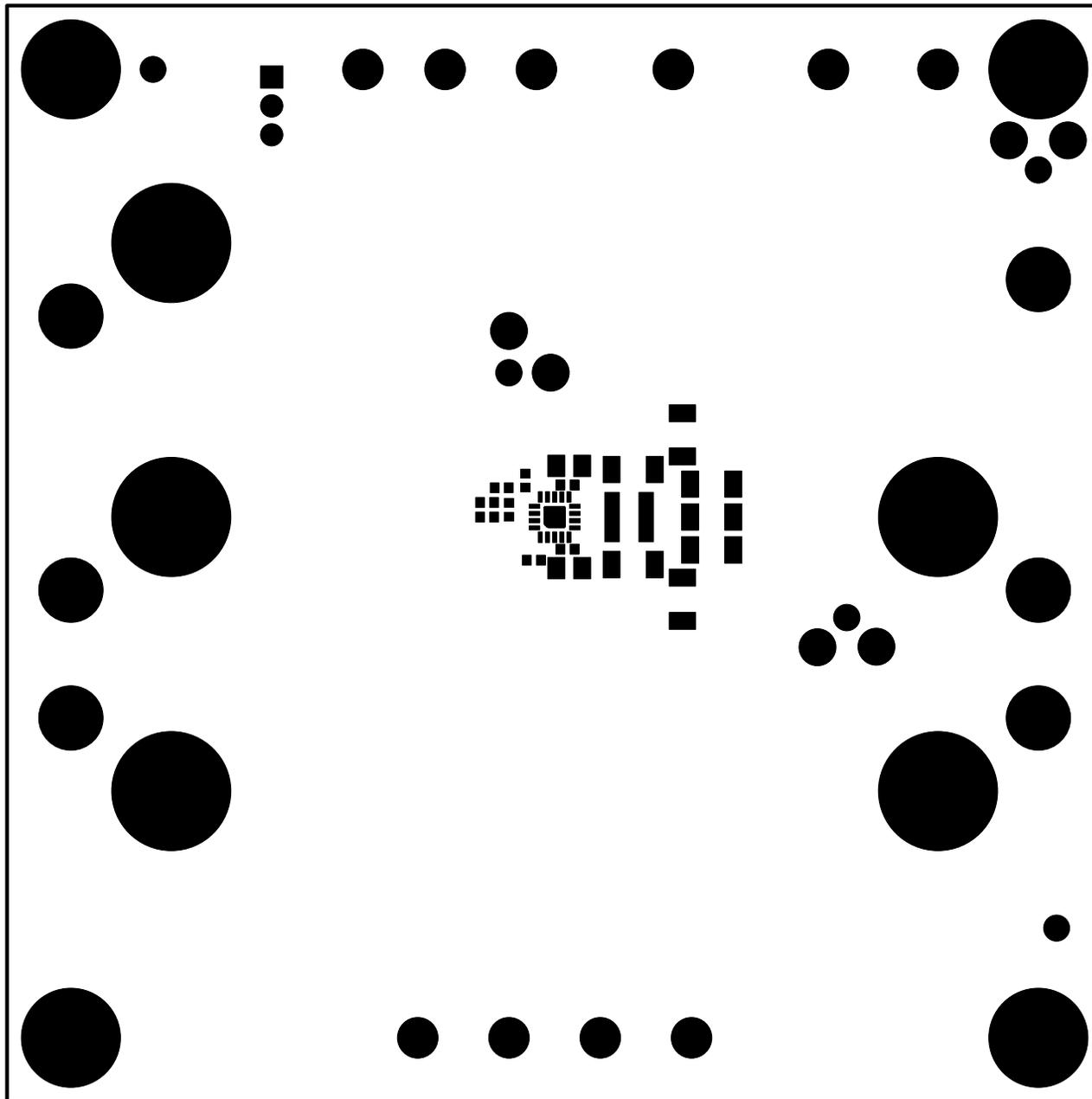
SHT 1 OF 1



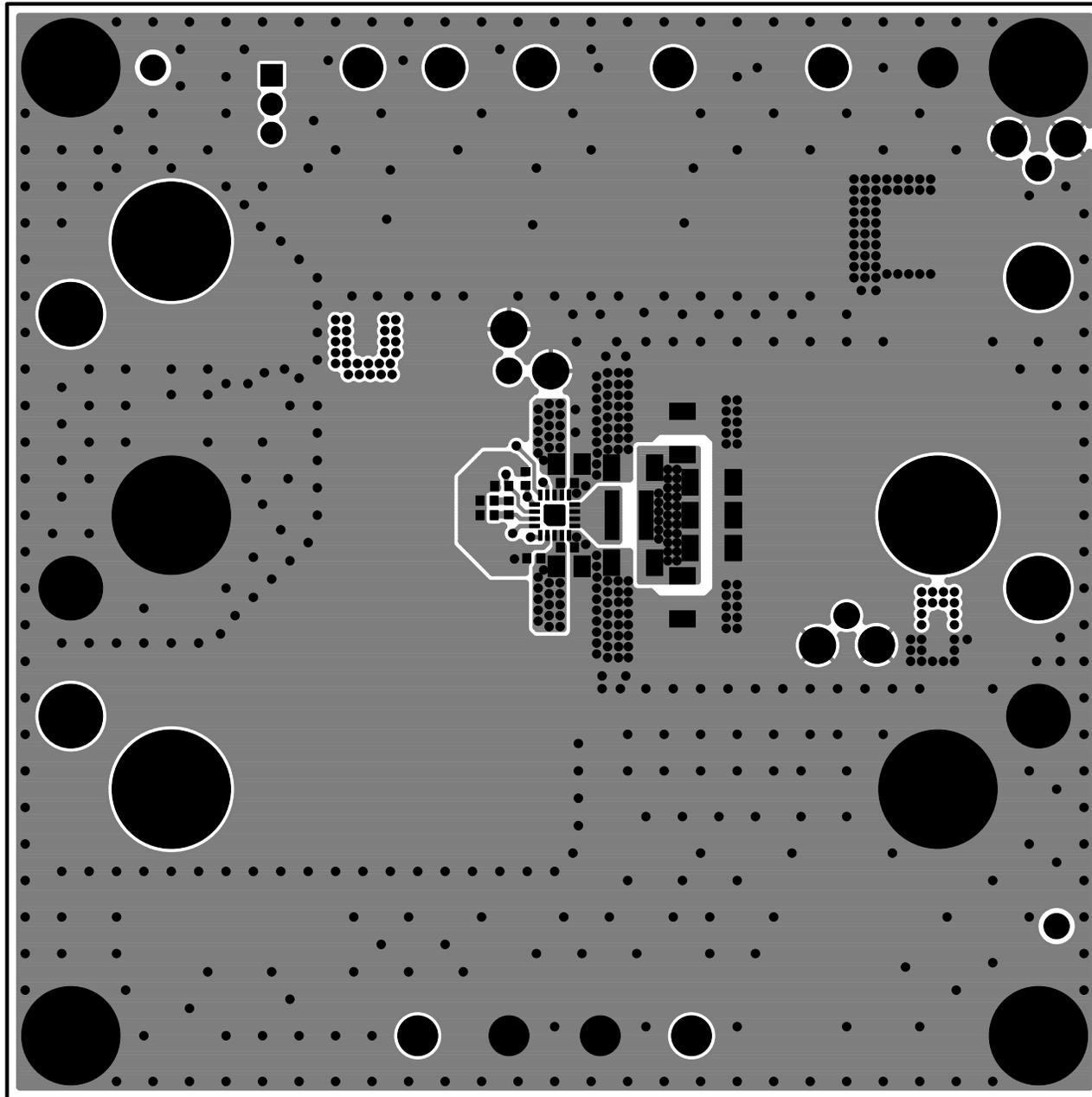
TOP SILKSCREEN
DC3021A_REV02



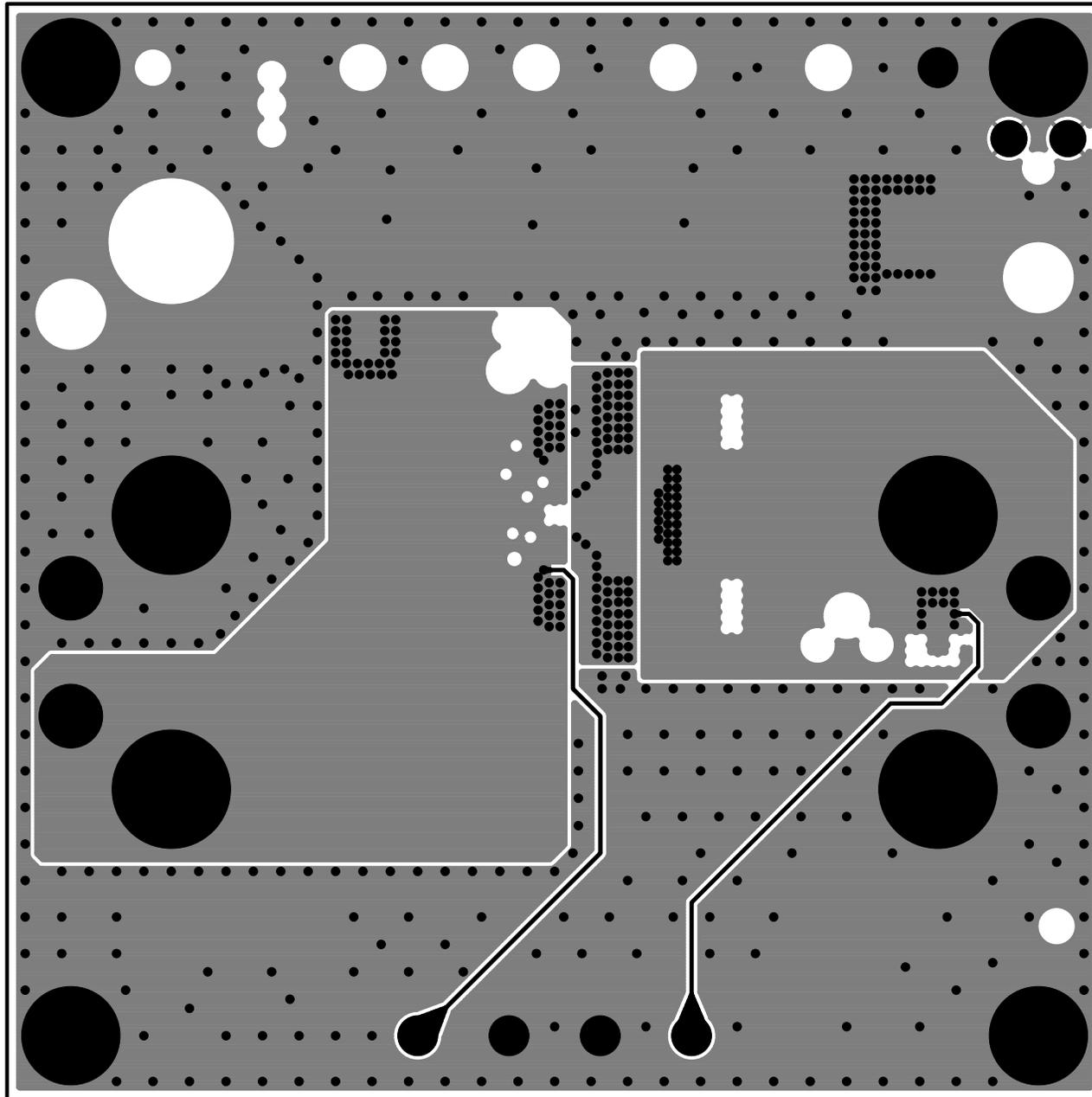
TOP SOLDER PASTE
DC3021A_REV02



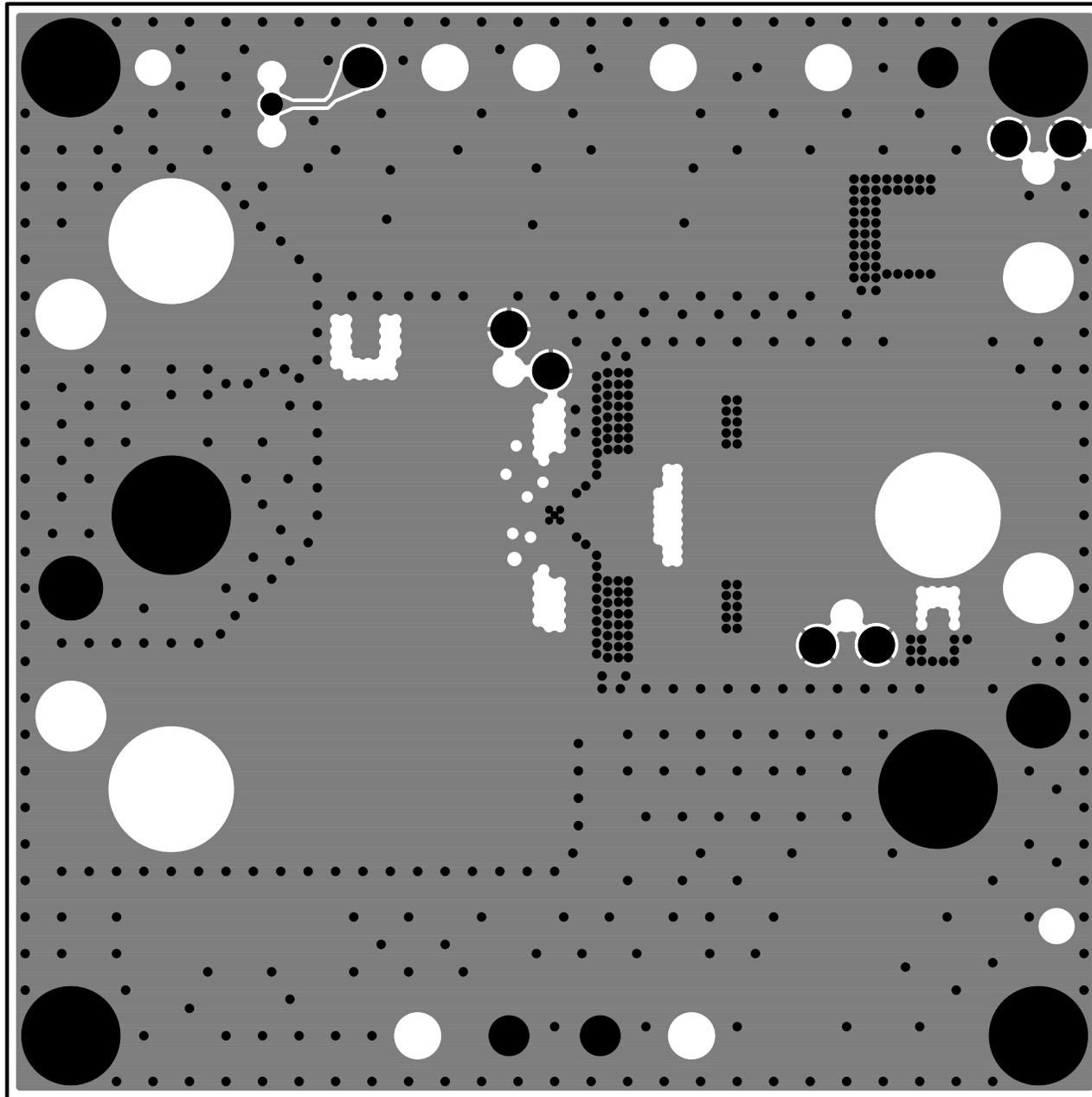
TOP SOLDER MASK
DC3021A_REV02



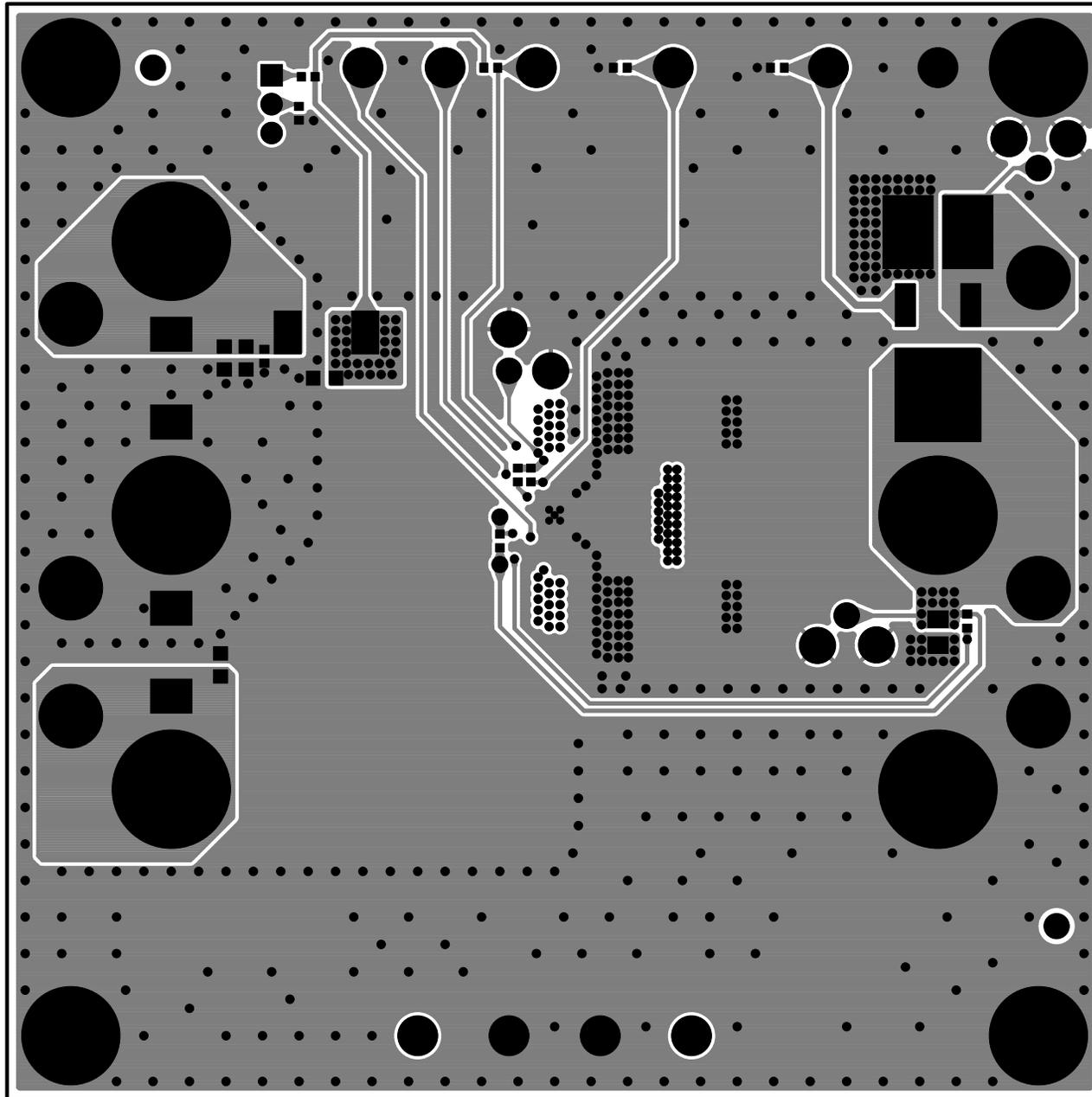
LAYER 1 : TOP LAYER
DC3021A_REV02



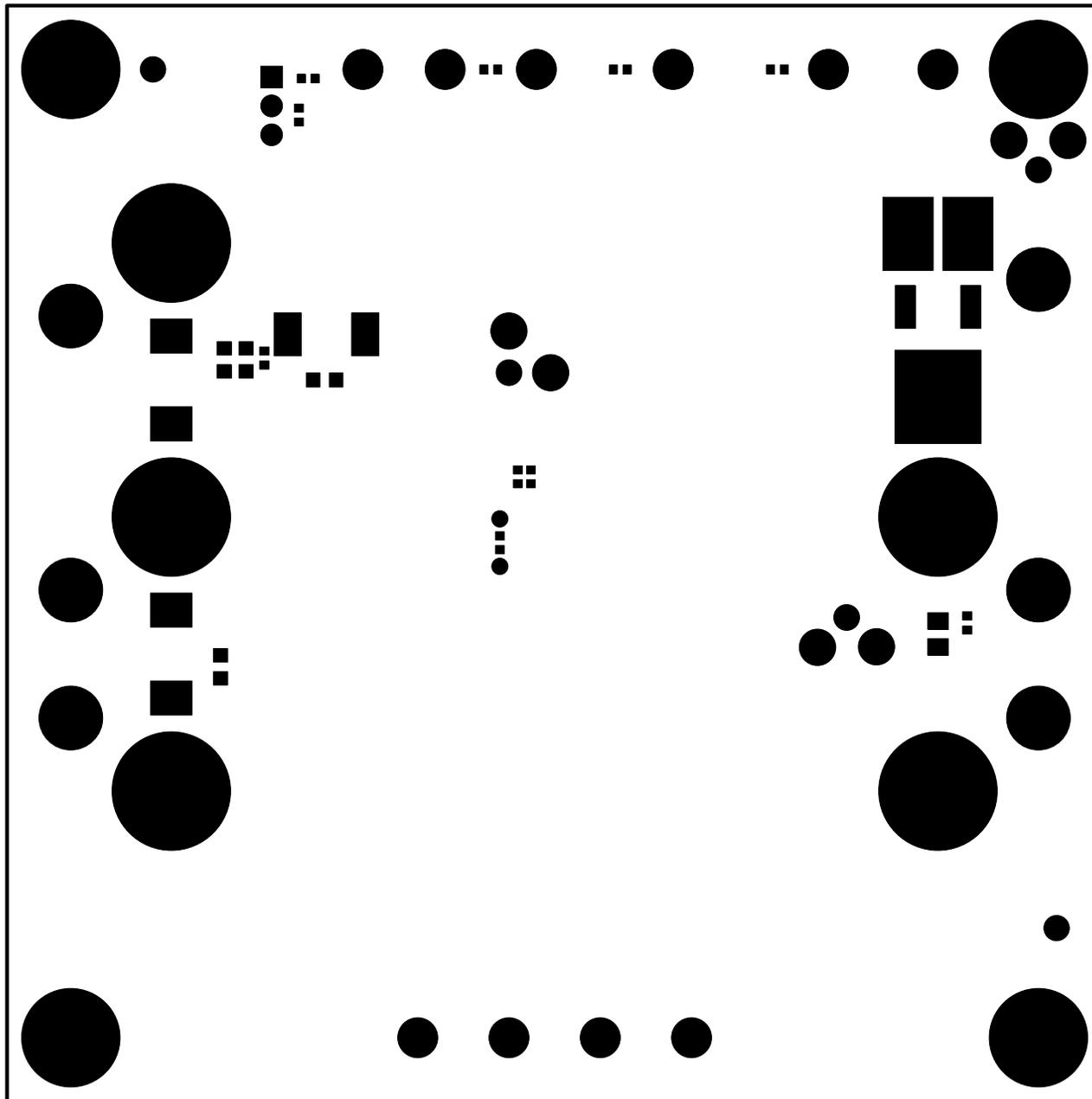
LAYER 2 : INNER LAYER1
DC3021A_REV02



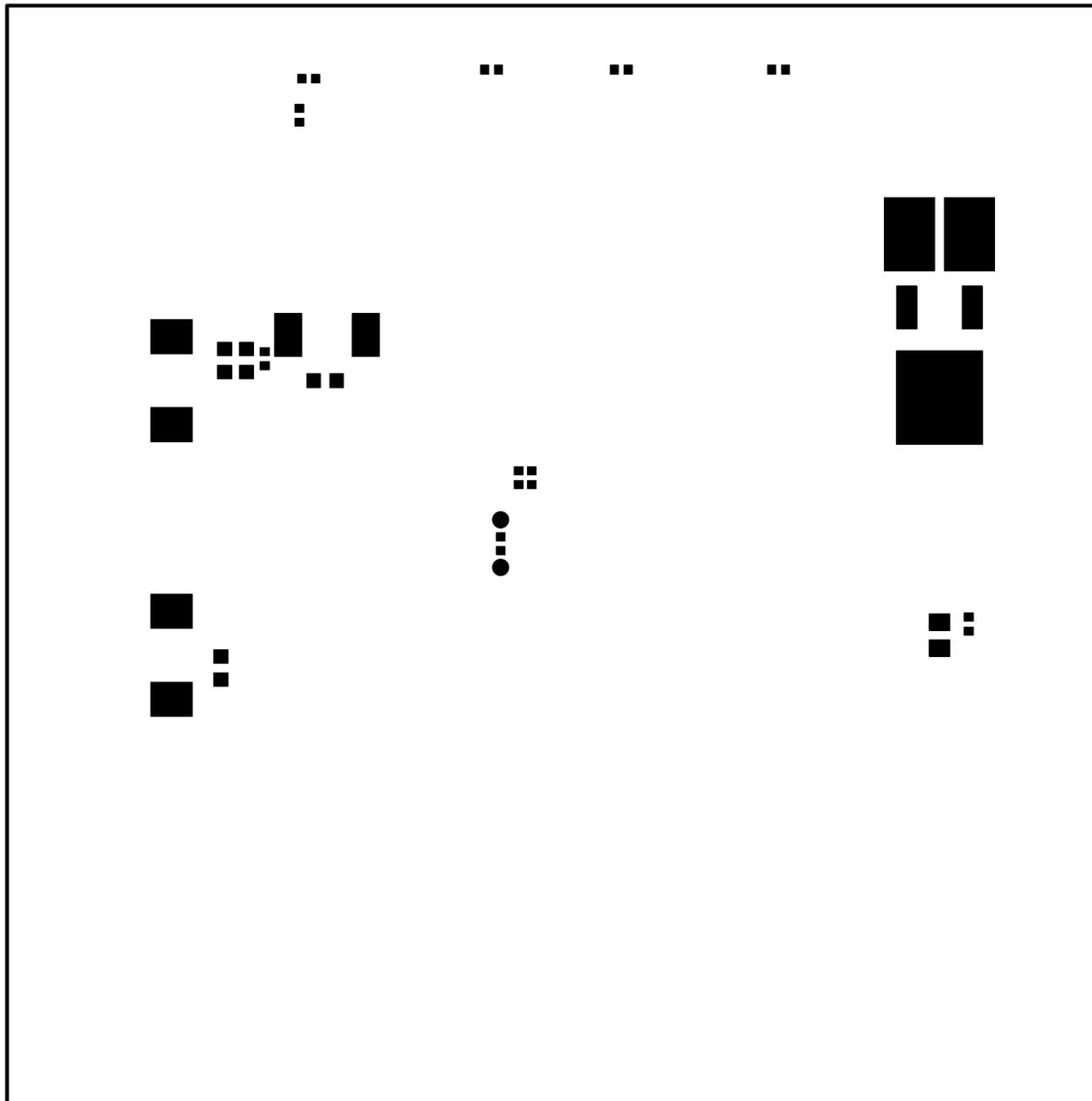
LAYER 3 : INNER LAYER2
DC3021A_REV02



LAYER 4 : BOTTOM LAYER
DC3021A_REV02



BOTTOM SOLDER MASK
DC3021A_REV02



BOTTOM SOLDER PASTE
DC3021A_REV02

BOTTOM SILKSCREEN
DC3021A_REV02

