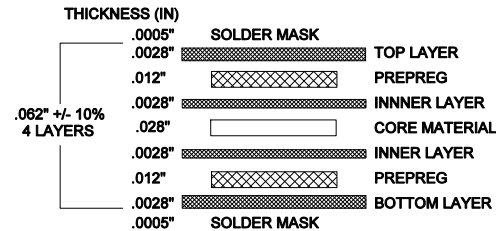


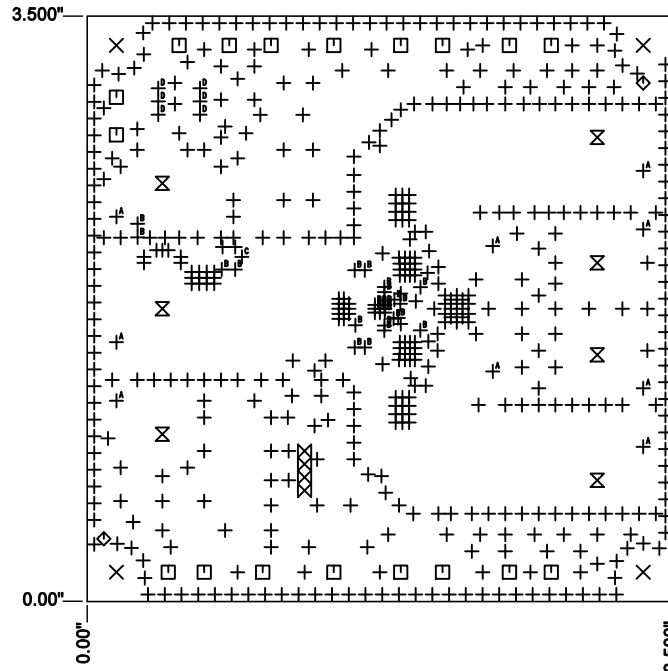
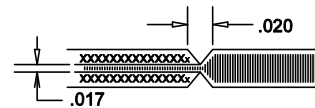
REVISION HISTORY			
ECO	REV	DESCRIPTION	APP. ENG. DATE
-	03	PRODUCTION	WL 09-10-21

LAYER STRUCTURE

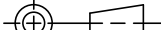




NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600, CLASS 2.
- MATERIAL: - EPOXY FIBERGLASS, NEMA GRADE FR-4
 - FINISHED THICKNESS TO BE 0.062" +/- .005"
 - TOTAL OF 4 LAYERS WITH 2 OZ. Cu ON EXTERNAL AND 2 OZ. Cu ON INTERNAL LAYERS
 - FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN 0.00" ARE PRIMARY DATUMS.
- DRILLING: - DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
 - ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
 - HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER.
- FINISH: - SMOBC USING LPI BOTH SIDES, COLOR GREEN. TENT BOTH SIDES.
 - GOLD IMMERSION BOTH SIDES.
 - FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE. PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



SIZE	QTY	SYM	PLATED	TOL
0.005	1	⊕ ^c	YES	+/-0.003
0.01	28	⊕ ^b	YES	+/-0.003"
0.012	489	+	YES	+/-0.003"
0.035	4	⊗	YES	+/-0.003"
0.04	6	⊕ ^b	YES	+/-0.003"
0.065	18	□	YES	+/-0.003"
0.07	2	◇	NO	+/-0.003"
0.095	9	⊕ ^a	YES	+/-0.003"
0.19	4	×	YES	+/-0.003
0.205	7	⊗	YES	+/-0.003

<div>UNLESS OTHERWISE SPECIFIED</div> <div>DIMENSIONS ARE IN INCHES</div> <div>TOLERANCES:</div> <div>0.XX" = ±0.01"</div> <div>0.XXX" = ±0.005"</div> <div>INTERPRET DIM AND TOL</div> <div>PER ASME Y14.5M-1994</div> <div>THIRD ANGLE PROJECTION</div> <div></div>	<div>APPROVALS</div>		<div> ANALOG DEVICES</div> <div> POWER BY LINEAR™</div> <div>FOR ADI CUSTOMER USE ONLY</div>		
	PCB DES	NC	<div>TITLE: FABRICATION DRAWING</div> <div>3.3V VIN, DUAL 6A, 2MHz SYNCHRONOUS STEP-DOWN REGULATORS WITH 1.2V AND 1.8V OUTPUTS</div> <div><div>SIZE N/A</div><div>IC NO. LTC3312SA</div><div>DEMO CIRCUIT 3091A</div></div> <div>REV 03</div>		
	APP ENG	WL			
SCALE = NONE		SHT 1 OF 1			