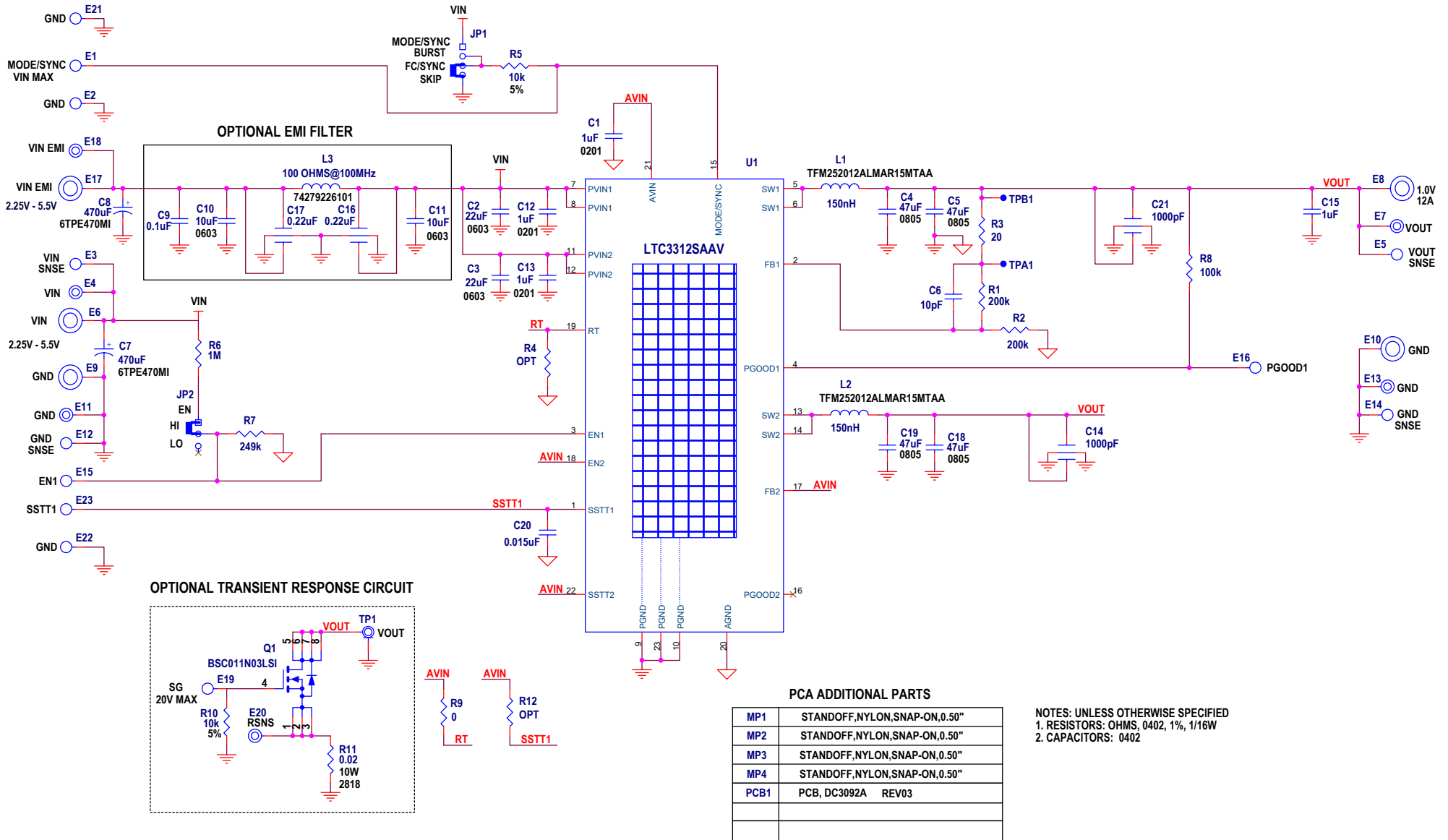


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
-	03	PRODUCTION	WL	09-13-21

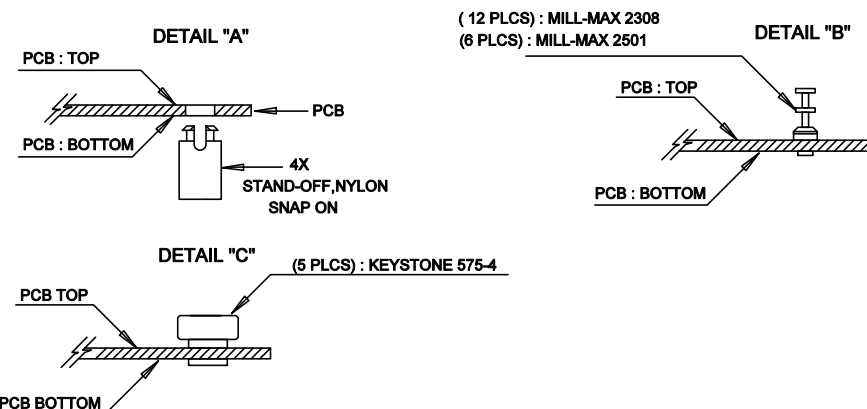
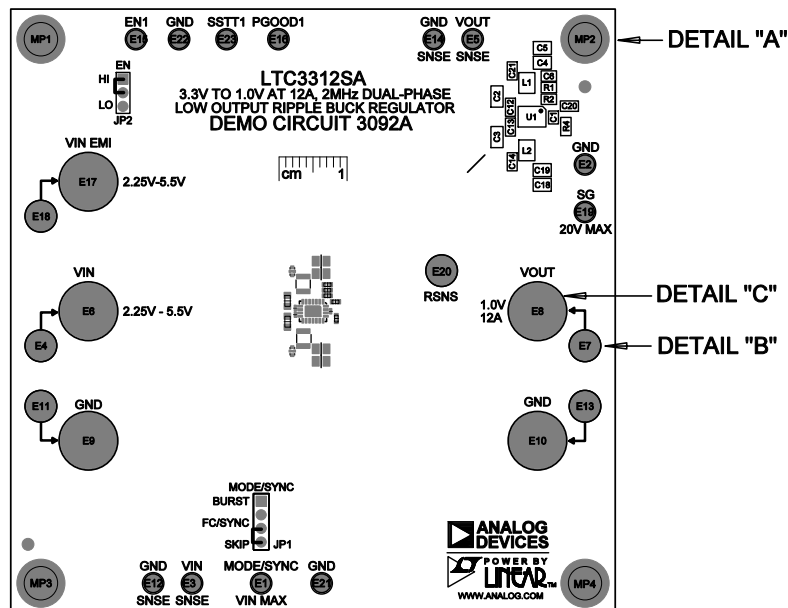




CUSTOMER NOTICE		APPROVALS		<div> </div> www.analog.com	
ANALOG DEVICES HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT ANALOG DEVICES APPLICATIONS ENGINEERING FOR ASSISTANCE.		PCB DES.	NC		
		APP ENG.	WL		
THIS CIRCUIT IS PROPRIETARY TO ANALOG DEVICES AND SUPPLIED FOR USE WITH ANALOG DEVICES PARTS.				TITLE: SCHEMATIC	
				3.3V TO 1.0V AT 12A, 2MHz DUAL-PHASE BUCK REGULATOR	
				IC NO. LTC3312SA SKU NO. DC3092A	SCHMATIC NO. AND REVISION: 710-DC3092A_REV03
				SIZE: N/A	DATE: 09-13-21
				SHEET 1 OF 1	

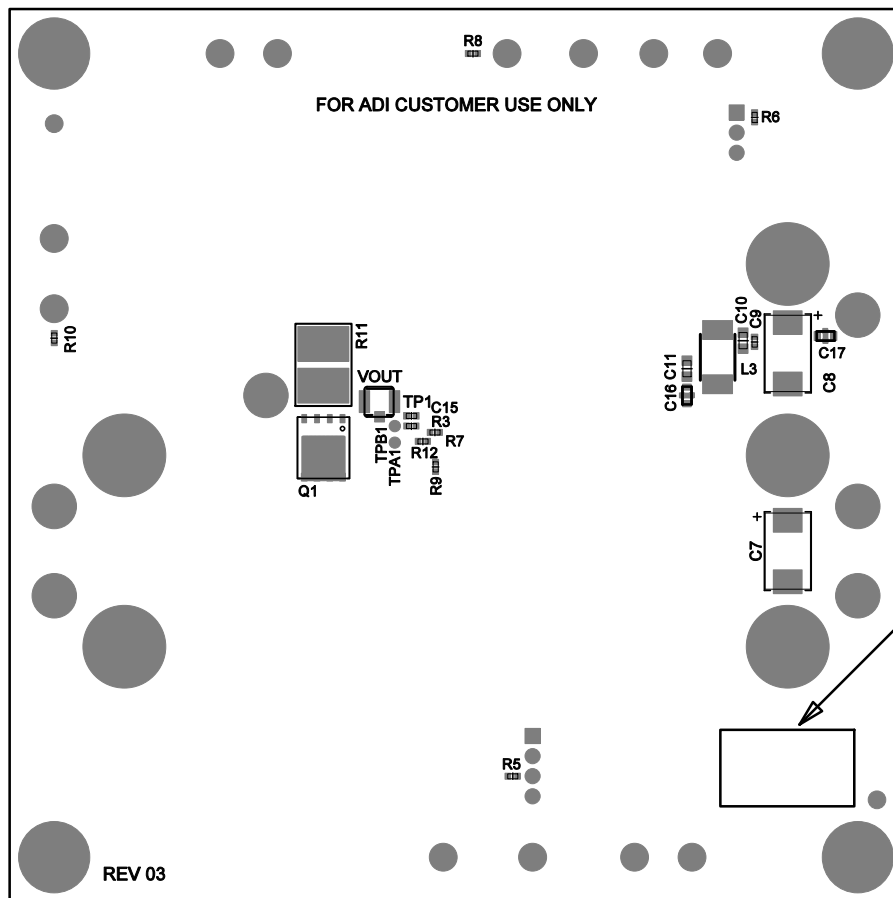
REVISION HISTORY			
ECO	REV	DESCRIPTION	APP. ENG. DATE
-	03	PRODUCTION	WL 09-13-21

NOTES: UNLESS OTHERWISE SPECIFIED

1. WORKMANSHIP SHALL BE IN ACCORDANCE WITH IPC-A-610.
2. ASSEMBLY REFLOW PROFILE SHALL BE IN ACCORDANCE WITH J-STD-020 WITH MAXIMUM SOLDER TEMPERATURE OF 250 DEGREES CELSIUS.
3. PARTS TO OMIT WILL BE SPECIFIED ON THE BILL OF MATERIALS
LOCATIONS OF OMITTED PARTS SHALL BE FREE OF SOLDER.
MASK THE SOLDER STENCIL WHERE SMT PARTS ARE OMITTED.
4. INSTALL SHUNTS AS SHOWN ON ASSY DRAWING.
5. DEPANELIZE BOARDS AFTER ASSEMBLY AND ROUTE-OUT THE BREAKOUT TABS ON FOUR SIDES OF THE BOARD EDGE.
6. APPLY ASSEMBLY STAMP OR QA STAMP TO BOTTOM OF BOARD (UNSHOWY AREA).
7. INSTALL TURRETS, STAND-OFFS AS SHOWN BELOW:
8. APPLY DEMO S/N AT AREA ON BOTTOM SIDE AS SHOWN ON SHEET 2.



APPROVALS		 ANALOG DEVICES		 POWER BY LINEAR™	
PCB DES	NC	FOR ADI CUSTOMER USE ONLY			
APP ENG	WL	TITLE: TOP ASSEMBLY DRAWING			
		3.3V TO 1.0V AT 12A, 2MHz DUAL-PHASE LOW OUTPUT RIPPLE BUCK REGULATOR			
		SIZE	IC NO.	LTC3312SA	REV.
		N/A		DEMO CIRCUIT 3092A	03
SCALE = NONE					SHT 1 OF 1



8 DEMO S/N LABEL
APPLY IN THIS AREA

APPROVALS

PCB DES.	NC
APP ENG.	WL

SCALE = NONE



FOR ADI CUSTOMER USE ONLY

TITLE: BOTTOM ASSEMBLY DRAWING

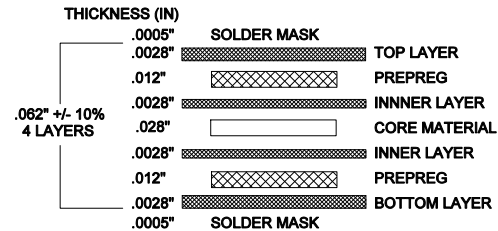
3.3V TO 1.0V AT 12A, 2MHz DUAL-PHASE
LOW OUTPUT RIPPLE BUCK REGULATOR

SIZE N/A	IC NO. LTC3312SA DEMO CIRCUIT 3092A	REV 03
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SHT 2 of 2

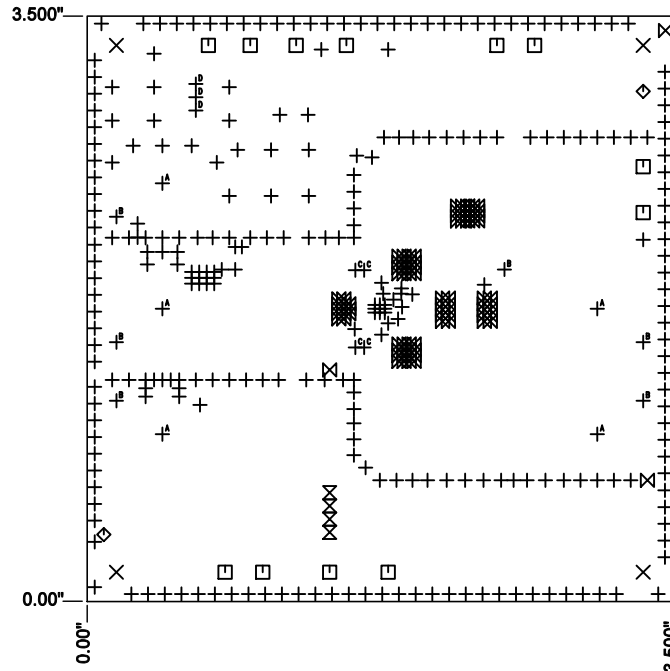
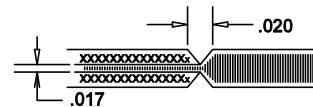
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	03	PRODUCTION	WL	09-13-21

LAYER STRUCTURE

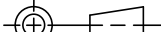




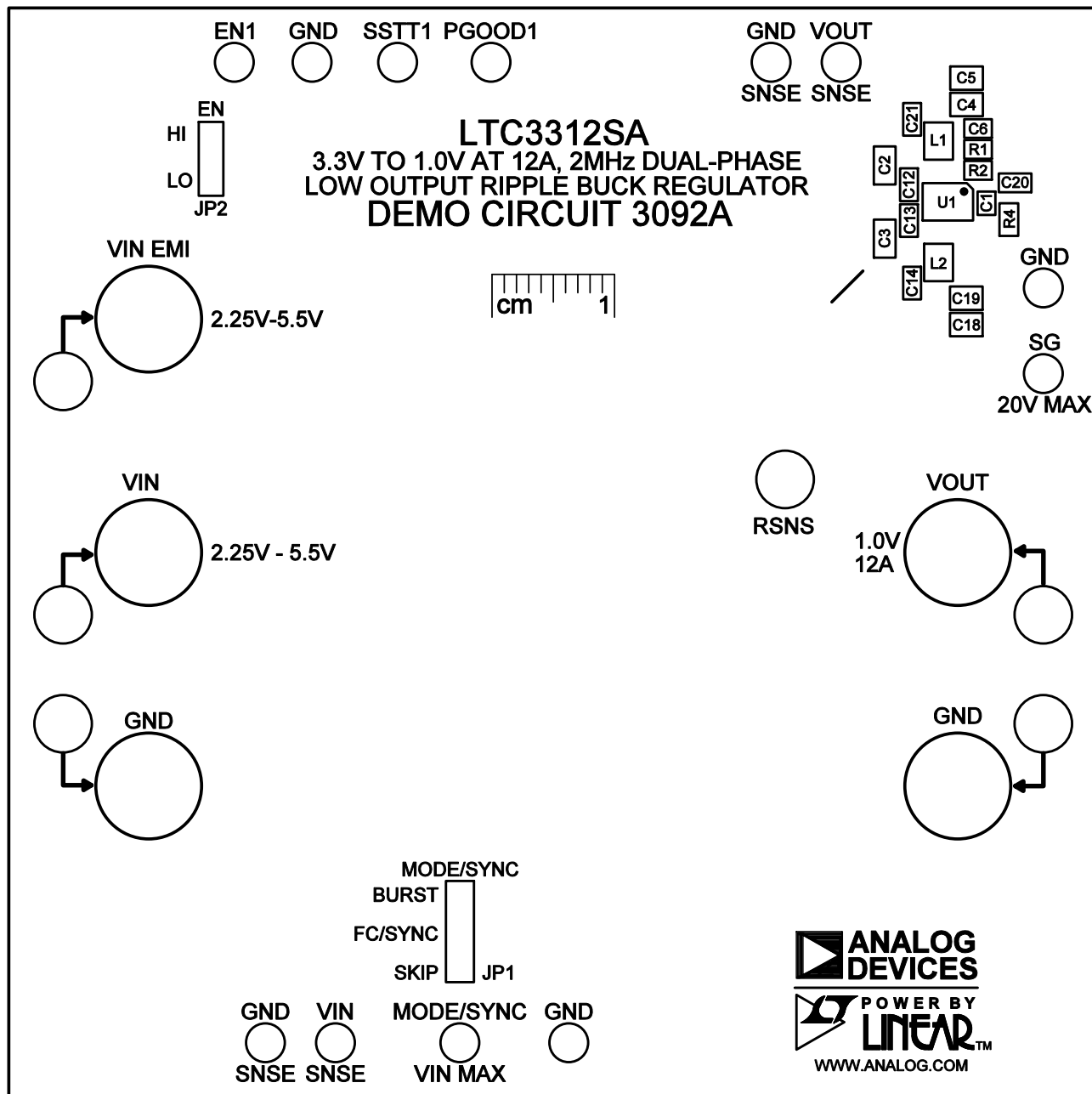
NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600, CLASS 2.
- MATERIAL: - EPOXY FIBERGLASS, NEMA GRADE FR-4
 - FINISHED THICKNESS TO BE 0.062" +/- .005"
 - TOTAL OF 4 LAYERS WITH 2 OZ. Cu ON EXTERNAL AND 2 OZ. Cu ON INTERNAL LAYERS
 - FLAMMABILITY RATING: 94 V-O MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN 0.00" ARE PRIMARY DATUMS.
- DRILLING: - DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
 - ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
 - HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER.
- FINISH: - SMOBC USING LPI BOTH SIDES, COLOR GREEN. TENT BOTH SIDES.
 - GOLD IMMERSION BOTH SIDES.
 - FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE. PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:

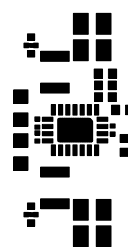


SIZE	QTY	SYM	PLATED	TOL
0.005	4	⊕	YES	+/-0.003"
0.01	270	+	YES	+/-0.003"
0.012	88	⊗	YES	+/-0.003"
0.035	4	⊗	YES	+/-0.003"
0.04	3	⊕	YES	+/-0.003"
0.065	12	⊗	YES	+/-0.003"
0.07	2	⊕	NO	+/-0.003"
0.095	6	⊕	YES	+/-0.003"
0.19	4	⊗	YES	+/-0.003"
0.205	5	⊕	YES	+/-0.003"

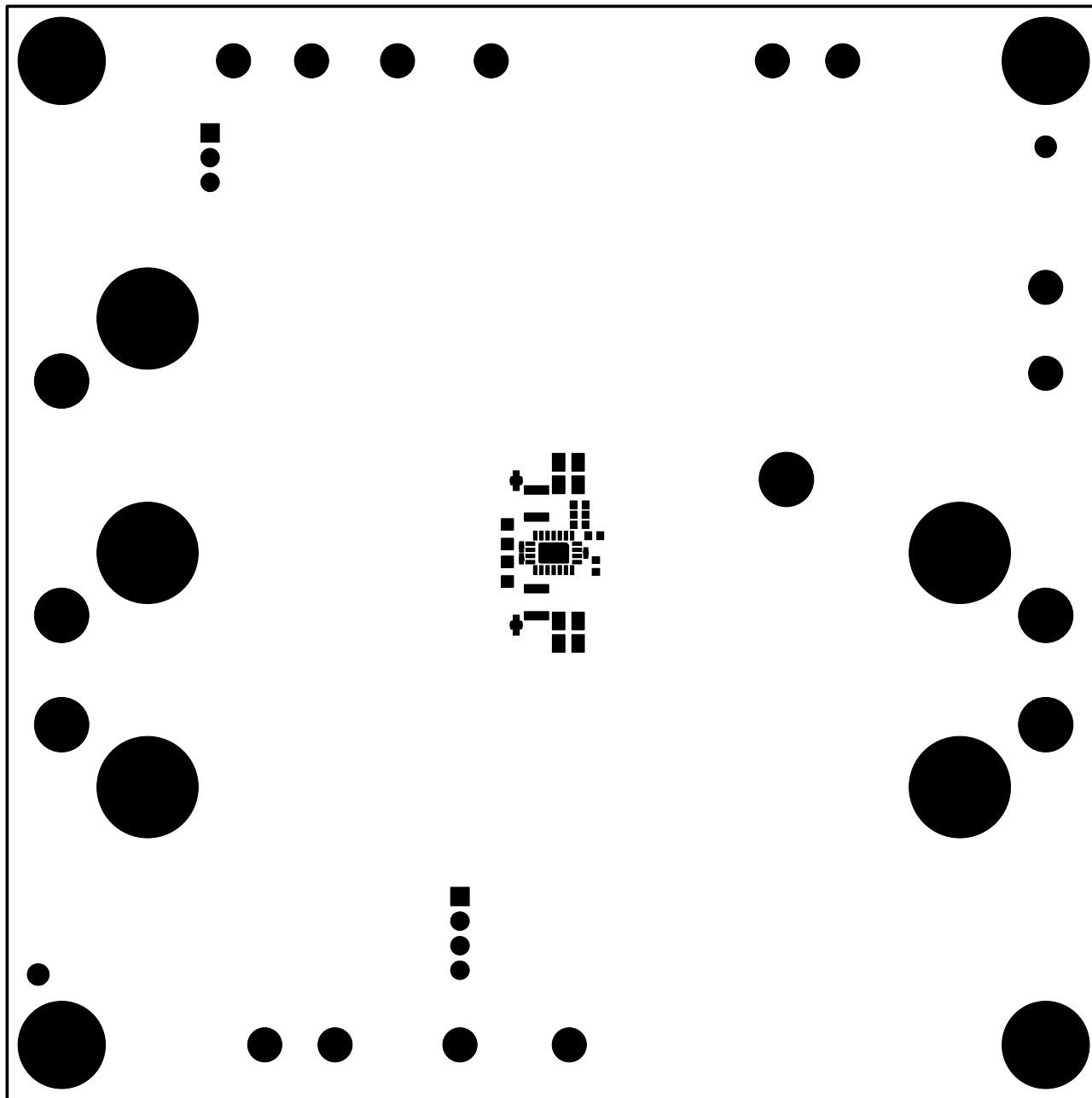
<div>UNLESS OTHERWISE SPECIFIED</div> <div>DIMENSIONS ARE IN INCHES</div> <div>TOLERANCES:</div> <div>0.XX" = ±0.01"</div> <div>0.XXX" = ±0.005"</div> <div>INTERPRET DIM AND TOL</div> <div>PER ASME Y14.5M-1994</div> <div>THIRD ANGLE PROJECTION</div> <div></div>	<div>APPROVALS</div>		<div> </div> <div>FOR ADI CUSTOMER USE ONLY</div>	
	PCB DES	NC	<div>TITLE: FABRICATION DRAWING</div> <div>3.3V TO 1.0V AT 12A, 2MHz DUAL-PHASE</div> <div>LOW OUTPUT RIPPLE BUCK REGULATOR</div>	
	APP ENG	WL		
			SIZE	IC NO. LTC3312SA
		N/A	DEMO CIRCUIT 3092A	03
	SCALE = NONE			SHT 1 OF 1



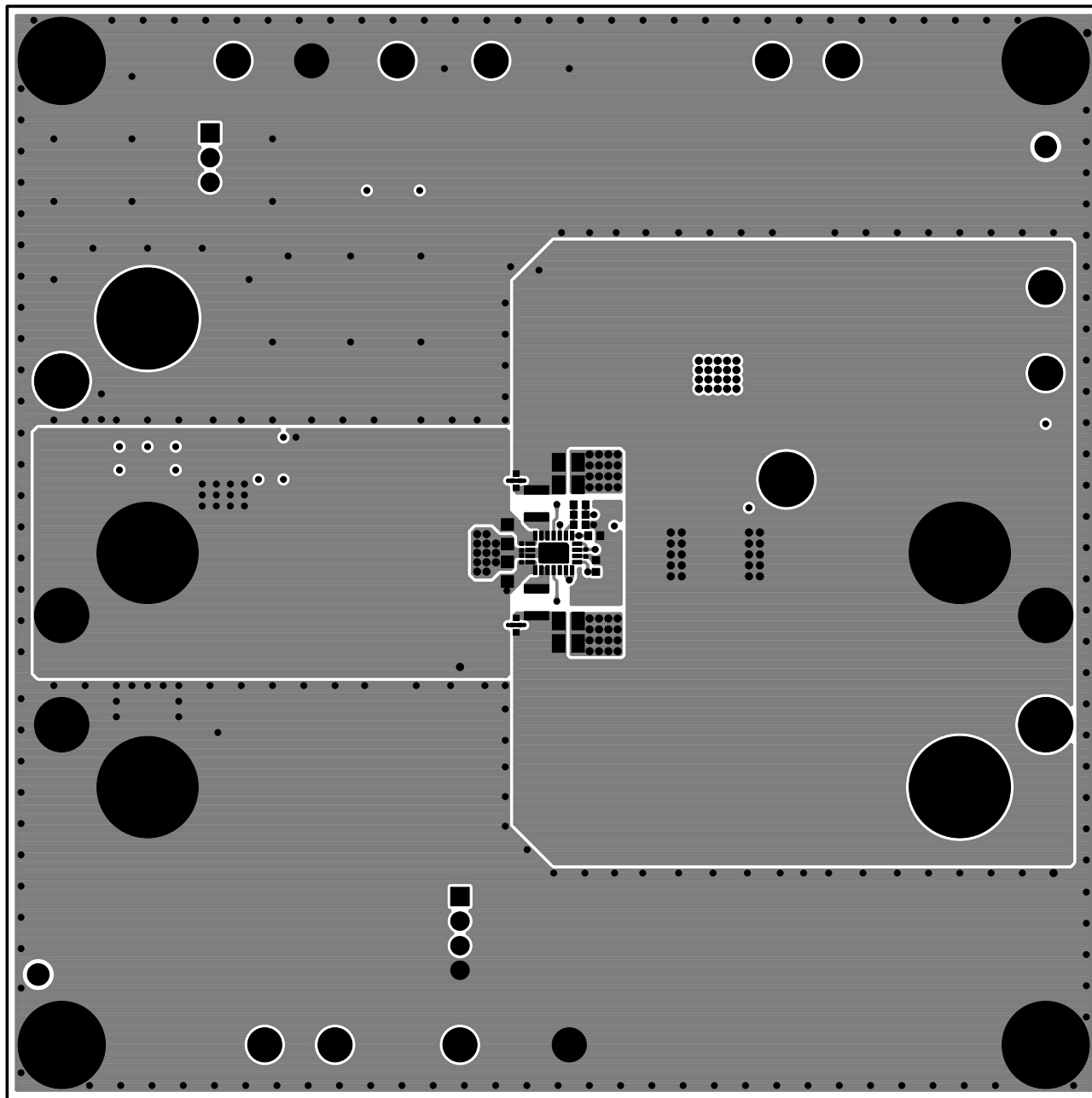
TOP SILKSCREEN
DC3092A_REV03



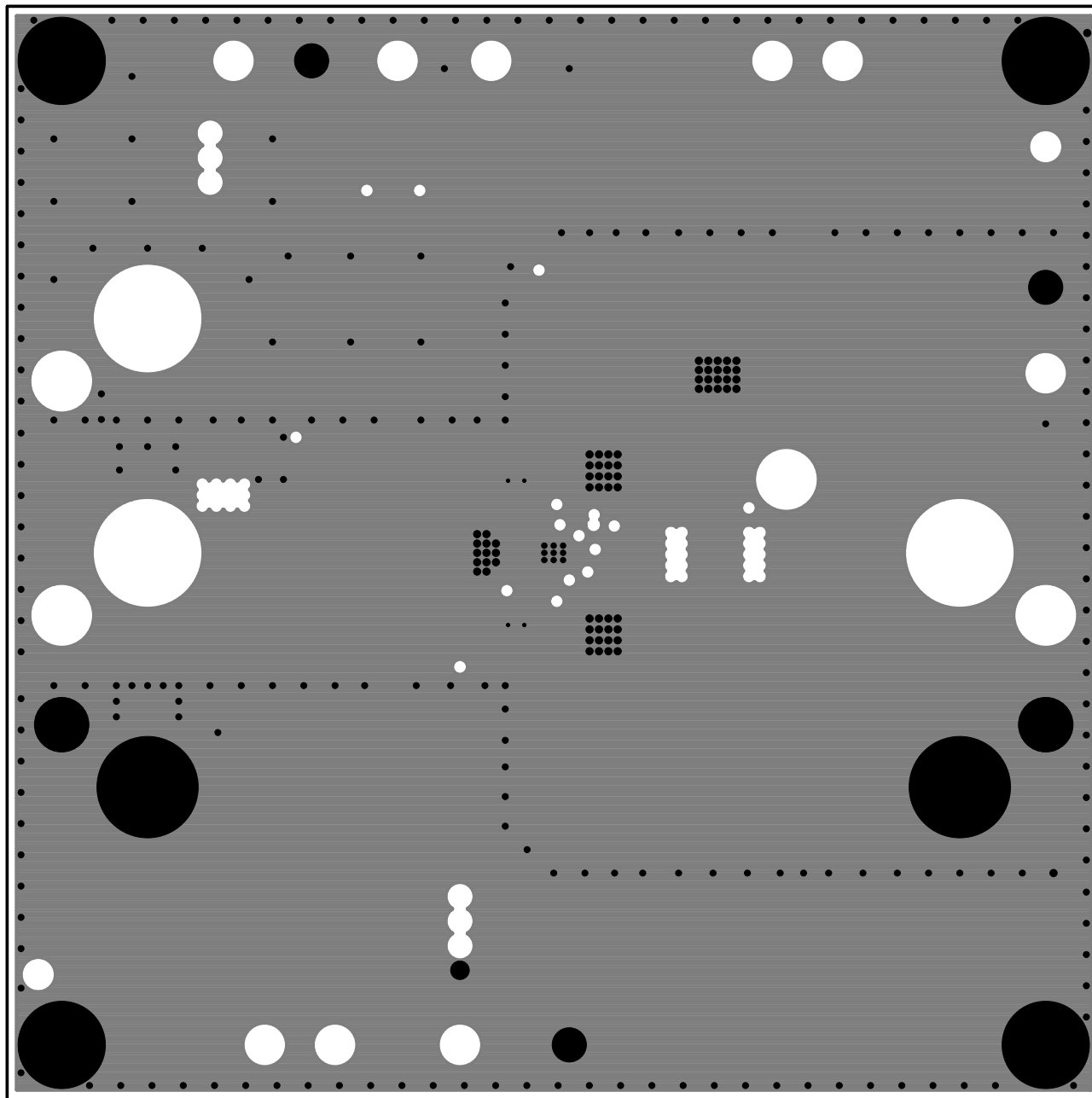
TOP SOLDER PASTE
DC3092A_REV03



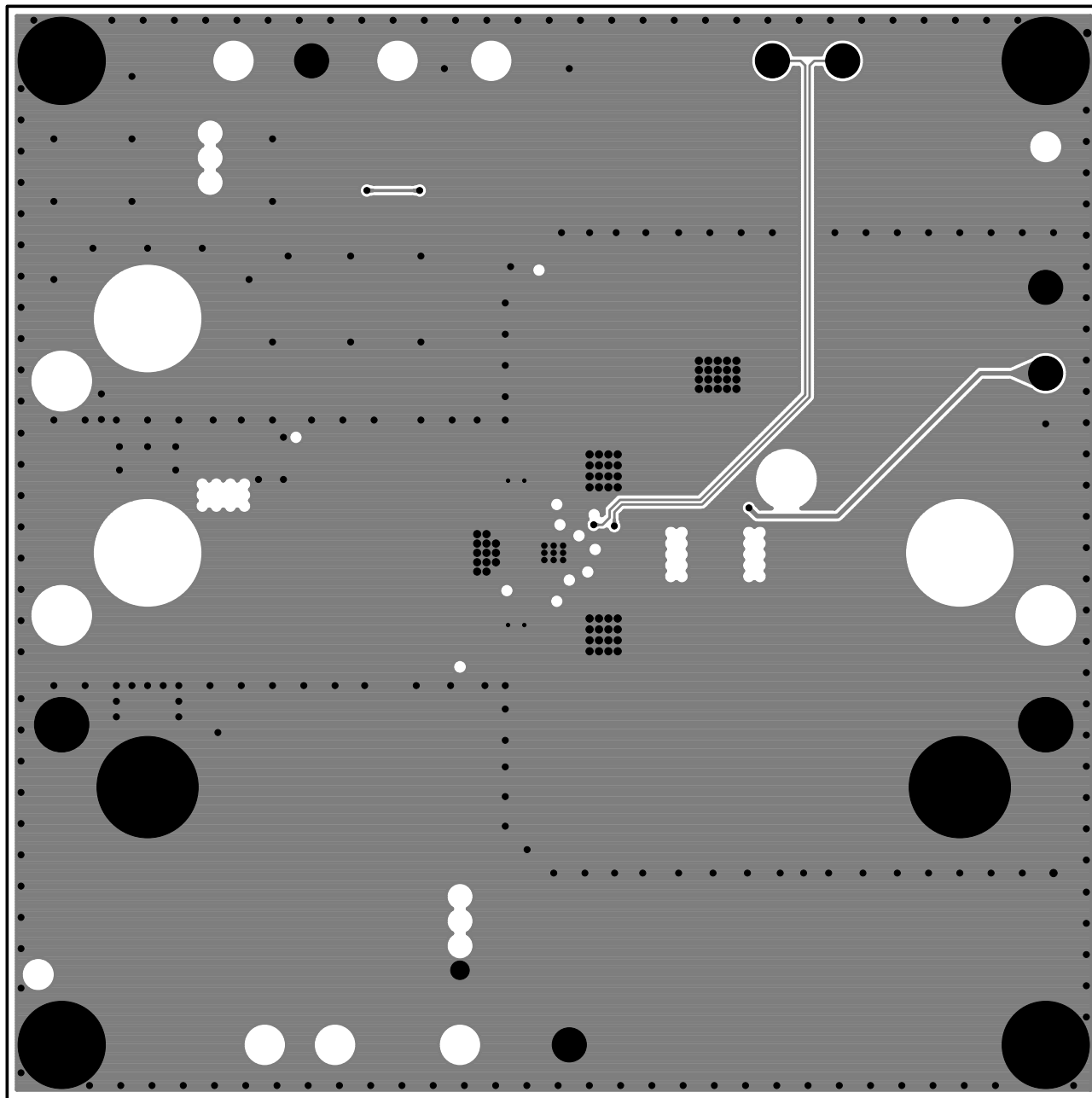
TOP SOLDER MASK
DC3092A_REV03



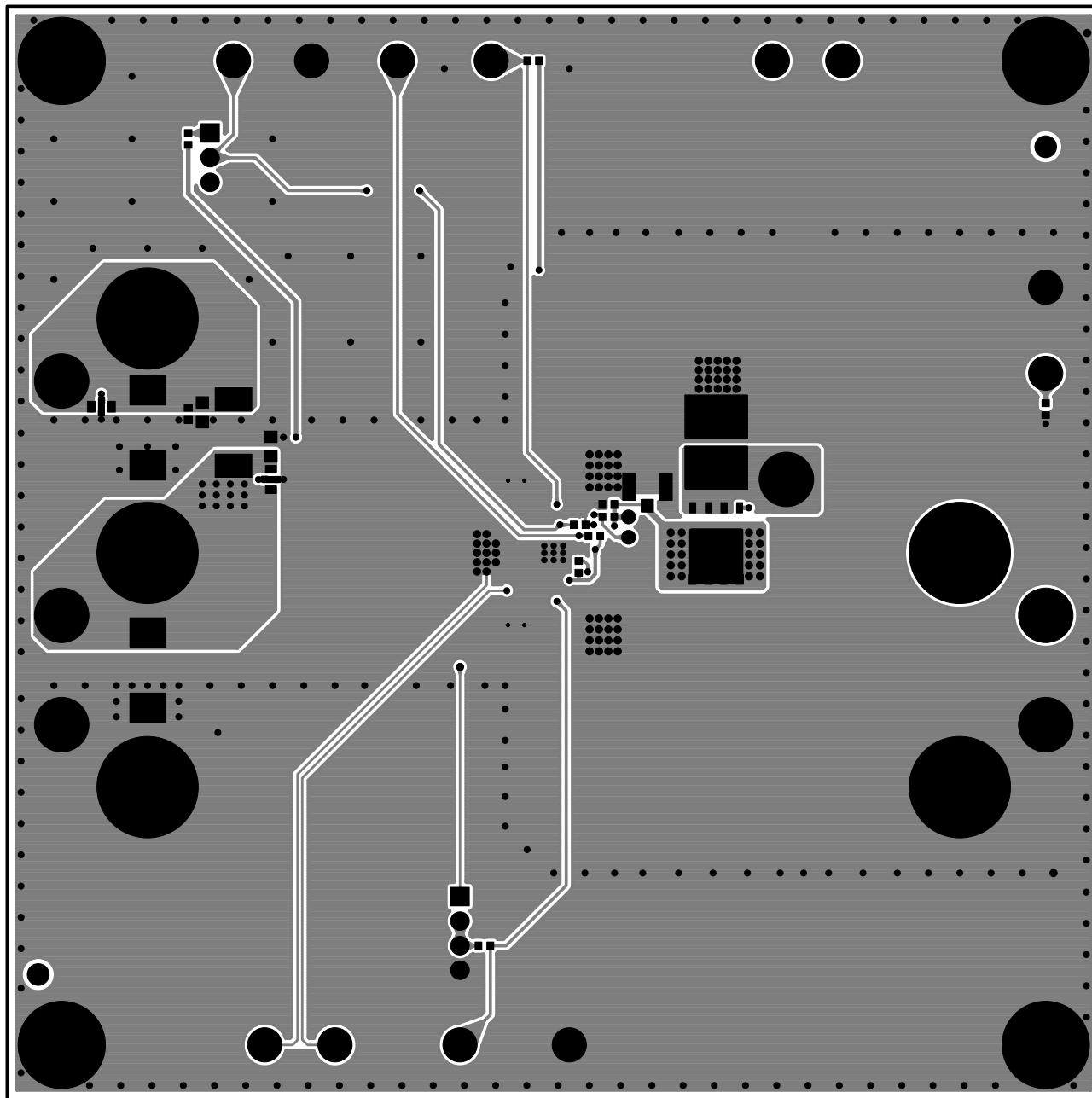
LAYER 1 : TOP LAYER
DC3092A_REV03



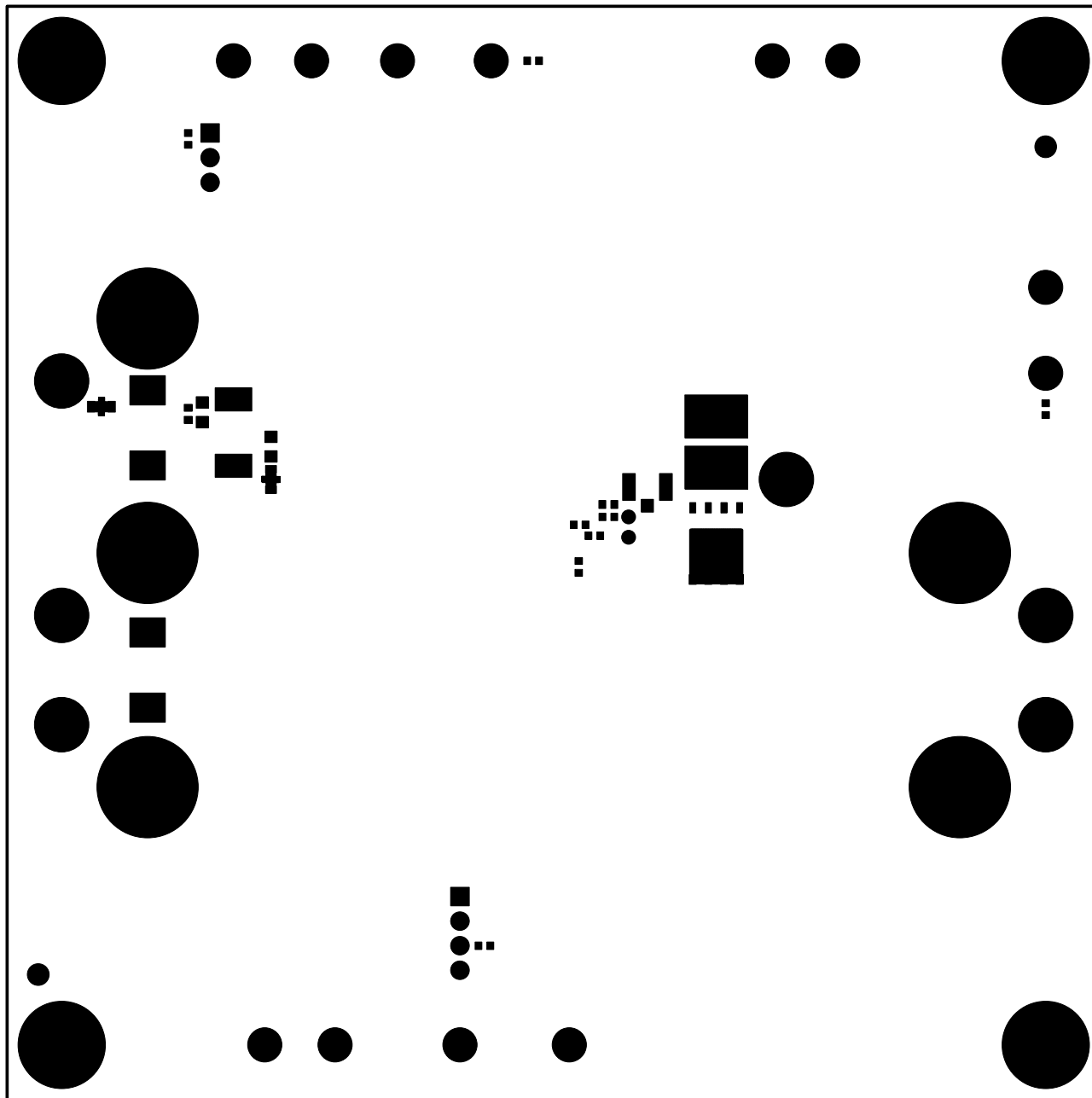
LAY 2 : - GND 1
DC3092A_REV03



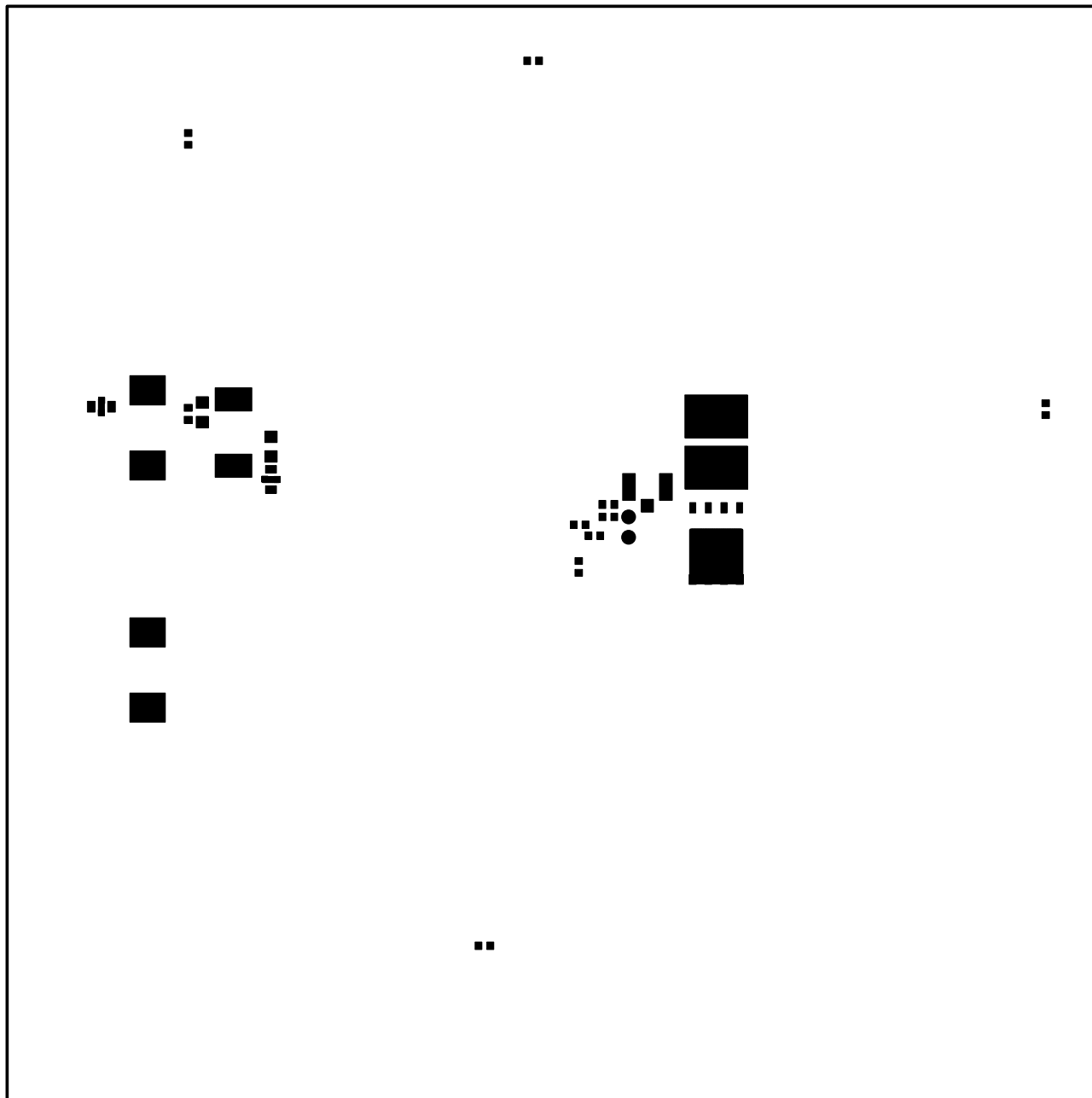
LAYER 3 : - GND 2
DC3092A_REV03



LAYER 4 : BOTTOM LAYER
DC3092A_REV03



BOTTOM SOLDER MASK
DC3092A_REV03



BOTTOM SOLDER PASTE
DC3092A_REV03

