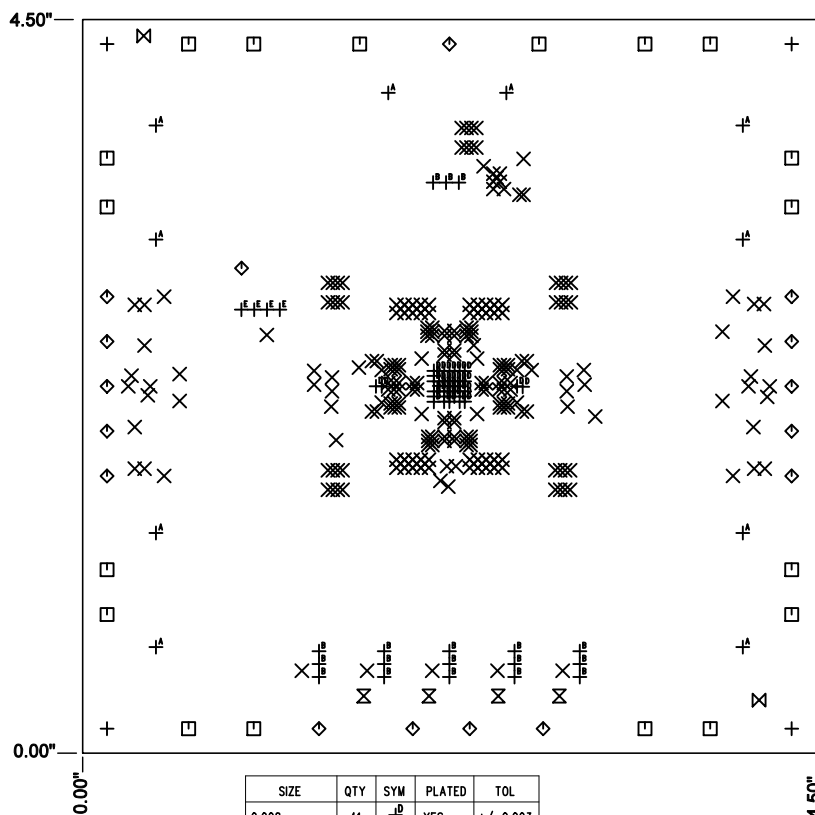
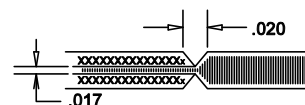


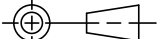


REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	01	PRODUCTION FAB	ZP	03-25-19

**THE FAB SHALL BE ROHS-COMPLIANT.**  
**NOTES: UNLESS OTHERWISE SPECIFIED**

1. FABRICATION: FABRICATION AND ACCEPTANCE TO MEET THE REQUIREMENTS OF IPC-A-600 AND IPC/ANSI-MLL950-C CLASS 2, AOL .25 GENERAL INSPECTION LEVEL II.
2. MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4  
- FINISHED THICKNESS TO BE 0.062" +/- .005"  
- TOTAL OF 6 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS  
AND 1 OZ. CU ON THE INNER LAYERS.  
- FLAMMABILITY RATING: 94 V-O MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
0.00" ARE PRIMARY DATUMS.
4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH  
HOLES WITH COPPER, 0.001" THICK MIN.  
- ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
- HOLE LOCATION TOLERANCES ARE +/-0.003"  
IN RELATION TO CENTER
5. FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN, TENT BOTH SIDES.  
- GOLD IMMERSION, 2-5 MICRO INCHES (ROHS COMPLIANT).  
- FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.  
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBS ARE TO BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCB:



SIZE	QTY	SYM	PLATED	TOL
0.008	41	⊕	YES	+/-0.003
0.01	260	×	YES	+/-0.003
0.03	4	⊗	YES	+/-0.003
0.035	4	⊕	YES	+/-0.003
0.04	18	⊕	YES	+/-0.003
0.065	16	⊕	YES	+/-0.003
0.07	2	⊗	NO	+/-0.003
0.095	18	⊕	YES	+/-0.003
0.188	4	⊕	NO	+/-0.003
0.204	10	⊕	YES	+/-0.003

<div>UNLESS OTHERWISE SPECIFIED</div> <div>DIMENSIONS ARE IN INCHES</div> <div>TOLERANCES:</div> <div>0.XX" = ".01"</div> <div>0.XXX" = ".005"</div> <div>INTERPRET DIM AND TOL</div> <div>PER ASME Y14.5M-1994</div> <div>THIRD ANGLE PROJECTION</div> <div></div>	<div>APPROVALS</div>		<div>  1630 MCCARTHY BLVD MILPITAS, CA 95038 www.Linear.com</div> <div>FOR ADI CUSTOMER USE ONLY</div>
	PCB DES.	NC	
	APP ENG.	ZP	
<div>TITLE: FABRICATION DRAWING</div> <div>20V, 4-CHANNEL, BUCK DC/DC WITH 8x</div> <div>CONFIGURABLE 1.5A POWER STAGES</div>			
<div>SIZE</div> <div>N/A</div>		<div>IC NO. LTC3376</div> <div>DEMO CIRCUIT 2966A</div>	<div>REV</div> <div>01</div>
<div>SCALE = NONE</div>			<div>FILENAME:</div> <div>SHT 1 OF 1</div>