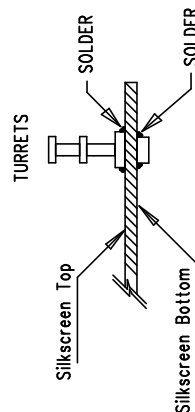



NOTES : Unless Otherwise Specified

1. BREAK BOARDS FROM THE PANEL WHEN ASSEMBLY IS COMPLETE.
2. OPTIONAL COMPONENTS ARE UNSTUFFED AND SHOULD BE COVER WITH SOLDER AFTER ASSEMBLY. (NO EXPOSED PADS)
3. JUMPER SHUNTS INSTALLATION: JP1- ON, JP2 - ON, JP3 - 1.2V, JP4 - 1.2V, JP4 - 2.5V, JP5 - Burst Mode.
4. INSTALL TURRETS



REVISIONS		
REV	APPR	DATE

APPROVALS						 1630 McCarthy Blvd. Milpitas, CA 95035 PH: (408)432-1900	
	DRAWN		INIT	DATE			
	CHECK						
	DESIGN		Antonina K				
	ENGR		Tom Gross				
TITLE: LTC3407AEDD							
Dual Synchronous Step-Down DC/DC Converter							
	SIZE	NONE		Demo Circuit 1180A		REV. A	
SCALE = NONE						DES-230730 SHT 1 of 1	