\* D3 IS RECOMMENDED FOR VIN>4.5V. PLEASE REFER TO DATASHEET FOR DETAILS. SUMIDA CDRH4D28C/100 D2 PMEG2010EA L1 10UH E4 VOUT VOUT 3.3V PHILIPS \_C2 -22UF,6.3V<sup>-</sup> 1210 \_C3 -180PF U1 LTC3440EDD D3 POWERMITE D1 PMEG2010EA PHILIPS ON SEMI **E**5 ★ OPT GND SW1 SW<sub>2</sub> VIN 2.5V-5.5V VIN VOUT **SHDN** <u>+</u>C8 →47UF,10V TAJB C1 -10UF 6.3V 1206 R4 1M SHDN/SS FΒ R3 1C4 MODE/SYNC VC ^//\^ 15K 560PF GND **GND** C5 10PF R7 4.87M \_C6 -0.047UF R6 60.4K 1% R8 6.19M R2 200K 1% GND( SYNC **BURST FIXED** VIN MODE FREQ. SHDN RUN JP2 JP1 VIN **IOUT** 540mA 2.5V 1630 McCarthy Blvd. 2.7V 600mA **CUSTOMER NOTICE** CONTRACT NO. Milpitas, CA 95035 Phone: (408)432-1900 LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A **APPROVALS** Fax: (408)434-0507 CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; **NOTES: UNLESS OTHERWISE SPECIFIED** DRAWN: LTC Confidential-For Customer Use Only HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO CHECKED: KIM T. TITLE: VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL 1. ALL RESISTORS ARE IN OHMS, 0402. APPLICATION. COMPONENT SUBSTITUTION AND PRINTED APPROVED: 1MHz SYNCHRONOUS BUCK-BOOST IN 3mm X 3mm DFN ALL CAPS. ARE 0402. CIRUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT ENGINEER: DONGYAN Z. PERFORMANCE OR RELIABILITY. CONTACT LINEAR SIZE DWG NO. REV 2. INSTALL SHUNTS ON JP1 AND JP2 PIN 2 AND 3. TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE. DESIGNER: DC797A-1\*LTC3440EDD A Α THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND DATE: Friday, August 20, 2004 SHEET 1 OF 1 SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.