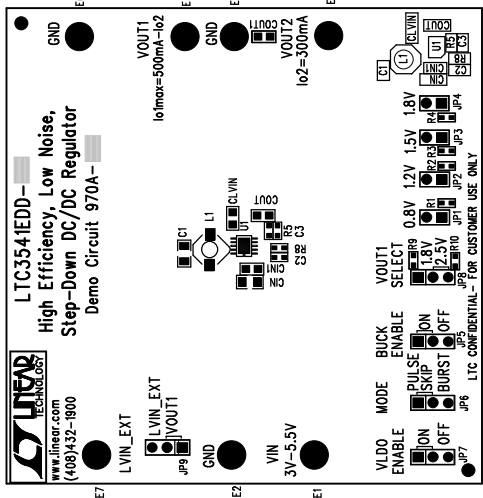


REVISIONS		
REV	APPR	DATE




LINEAR TECHNOLOGY CORPORATION  
DC970A  
TOP SILKSCREEN

## Top Assembly Drawing

### NOTES : Unless Otherwise Specified

1. BREAK BOARDS FROM THE PANEL WHEN ASSEMBLY IS COMPLETE.
2. OPTIONAL COMPONENTS ARE UNSTUFFED AND SHOULD BE FREE OF SOLDER AFTER ASSEMBLY.
3. JUMPER SHUNTS INSTALLATION: JP5-ON, JP6-PULSE SKIP, JP7-ON, JP8-2.5V, JP9-VOUT1, JP4

ASSEMBLY TYPE	U1
DC970A-A	LTC3541EDD
DC970A-B	LTC3541EDD-1

APPROVALS				1630 McCarthy Blvd. Milpitas, CA 95035 PH: (408)432-1900	
		INIT	DATE		
DRAWN		Antonia K			
CHECK					
DESIGN					
ENGR		George Yu			
		SIZE	NONE	Demo Circuit 970A	REV. 3
		DESIGN #		DC970A.DSN	SHT 1 of 1
		SCALE = NONE			

TITLE: LTC3541EDD/LTC3541EDD-1  
High Efficiency, Low Noise, Step-Down DC/DC Regulator