



**NOTES: UNLESS OTHERWISE SPECIFIED**

1. ALL RESISTORS ARE IN OHMS, 0402.  
ALL CAPACITORS ARE 0402.
  2. INSTALL SHUNT ON JP1, JP2, & JP5 PINS 1 AND 2.
- \* CIN1 IS AN OPTIONAL CAPACITOR. IT IS INSERTED ON THE DC1025A TO DAMPEN THE (POSSIBLE) RINGING VOLTAGE DUE TO THE LONG INPUT LEADS. ON A NORMAL, TYPICAL PCB, WITH SHORT TRACES, THE CAPACITOR IS NOT NEEDED.

**CUSTOMER NOTICE**

LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.

THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.

CONTRACT NO.

APPROVALS

DRAWN: KIM T.

CHECKED:

APPROVED:

ENGINEER: TOM G.

DESIGNER:



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LTC Confidential-For Customer Use Only

TITLE: SCHEMATIC

**800mA MONOLITHIC SYNCHRONOUS BUCK REGULATOR**

SIZE  
A

DWG NO.

**DC1025A-1 \* LTC3560ES6**

REV  
A-1

DATE: Friday, December 22, 2006

SHEET 1 OF 1