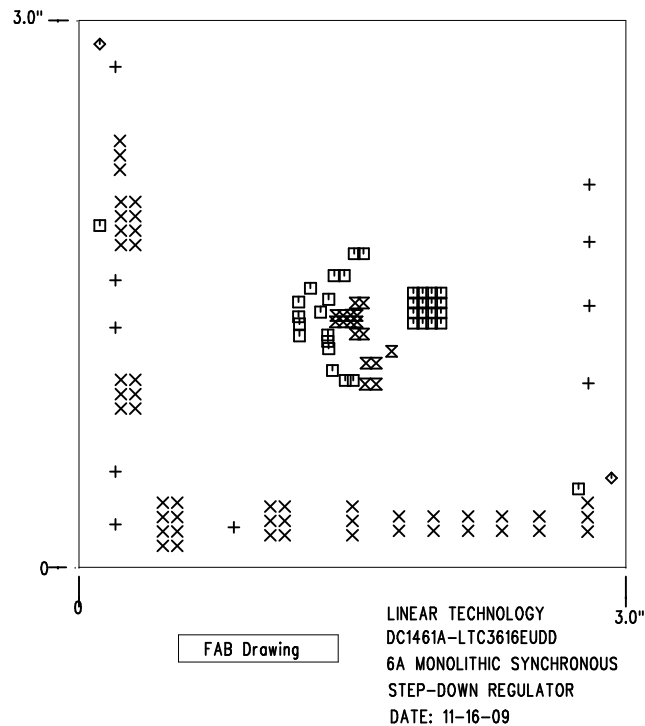








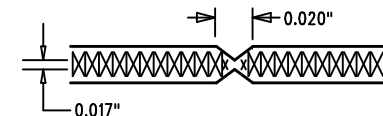
REVISION HISTORY				
ECO	REV	DESCRIPTION	APPR	DATE
1	2	PRODUCTION	TOM.G	11-16-09




SIZE	QTY	SYM	PLATED	TOL
0.095	10	+	YES	+/-0.003
0.035	47	×	YES	+/-0.003
0.01	35	□	YES	+/-0.003
0.07	2	◇	NO	+/-0.003
0.02	17	⊗	YES	+/-0.003

 LAYER 1 ( COMPONENT SIDE )  
 LAYER 2  
 LAYER 3  
 LAYER 4  
 LAYER 5  
 LAYER 6 (SOLDER SIDE)

1. FAB PER IPC-A-600.
2. MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4  
-FINISHED THICKNESS TO BE 0.062" +/- .005"  
-TOTAL OF 6 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS  
AND 1 OZ. CU ON THE INNER LAYERS.  
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.  
0.00" ARE PRIMARY DATUMS.
4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH  
HOLES WITH COPPER, 0.001" THICK MIN.  
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.  
-HOLE LOCATION TOLERANCES ARE +/-0.003"  
IN RELATION TO CENTER
5. FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.  
-GOLD IMMERSION BOTH SIDES.  
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.  
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs ARE TO BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCB:



UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES TOLERANCES ON ANGLE ±1° 0.XX" = ±0.01" 0.XXX" = ±0.005" INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION	APPROVALS		 1630 MCCARTHY BLVD MILPITAS, CA 95035 PH: (408)432-1900 LTC CONFIDENTIAL- FOR CUSTOMER USE ONLY
	PCB DES.	NAME	DATE
	ENG.	TOM G.	11-16-09
DO NOT SCALE DRAWING		SCALE: NONE	FILENAME: DC1461A-1.PCB
TITLE: FABRICATION DRAWING		6A MONOLITHIC SYNCHRONOUS STEP-DOWN REGULATOR	
SIZE A		IC NO. LTC3616EUDD DEMO CIRCUIT 1461A	REV. 2
		SHT 1 of 1	