A		В		С		D		E	
CIN1 IS AN OPTIONAL CAPACITOR. IT IS INSERTED ON THE DC1460A TO DAMPEN THE (POSSIBLE) RINGING VOLTAGE DUE TO THE LONG INPUT LEADS. ON A NORMAL TYPICAL PCB. WITH SHORT TRACES					REVISION #	DESCRI	EVISION HISTORY	DATE	APPROVED
						1st Release			
						13t Refe	"I DS3008" corios	4/0/00	ChuonmingT
THE CAPACITOR IS NOT NEEDED.								4/9/09	ChuenmingT
					3 2 1 2	LI TROM LPS3008 to	LPS3010 series.	6/26/09	Chuenming
$\frac{VIN}{2.8V - 5.5V} \bigoplus_{\substack{E2\\ GND} \bigoplus_{\substack{E2\\ \hline \\ GND} \bigoplus_{E2\\ \hline \\ \\ \hline \\ \\ \hline \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\ \\$	N1 \ast CIN 1uF 6.3V R1 IM = IM IM = IM IM = IM IM = IM IM = IM IM = IM IM = IM IM = IM IM = IM IM = IM IM = IM IM = IM IM = IM IM = IM IM = IM IM IM = IM IM IM IM = IM	FREE RUN EXTERNAL CLOCK 50kHz (MIN.) R3 IM	20EDC LOBATB SW LOBATB SW LOBATB SW LOBATB C SW SW SW SW SW SW SW SW SW SW		VOUT ML $COUT$ $1uF$ $6.3V$ $=$ CFF 22 22 $1.65M$ $1%$ $1%$ $1.2V$ 0 $1.2V$ 0	FW pF RFB1 1.37M 1% RFB2 1.37M 681K 1% JP2 1.8V 7	R2 715K 1% COUT1 0.1uF =	E7 LOBATB	ax) 3
				APPROV	OVALS				
This circuit is proprietary to Linear Technology and supplied for use with Linear Technology parts. Customer Notice: Linear Technology has made a best effort to design a circuit that meets customer-supplied specifications; however, it remains the customers responsibility to verify proper and reliable operation in the actual application, Component substitution and printed circuit board layout may significantly affect circuit performance or reliability. Contact Linear				DRAWN: Rudy	Bautísta	(408)432-1900 LTC Confidential - (408)434-0507 (FAX) For Customer Use Only Title: LTC3620EDC			ential - er Use Only ¹
				ENGINEER: Tom	Gross/Chuen MT				
				APPROVED:	APPROVED: High Efficiency Monolithic Buck Regulator				
				CHECKED:		SD Document Number Demo Circuit 1460A			Rev 3
	Applications Engineering f	ons Engineering for assistance.		Date: Tuesday, August 04	, 2009 C:\C	DRCADWIN\CAPTURE\1357	A\1460A_REV3.DSN	Sheet 1	. ^{of} 1
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