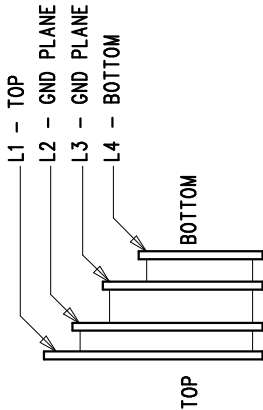
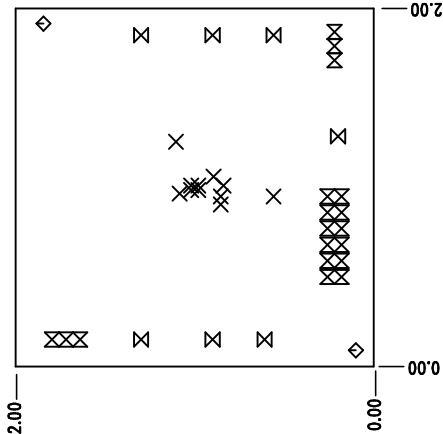
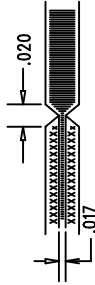


REVISION HISTORY			
ECO	REV	DESCRIPTION	APPR DATE
	2	PRODUCTION	TOM G. 11-30-12



NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062 +/- .005
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
5. FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
7. PCBs ARE TO BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCB



SIZE	QTY	SYM	PLATED	TOL
0.01	11	X	YES	+/-0.003
0.07	2	◇	NO	+/-0.003
0.095	7	⊗	YES	+/-0.003
0.035	18	⊗	YES	+/-0.003

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES:
0.XX" = ±0.01"
0.XXX" = ±0.005"
INTERPRET DIM AND TOL
PER ASME Y14.5M-1994
THIRD ANGLE PROJECTION

APPROVALS

PCB DES.	HZ
APP ENG.	TOM G.
TITLE: FABRICATION DRAWING	
17V VIN SYNCHRONOUS STEP-DOWN REGULATOR	
WITH ULTRA-LOW QUIESCENT CURRENT	
SIZE	IC NO.
N/A	LTC362IEDCB-2
SCALE = NONE	
FILENAME: DC1864A-2.PCB	

LINEAR
TECHNOLOGY

1630 MCCARTHY BLVD
MILFORD, MA 01801
(978) 675-1000
WWW.LINEAR.COM
LTC CONFIDENTIAL -
FOR CUSTOMER USE ONLY

TITLE: FABRICATION DRAWING
17V VIN SYNCHRONOUS STEP-DOWN REGULATOR
WITH ULTRA-LOW QUIESCENT CURRENT

SIZE: N/A
IC NO.: LTC362IEDCB-2
REV: 2

FILENAME: DC1864A-2.PCB
SHT 1 OF 1