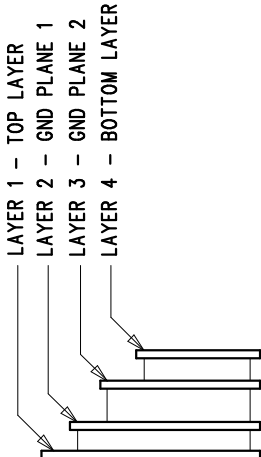


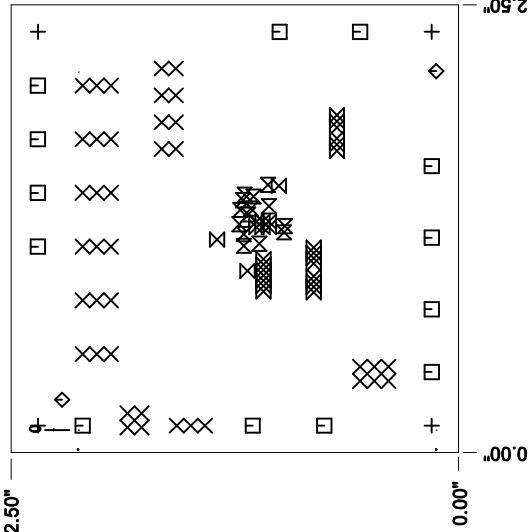
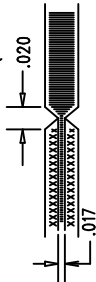
REVISION HISTORY			
ECO	REV	DESCRIPTION	APP. ENG.
-	2	PRODUCTION	TOM G.
			05-17-12

LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
4. DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003" IN RELATION TO CENTER
5. FINISH: -SNOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBs ARE TO BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



SIZE	QTY	SYM	PLATED	TOL
0.187	4	+	NO	+/-0.003
0.035	39	X	YES	+/-0.003
0.095	13	□	YES	+/-0.003
0.07	2	◇	NO	+/-0.003
0.01	30	⊗	YES	+/-0.003
0.008	14	⊗	YES	+/-0.003

UNLESS OTHERWISE SPECIFIED		APPROVALS	
DIMENSIONS ARE IN INCHES		PCB DES.	JW
TOLERANCES:		APP ENG.	TOM G.
0.XXX" = ±0.01"			
0.XXX" = ±0.005"			
INTERPRET DIM AND TOL PER ASME Y14.5M-1994			
THIRD ANGLE PROJECTION			
TITLE: FABRICATION DRAWING		SCALE = NONE	
2.5A SYNCHRONOUS BUCK REGULATOR			
WITH MONITORING			
SIZE	IC NO.	LTC3626EUDC	REV
N/A		DEMO CIRCUIT 1768A	2
FILENAME: DC1768A-2.PCB		SHT 1 OF 1	

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