

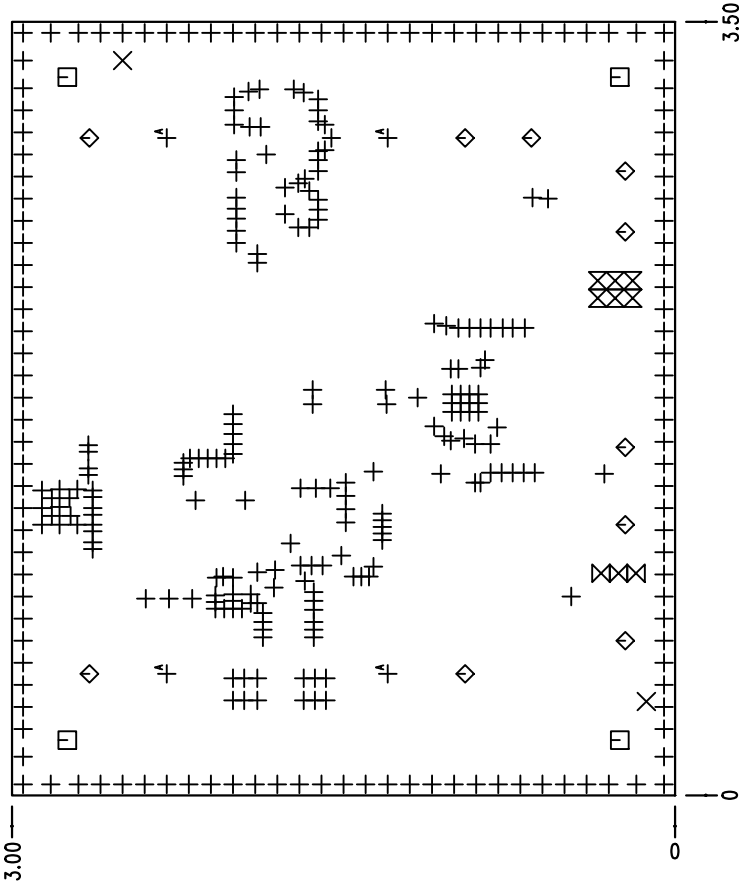
REVISION HISTORY			
ECO	REV	DESCRIPTION	APP. ENG.
-	2	PRODUCTION	Ding L.
			5/6/14

LAYER STRUCTURE

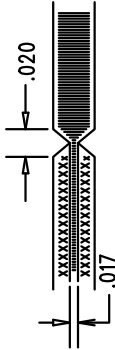
- LAYER 1 (TOP SIDE)
- LAYER 2 (GND PLANE)
- LAYER 3 (TRACE + GND PLANE)
- LAYER 4 (BOTTOM SIDE)

NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062+/- .005
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
- PCBS ARE TO BE RGHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



SIZE	QTY	SYM	PLATED	TOL
0.01	313	+	YES	+/-0.003
0.07	2	X	NO	+/-0.003
0.188	4	□	YES	+/-0.003
0.094	10	◇	YES	+/-0.003
0.031	3	⊗	YES	+/-0.003
0.035	6	⊗	YES	+/-0.003
0.215	4	⊕	YES	+/-0.003



UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES:
0.XX = ± 0.01
0.XXX = ± 0.005
INTERPRET DIM AND TOL
PER ASME Y14.5M-1994
THIRD ANGLE PROJECTION

APPROVALS

PCB DES. HZ
APP ENG. Ding L.

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LTC CORPORATION
FOR CUSTOMER USE ONLY

**LINEAR
TECHNOLOGY**

TITLE: FABRICATION DRAWING
LOW QUIESCENT CURRENT HIGH VOLTAGE
STEP-DOWN CONVERTER

SIZE IC NO. LTC3807EJ
N/A DEMO CIRCUIT 2221A

REV 2

SCALE = NONE

FILENAME: DC2221A2.PCB

SHT 1 OF 1