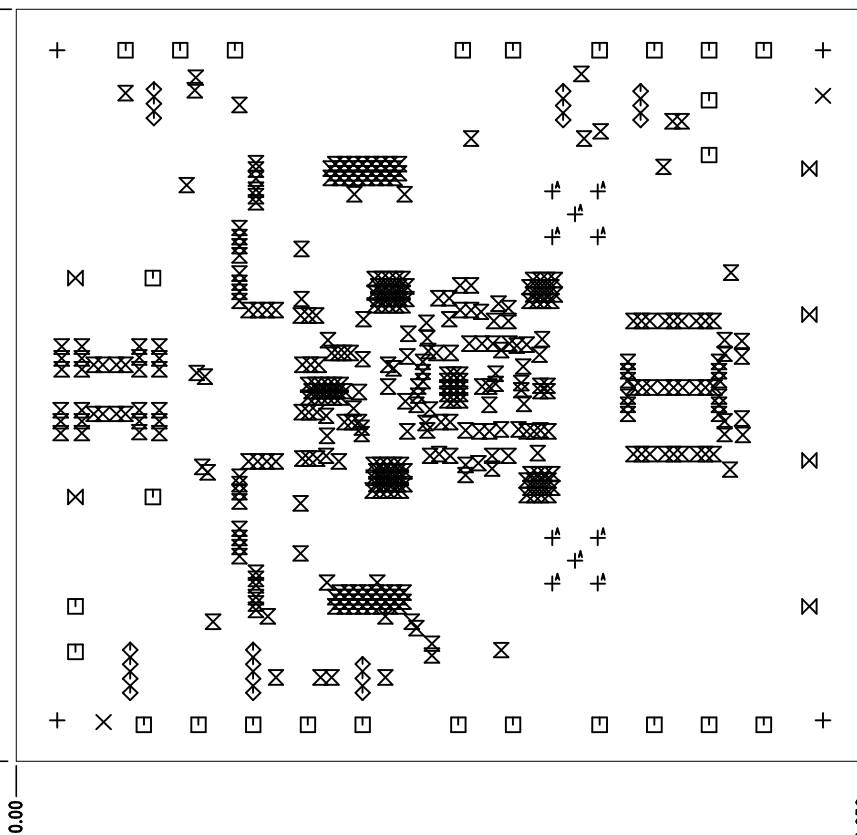


4.125

0.00

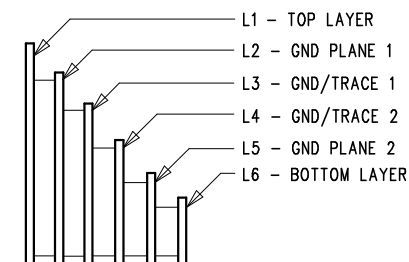


SIZE	QTY	SYM	PLATED	TOL
0.01	438	⊗	YES	+/-0.003
0.035	20	◇	YES	+/-0.003
0.055	10	+ ^A	YES	+/-0.003
0.07	2	×	NO	+/-0.003
0.095	26	□	YES	+/-0.003
0.187	4	+	YES	+/-0.003
0.19	6	⊗	YES	+/-0.003

REVISION HISTORY

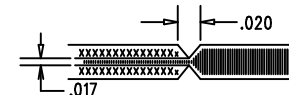
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	2	PRODUCTION	MIKE S.	08-29-12

LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
-FINISHED THICKNESS TO BE 0.062 +/- .005
-TOTAL OF 6 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003 IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
-FOR SILKSCREENS: USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE. PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



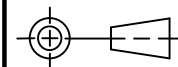
UNLESS OTHERWISE SPECIFIED

DIMENSIONS ARE IN INCHES

TOLERANCES:

0.XX = ± 0.01

0.XXX = ± 0.005

INTERPRET DIM AND TOL
PER ASME Y14.5M-1994
THIRD ANGLE PROJECTION

APPROVALS

PCB DES.	LT
APP ENG.	MIKE S.
SCALE = NONE	

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TITLE: FABRICATION DRAWING

FAST TRANSIENT DUAL OUTPUT
STEP-DOWN DC-DC CONVERTER

SIZE	IC NO.	LTC3838EUHF-1/-2	REV
N/A		DEMO CIRCUIT 1997A	2

FILENAME: 1997A-2.PCB

SHT 1 OF 1