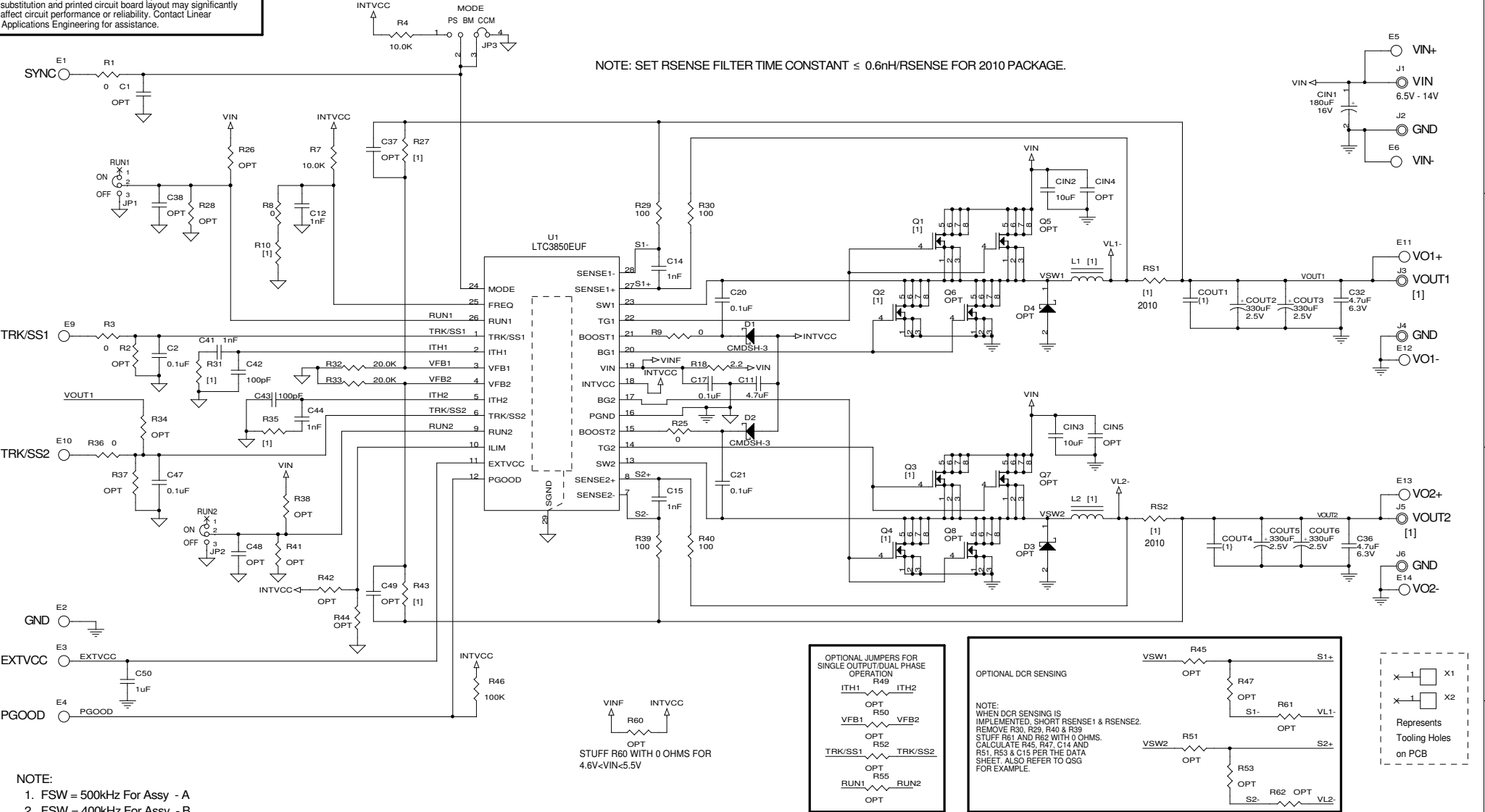


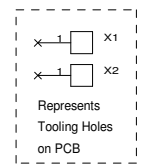
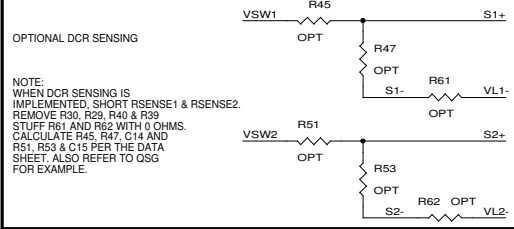
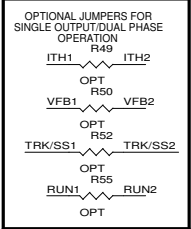
This circuit is proprietary to Linear Technology and supplied for use with Linear Technology parts.  
**Customer Notice:** Linear Technology has made a best effort to design a circuit that meets customer-supplied specifications; however, it remains the customer's responsibility to verify proper and reliable operation in the actual application. Component substitution and printed circuit board layout may significantly affect circuit performance or reliability. Contact Linear Applications Engineering for assistance.

REVISION HISTORY				
ECO	REV	DESCRIPTION	DATE	APPROVED
	1	PROD	12/12/07	



NOTE:  
1. FSW = 500kHz For Assy - A  
2. FSW = 400kHz For Assy - B  
3. DO NOT APPLY LOAD FROM THE VO1+ TO VO1- OR FROM THE VO2+ TO VO2- TURRETS. THESE ARE ONLY INTENDED TO KELVIN SENSE THE OUTPUT VOLTAGE ACROSS COUT1 AND COUT4.

[1]	ASSY	VOUT1	VOUT2	RS1, RS2	L1, 2	Q1, 3	Q2, 4	R27	R43	R31, R35	R10	COUT1/COUT4
	- A	2.0V/10A	1.8V/10A	0.003	TOKO FDU0650-R56M=P3	VISHAY Si4420BDY	VISHAY Si4630DY	30.1K	25.5K	8.66K	3.16K	47uF
	- B	1.5V/15A	1.2V/15A	0.002	VITEC 59PR9875 0.4uH	RENESAS RJK0305DPB	RENESAS RJK0301DPB	17.8K	10.0K	5.9K	2.55K	100uF



CONTRACT NO.		APPROVALS		DATE	
DRAWN [MEI]		CHECKED		10/31/06	
APPROVED		ENGINEER			
DESIGNER		TUESDAY, APRIL 29, 2008			
TITLE		SCH, LTC3850EUF, DUAL OUTPUT SYNCHRONOUS BUCK CONVERTER		REV 1	
SIZE		CAGE CODE		DWG NO	
SCALE: NONE		FILENAME: 1185B-1.DSN		SHEET 1 OF 1	