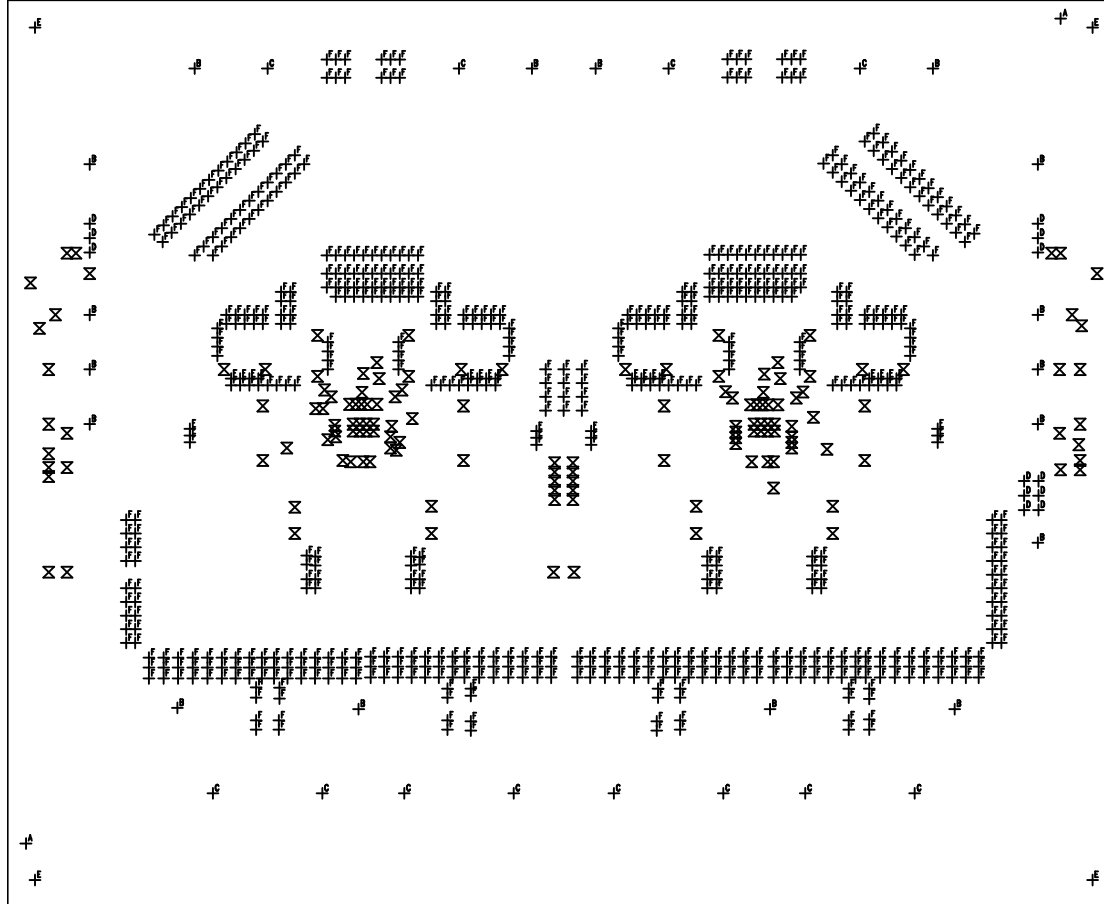


4.975" LINEAR TECHNOLOGY DC1510A - LTC3858EUH POLYPHASE HIGH VOUT SYNCHRONOUS BUCK CONVERTER DATE: 03-18-10



0.0

0.0

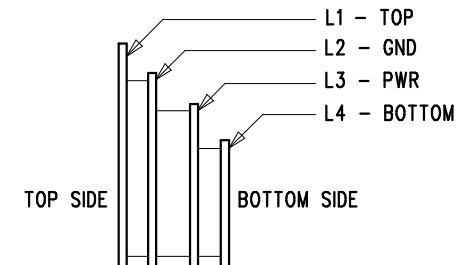
6.100"

SIZE	QTY	SYM	PLATED	TOL
0.008	145	⊗	YES	+/-0.003
0.07008	2	⊕ ^A	NO	+/-0.003
0.095	17	⊕ ^B	YES	+/-0.003
0.215	12	⊕ ^C	YES	+/-0.003
0.04	12	⊕ ^D	YES	+/-0.003
0.187	4	⊕ ^E	NO	+/-0.003
0.015	662	⊕ ^F	YES	+/-0.003

REVISION HISTORY

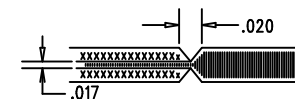
ECO	REV	DESCRIPTION	APP. ENG.	DATE
-	2	PRODUCTION	RICHARD Y.	03-18-10

LAYER STRUCTURE

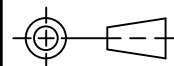


NOTES: UNLESS OTHERWISE SPECIFIED

- FAB PER IPC-A-600.
- MATERIAL: -EPOXY FIBERGLASS, NEMA GRADE FR-4
-FINISHED THICKNESS TO BE 0.062" +/- .005"
-TOTAL OF 4 LAYERS WITH 2 OZ. CU ON THE OUTER LAYERS
AND 1 OZ. CU ON THE INNER LAYERS.
-FLAMMABILITY RATING: 94 V-0 MINIMUM.
- SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00" ARE PRIMARY DATUMS.
- DRILLING: -DRILL HOLES PER SCHEDULE. PLATE THROUGH
HOLES WITH COPPER, 0.001" THICK MIN.
-ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
-HOLE LOCATION TOLERANCES ARE +/-0.003"
IN RELATION TO CENTER
- FINISH: -SMOBC USING LPI BOTH SIDES, COLOR GREEN.
-GOLD IMMERSION BOTH SIDES.
(LEAD FREE SOLDER CAN BE USED FOR PROTOTYPE)
-FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
- DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.
PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
- PCBS ARE TO BE RoHS COMPLIANT.
- SCORING FOR PANELIZED PCB:



UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES:
0.XX" = ±0.01"
0.XXX" = ±0.005"
INTERPRET DIM AND TOL
PER ASME Y14.5M-1994
THIRD ANGLE PROJECTION



APPROVALS

PCB DES. AK
APP ENG. RICHARD Y.

SCALE = NONE



1630 MCCARTHY BLVD
MILPITAS, CA 95035
PH: (408)432-1900
www.linear.com
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TITLE: FABRICATION DRAWING
POLYPHASE HIGH VOUT
SYNCHRONOUS BUCK CONVERTER

SIZE IC NO. LTC3858EUH
N/A DEMO CIRCUIT 1510A

REV 2

FILENAME: DC1510A-2.PCB

SHT 1 OF 1