

NOTES: UNLESS OTHERWISE SPECIFIED,

A cross-sectional diagram of a 4-layer printed circuit board (PCB). The layers are labeled from top to bottom: L1 - TOP, L2 - GND PLANE, L3 - GND PLANE/TRACE, and L4 - BOTTOM. The top surface is labeled 'TOP SIDE' and the bottom surface is labeled 'BOTTOM SIDE'. Arrows indicate the orientation of each layer.



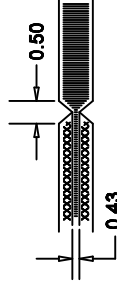
6. TOLERANCES:
A. WARPAGE
B. ETCHING
C. REGISTRATION
D. MASK THICKNESS
E. HOLE PLACEMENT



**A. CUT TO DIMENSIONS AND TOLERANCES SHOWN.
0.00 ARE PRIMARY DATUMS.**

9. DEBURR AND BREAK ALL SHARP EDGES.

10. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE.

11. SCORING: PANELIZED FAB ONLY.



UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN MILLIMETERS TOLERANCES ON ANGLE $\pm 1^\circ$ XX ± 0.25 XXX ± 0.127		CONTRACT NO.		<div></div> 1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 Fax: (408)434-0607					
		APPROVALS		TITLE					
THIRD ANGLE PROJECTION		DRAWN	RB/MI	DATE		FAB, LTC3864EMSE, 60V, LOW I _Q , STEP-DOWN CONTROLLER			
		CHECKED		10/31/11		SIZE		REV	
		DAVID B.		05/11/12		A		3	
		APPROVED				CAGE CODE		DC1722A	
DO NOT SCALE DRAWING		DESIGNED BY				DWG NO.			
						SCALE		1/1	
						FILENAME: 1722A-3.PCB		SHEET 1 OF 1	