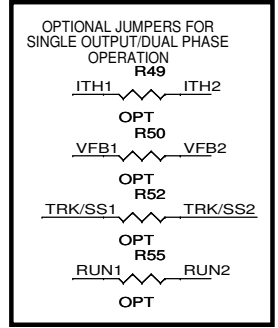
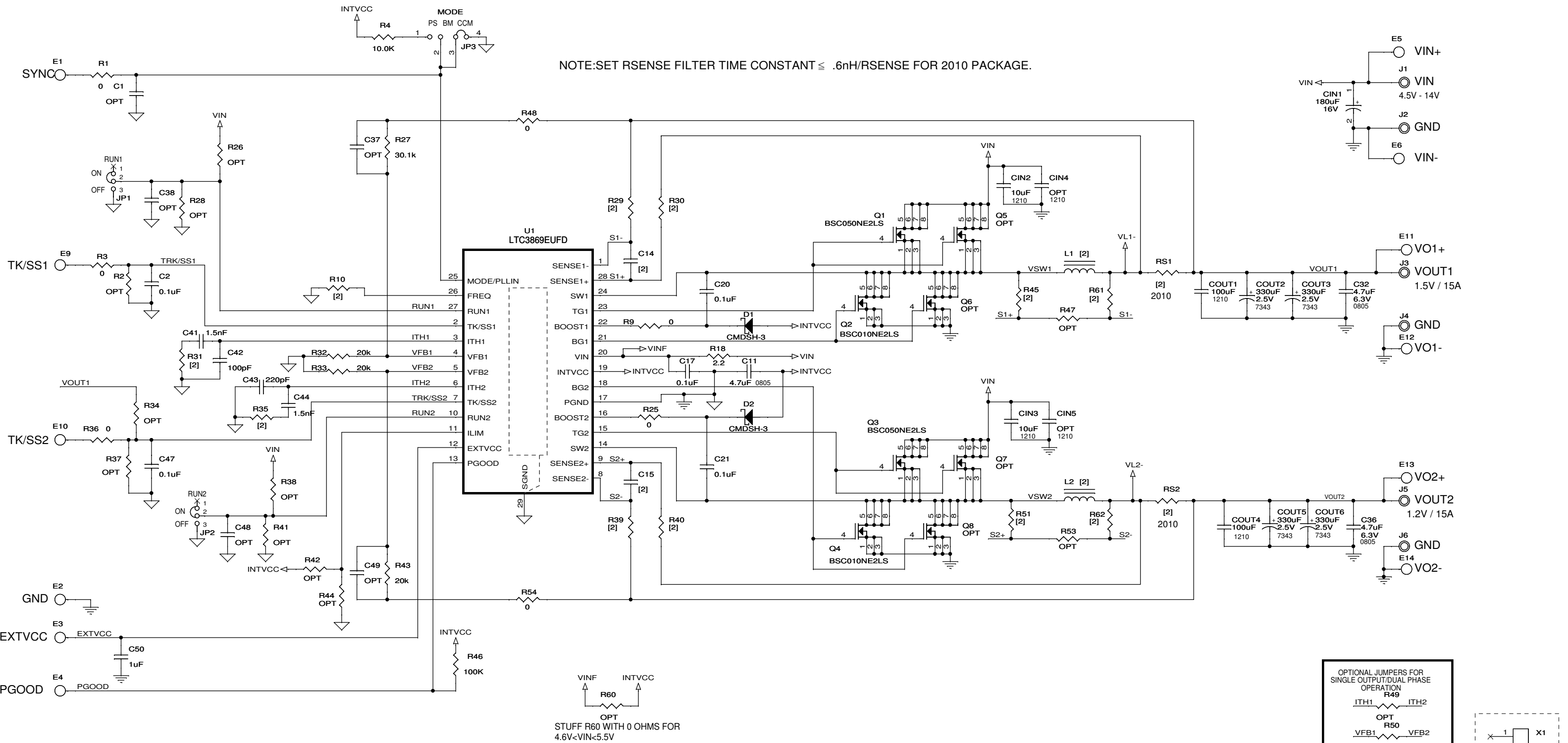


REVISION HISTORY				
ECO	REV	DESCRIPTION	APPROVED	DATE
	1	1ST PROTOTYPE	YI.S.	03/24/11



NOTES: UNLESS OTHERWISE SPECIFIED.

- DO NOT APPLY LOAD FROM THE VO1+ TO VO1- OR FROM THE VO2+ TO VO2- TURRETS. THESE ARE ONLY INTENDED TO KELVIN SENSE THE OUTPUT VOLTAGE ACROSS COUT1 AND COUT4.

[2]	ASSY	FSW	R10	L1, L2	RS1, RS2	R45, R51	R61, R62	R29, R30, R39, R40	C14, C15	C41, C44	R31, R35
- A	R SENSE	400kHz	102k	VITEC 59PR9875, 0.4uH	0.002	OPT	OPT	100	1nF	1nF	5.9k
- B	DCR SENSE	300kHz	75k	VISHAY IHLP4040DZ11, 0.56uH	0	1.58k	0	OPT	220nF	1.5nF	5.9k

3. ALL CAPACITORS AND RESISTORS ARE 0603.

CUSTOMER NOTICE		CONTRACT NO.	1630 McCarthy Blvd. Milpitas, CA 95035 Phone: (408)432-1900 Fax: (408)434-0507 LTC Confidential-For Customer Use Only	
LINEAR TECHNOLOGY HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT MEETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUSTOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR RELIABILITY. CONTACT LINEAR TECHNOLOGY APPLICATIONS ENGINEERING FOR ASSISTANCE.		APPROVALS		
THIS CIRCUIT IS PROPRIETARY TO LINEAR TECHNOLOGY AND SUPPLIED FOR USE WITH LINEAR TECHNOLOGY PARTS.		PCB DES. MI		
		ENG. YI.S.	TITLE: SCHEMATIC DUAL PHASE / DUAL OUTPUT SYNCHRONOUS BUCK CONVERTER	
		DATE: Friday, March 25, 2011	SIZE N/A	IC NO. LTC3869EUF DEMO CIRCUIT 1807A
				REV 1
				SHEET 1 OF 1