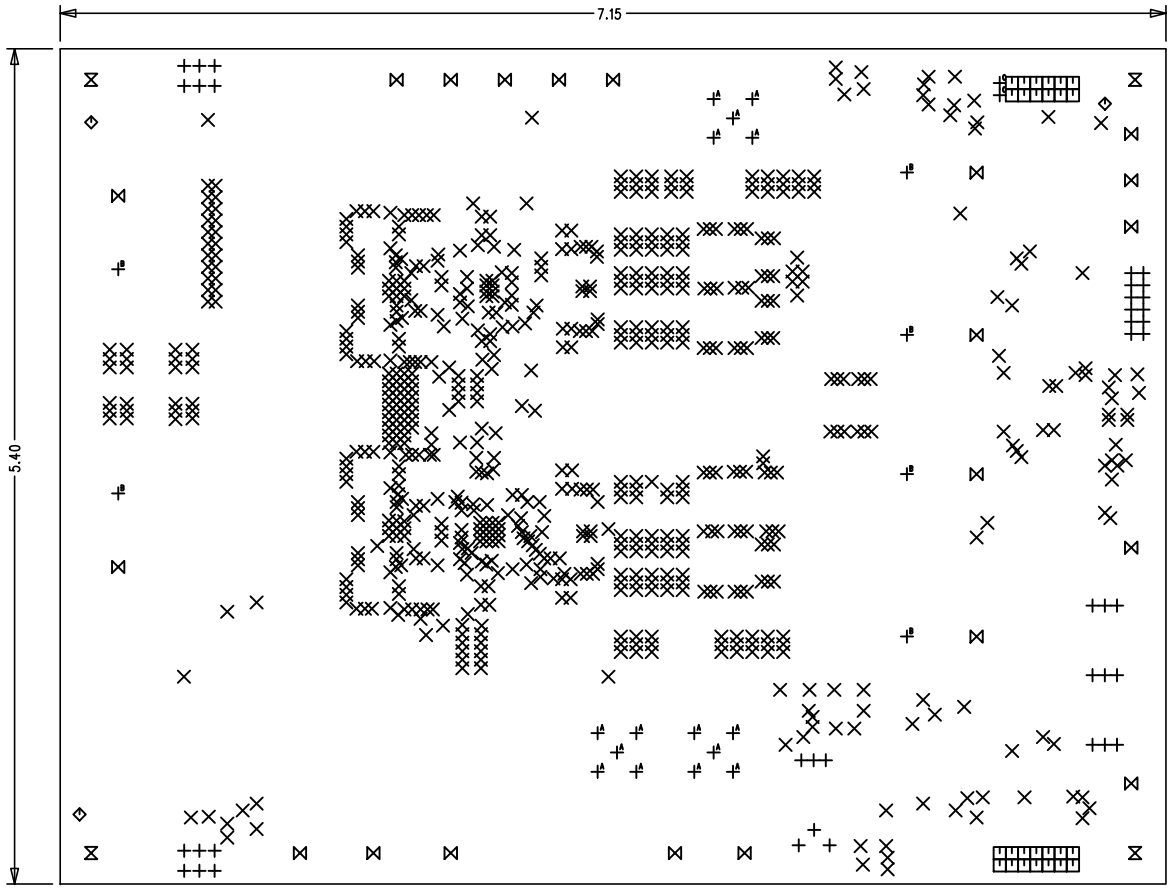
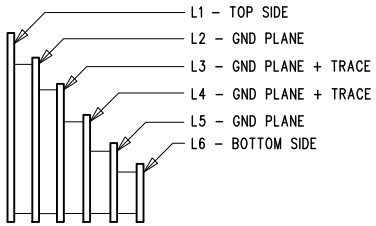
REVISION HISTORY				
ECO	REV	DESCRIPTION	APP. ENG.	DATE
–	2	PRODUCTION	YI S.	5-16-14

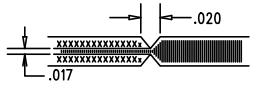


LAYER STRUCTURE



NOTES: UNLESS OTHERWISE SPECIFIED

1. FAB PER IPC-A-600.
2. MATERIAL: –LEAD FREE ASSEMBLY COMPLIANT, ISOLA FR-370HR OR EQUIVALENT.
   –FINISHED THICKNESS TO BE 0.062+/– .005
   –TOTAL OF 6 LAYERS WITH 2 OZ. CU ON THE LAYER 1, LAYER 2, LAYER 5 AND LAYER 6.
   1 OZ. CU ON THE LAYER 3 AND LAYER4
   –FLAMMABILITY RATING: 94 V-O MINIMUM.
3. SIZE: CUT TO DIMENSIONS AND TOLERANCES SHOWN. 0/0 ARE PRIMARY DATUMS.
4. DRILLING: –DRILL HOLES PER SCHEDULE. PLATE THROUGH HOLES WITH COPPER, 0.001 THICK MIN.
   –ALL HOLE SIZES ARE SPECIFIED AFTER PLATING.
   –HOLE LOCATION TOLERANCES ARE +/–0.003 IN RELATION TO CENTER
5. FINISH: –SMOBC USING LPI BOTH SIDES, COLOR GREEN.
   –GOLD IMMERSION BOTH SIDES.
   –FOR SILKSCREEN: BOTH SIDES USE WHITE NON-CONDUCTIVE INK.
6. DO NOT ALTER ARTWORK e.g. TO ADD LOGO OR DATE CODE. PAD SIZE CAN BE MODIFIED TO MEET END FINISH.
7. PCBS ARE TO BE RoHS COMPLIANT.
8. SCORING FOR PANELIZED PCB (PRODUCTION FAB ONLY):



SIZE	QTY	SYM	PLATED	TOL
0.01	744	X	YES	+/–0.003
0.0315	2	C	YES	+/–0.003
0.035	39	+	YES	+/–0.003
0.037	26	□	YES	+/–0.003
0.055	15	A	YES	+/–0.003
0.067	21	⋈	YES	+/–0.003
0.07	3	◇	NO	+/–0.003
0.187	4	⧗	YES	+/–0.003
0.189	6	B	YES	+/–0.003

UNLESS OTHERWISE SPECIFIED
DIMENSIONS ARE IN INCHES
TOLERANCES:
0.XX = ± 0.01
0.XXX = ± 0.005
INTERPRET DIM AND TOL PER ASME Y14.5M-1994 THIRD ANGLE PROJECTION

APPROVALS	
PCB DES.	LT
APP ENG.	YI S.

SCALE = NONE

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TITLE: FABRICATION DRAWING
POLY-PHASE DC/DC CONVERTER
WITH POWER SYSTEM MANAGEMENT

SIZE	IC NO.	REV
N/A	LTC3880EUJ + LTC3870IUFD DC2089A	2

FILENAME: DC2089A-2.PCB
SHT 1 OF 1